

Unit 5

Push Down Automata

PDA = FA + Stack

Formal definition :-

It has 7 tuples namely

z_0 = starting symbol of stack

Γ = set of stack alphabets

q_0 F Σ Q δ

$\delta: Q \times (\Sigma \cup \Lambda) \times \Gamma \rightarrow Q \times \Gamma^*$

Case 1 -

Γ and Γ^* are same then skip operation has been done.

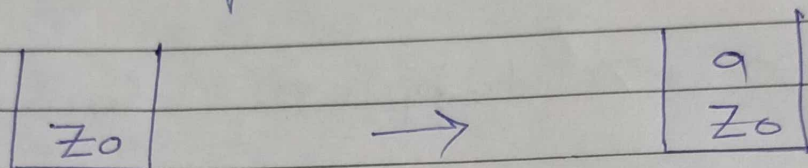
Case 2 -

When Γ and Γ^* are different then push operation been done.

(iii) Case 3 -

When \sim is any stack alphabet
and \sim^* is null then
pop - operation has been done.

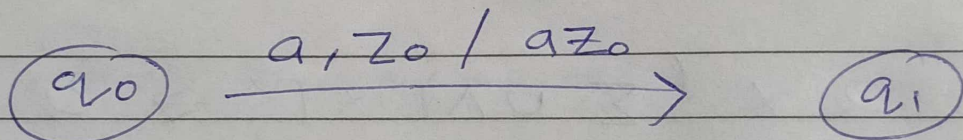
Push operation



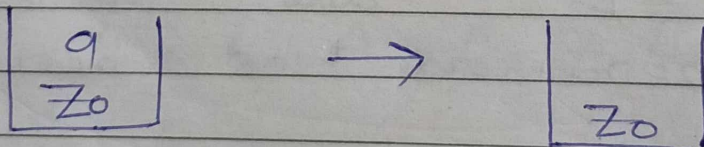
q_0

q_1

$$(q_0, a, Z_0) \rightarrow (q_1, aZ_0)$$



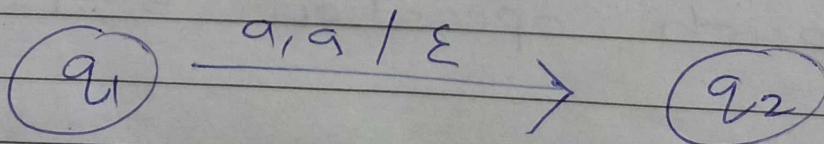
Pop Operation



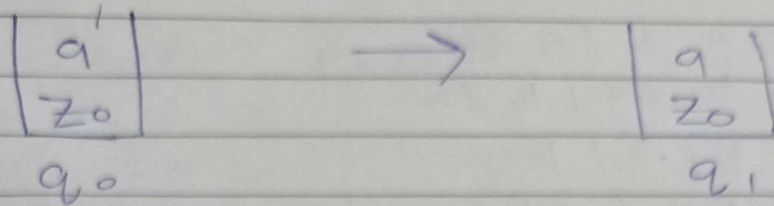
q_1

q_2

$$(q_1, a, a) \rightarrow (q_2, \epsilon)$$

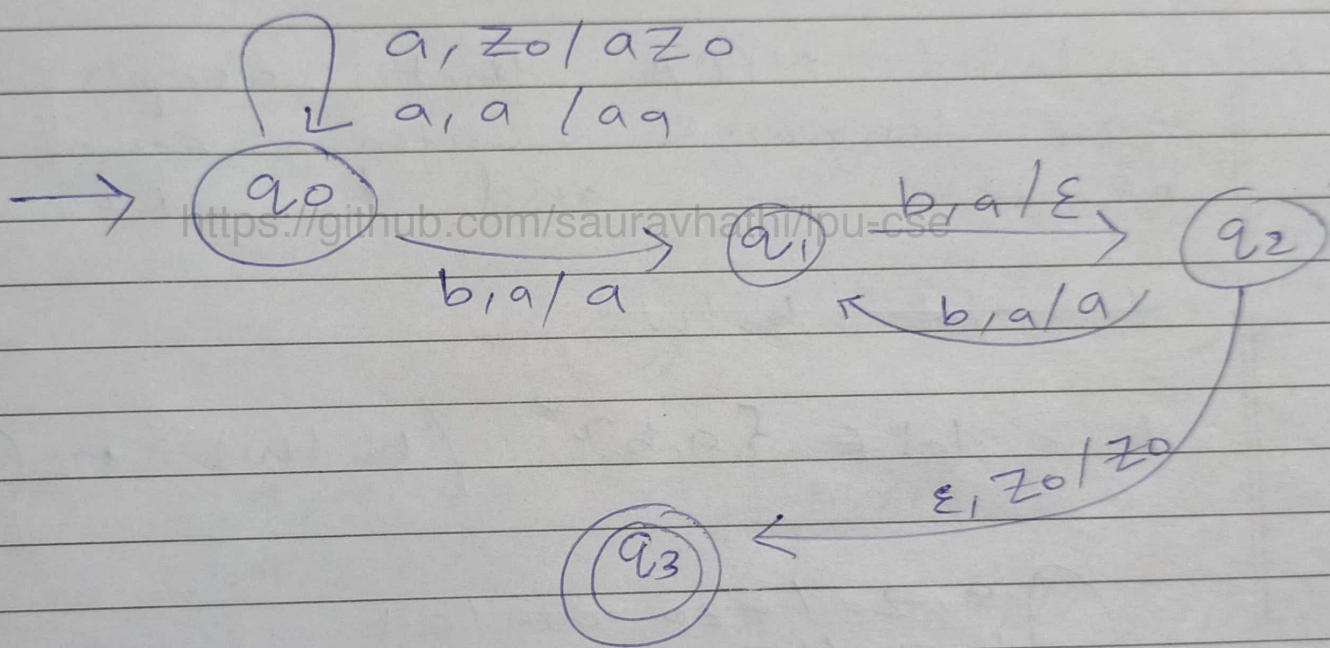


Skip Operation

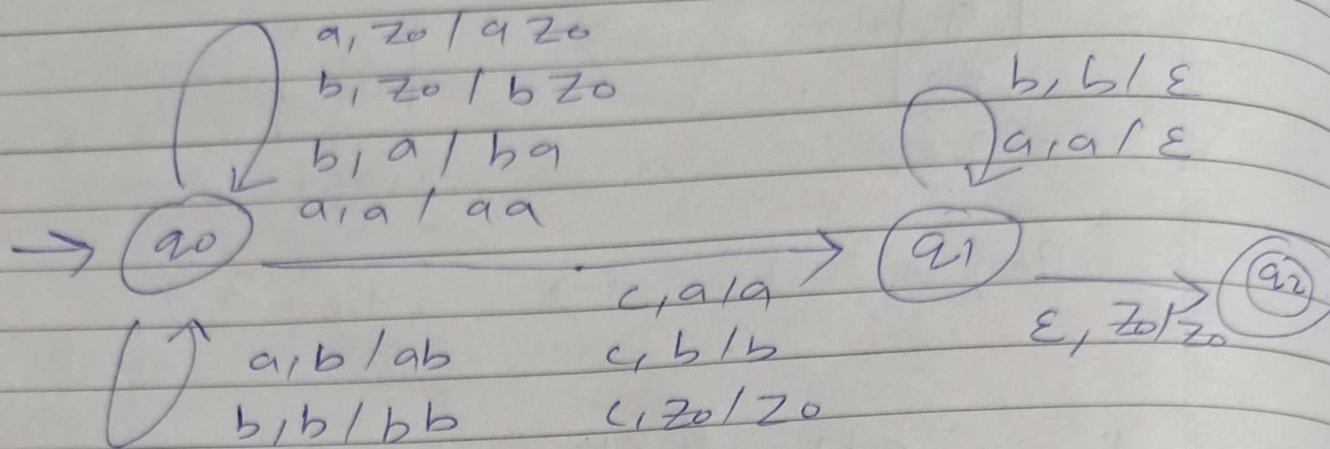


$(q_0, a, a) \rightarrow (q_1, a)$

Q2) $L = a^n \cdot b^{2n} ; n \geq 1$



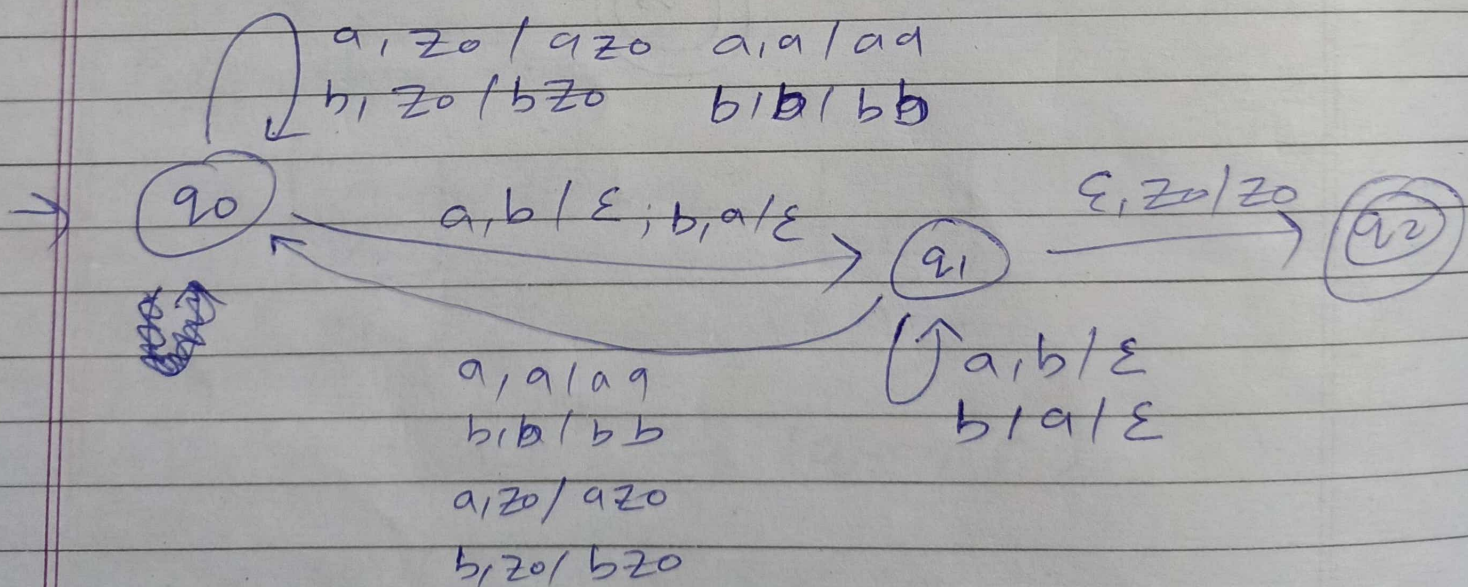
10) $L = \{w \in \{a, b\}^* \mid n_a(w) = n_b(w)\}$



10) Construct NPDA that accepts the language with equal no of a 's and b 's i.e.

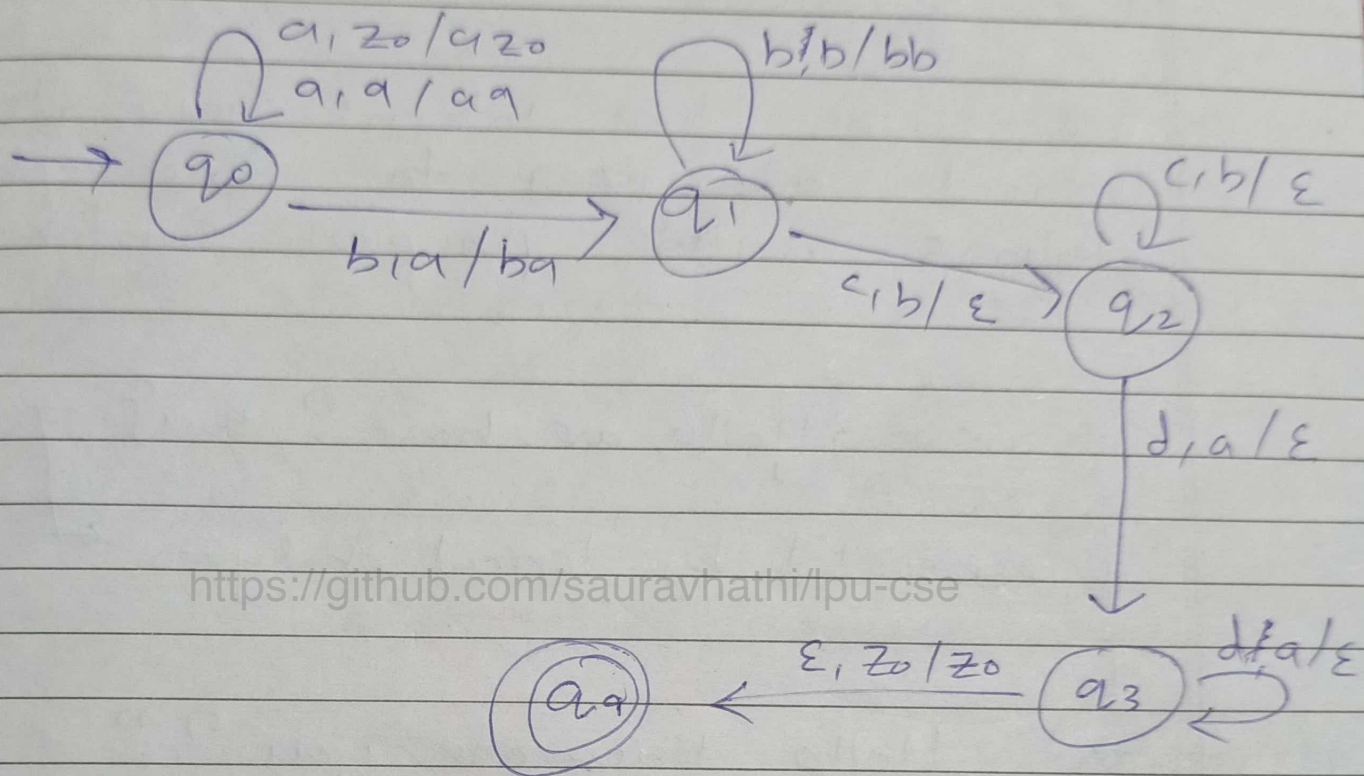
$$L = \{w \in \{a, b\}^* \mid n_a(w) = n_b(w)\}$$

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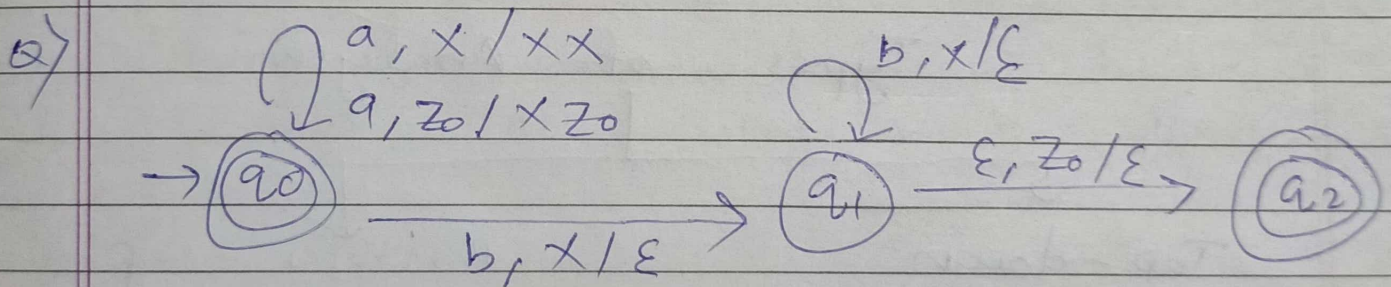


Q) $L = a^n b^m c^m d^n$; $n, m \geq 1$

Construct PDA



<https://github.com/sauravhathi/lpu-cse>



$\Sigma = \{a, b\}$ $\Gamma = \{x, z_0\}$

(A) $L = \{a^n ; n \geq 0\}$

✓ (B) $L = \{a^n\} \cup \{a^n b^n ; n \geq 0\}$

✓ (C) L is accepted by any Turing machine.

Parser

- It is syntax analyzer which is used to generate the string from the grammar.
- It uses the tokens generated by lexical analyzer to form the strings with proper syntax

Tokens = {Hello, are, how, ?, you}

Generated by lexical analyzer

→ "Hello how are you ?"

Types of Analyser

Top-down
Analyzer

Bottom-up
analyzer

recursively
Decent
parser

LL(K) →
{Predictive}
Left to right LMP Look after symbol

LR(0)
↓
SLR(0)

LR(1)
↓
LALR(1)

CLR(1)

- Recursive decent parser backtracking could be there
- $LL(k) \rightarrow$ No Backtracking
- $SLR \rightarrow$ Simple LR
- $LALR \rightarrow$ look at LR
- $LR \rightarrow$ Left to Right RMD

$LL(k)$ Parser

<https://github.com/sauravhathi/lpu-cse>

- ~~Ed~~ It uses top-down approach
- A grammar can be in $LL(2)$ ~~parser~~ however not in $LL(1)$, therefore if a grammar is in $LL(k+1)$ it may or may not be in $LL(k)$

ex \Rightarrow ^{check} show the following grammar is in $LL(1)$ or not by considering the string = aaabbd

$S \rightarrow aA/bB$
 $A \rightarrow aB/cB$
 $B \rightarrow bC/aC$
 $C \rightarrow bD$

$D \rightarrow d$

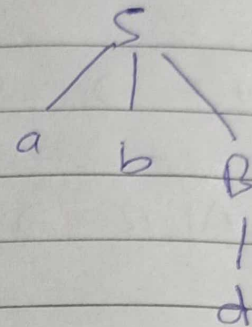
✓

* Check whether the grammar is in $LL(1)$, $LL(2)$, $LL(3)$ with string = abd

$S \rightarrow abB / aaA$

$A \rightarrow c/d$

$B \rightarrow d$



$LL(2)$ and $LL(3)$

Q) $LL(1)$ grammar is which type of grammar

(i) ~~$LL(1)$~~ type 0 (ii) ~~$LL(2)$~~ type 2 ✓

(iii) type 3 (iv) type 1

Q) $LL(1)$ grammar is

(i) always ambiguous

✓(ii) always unambiguous

✓(iii) Need to be converted to unambiguous

(iv) Not a

Handle is combination of terminals and non terminals.

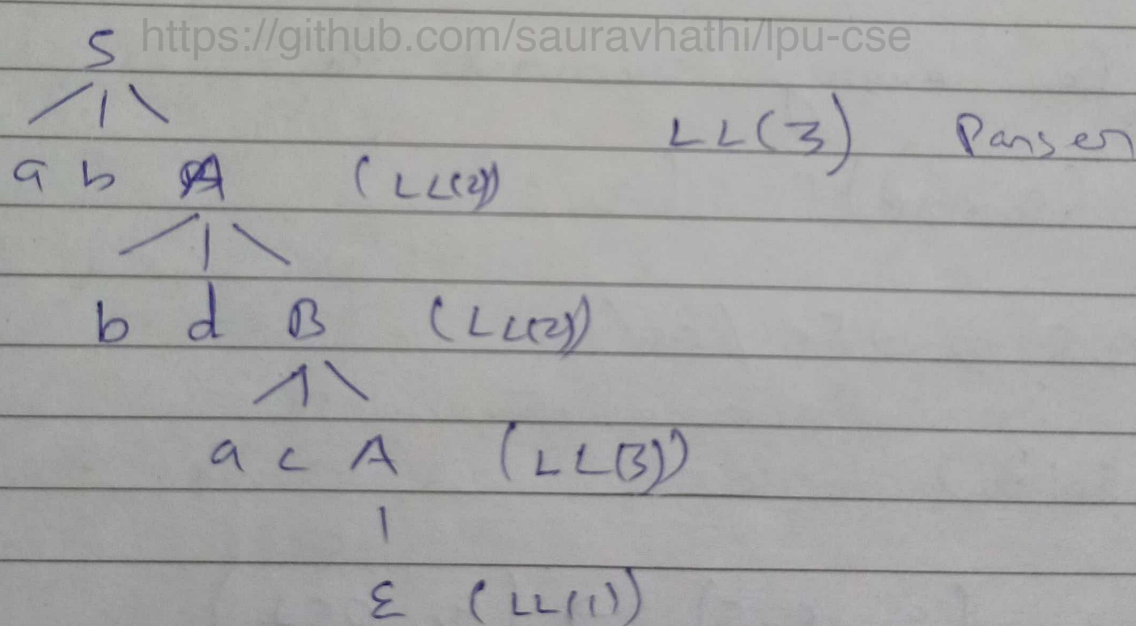
Yield is output string in terms of terminals

* String = aabdac

$S \rightarrow aaA / abB / \epsilon$

$A \rightarrow bdB / bcA / \epsilon$

$B \rightarrow acA / acB$



Q) Simplify the grammar

$$S \rightarrow axb$$

$$x \rightarrow axb / \epsilon$$

(i) $S \rightarrow ab$; ~~$x \rightarrow axb$~~ ; $x \rightarrow ab$

✓ (ii) $S \rightarrow axb / ab$; $x \rightarrow axb / ab$

(iii) $S \rightarrow ab$; $x \rightarrow axb / ab$

Q) CF Languages are applied in

✓ (i) Parser Design

<https://github.com/sauravhathi/lpu-cse>

(ii) DFA

(iii) NFA

Q) A derivation $A \rightarrow x$ is a
if we apply to
Right most variable on
every step.

(i) LMD

✓ (ii) RMD ✓

Q) A Grammar in CNF has the following property of derivation tree, i.e. every node has atleast 2 descendants

(i) Either single instead vertex on single leaf

(ii) Either 2 internal vertices on 2 leaves.

✓(iii) Either 2 internal vertices on single leaf

Q) In which normal form of CFG left recursion is not possible

⇒ GNF

Q) Top down parsing is equivalent to finding a

<https://github.com/sauravhathi/lpu-cse>

(i) RMD

✓(ii) LMD

(iii) Both (i) and (ii)

Q) The use of variable dependency graph is in

✓(i) Removal of useless variable

(ii) Removal of null production

(iii) Removal of unit production