

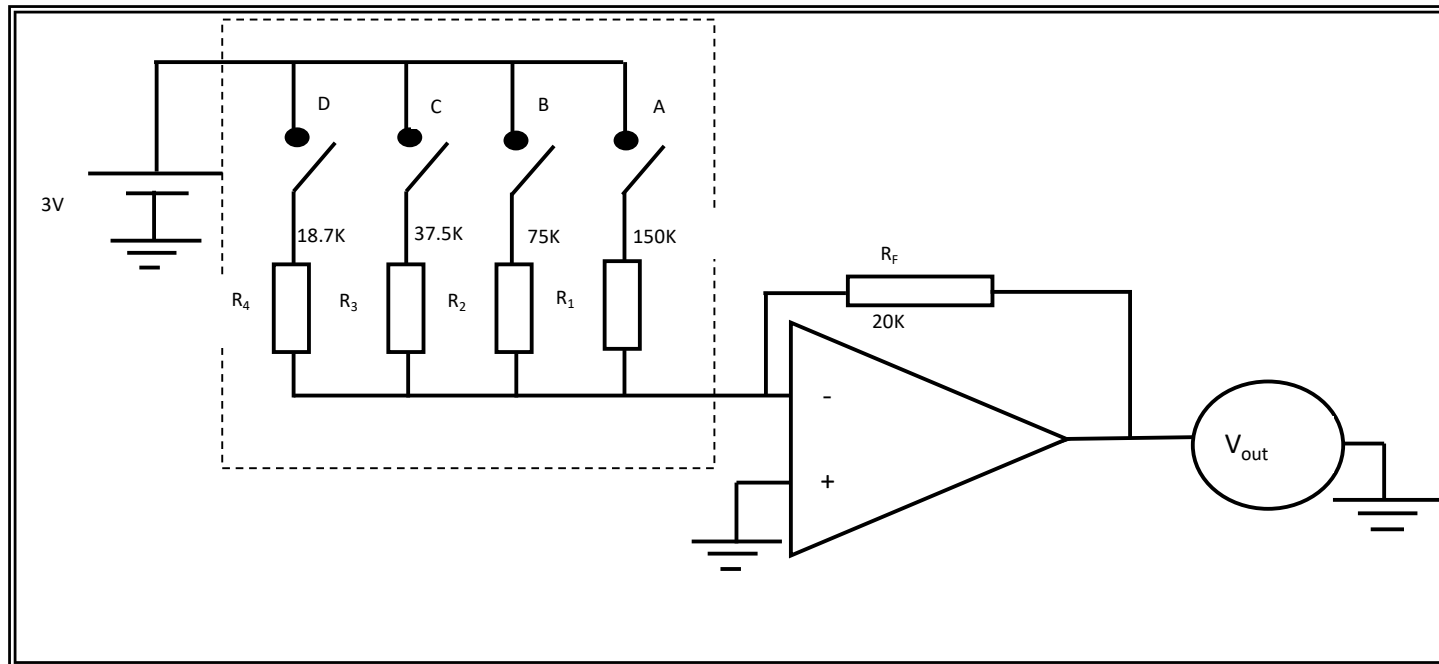
UNIT VI



Analog to digital converter
And
Digital to analog converter

Weighted Binary Resistance Network

Weighted Binary Resistance Network Circuit



$$\text{Voltage Gain } (A_v) = \frac{R_F}{R_1}$$

$$\text{Voltage Output } (V_{out}) = V_{ref} \times A_v \quad \text{or} \quad V_{ref} \times R_F \times R_{in}$$

$$R_{in} = \frac{1}{R_4} + \frac{1}{R_3} + \frac{1}{R_2} + \frac{1}{R_1}$$

Continue

- ❖ For example
- ❖ Referring to the circuit as shown, the highest value resistor ($150\text{K}\Omega$) is a digital input resistor. The smallest bit (least significant bit), and the values of other resistor is

$$R_1 = 150\text{K}$$

$$R_2 = \frac{R_1}{2^1} = \frac{150\text{K}}{2^1} = 75\text{k}\Omega, \Rightarrow \text{Bit ke } 2^1$$

$$R_3 = \frac{R_1}{2^2} = \frac{150\text{K}}{2^2} = \frac{150\text{K}}{4} = 37.5\text{k}\Omega, \Rightarrow \text{Bit ke } 2^2$$

$$R_4 = \frac{R_1}{2^3} = \frac{150\text{K}}{8} = 18.75\text{k}\Omega \Rightarrow \text{Bit ke } 2^3$$

Circuit analysis to find V_{out}

If binary input is 0001

$$R_1 = 150\text{K}\Omega, R_F = 20\text{K}\Omega, V_{ref} = 3\text{V}$$

$$\text{Voltage Gain } (A_V) = \frac{R_F}{R_1} = \frac{20\text{K}\Omega}{150\text{K}\Omega} = 0.133$$

$$\begin{aligned} V_{out} &= V_{ref} \times A_V \\ &= 3\text{V} \times 0.1333 \\ &= \underline{0.4\text{V}} \end{aligned}$$

Continue

❖ If binary input is 0110

$$R_2 = 75\text{K}\Omega, \quad R_3 = 37.5\text{K}\Omega, \quad R_F = 20\text{K}\Omega, \quad V_{\text{ref}} = 3\text{V}$$

$$R_T = R_2 // R_3 = 25\text{K}\Omega$$

$$\text{Voltage Gain } (A_V) = \frac{R_F}{R_T} = \frac{20\text{K}\Omega}{25\text{K}\Omega} = 0.8$$

$$\begin{aligned} V_{\text{out}} &= V_{\text{ref}} \times A_V \\ &= 3\text{V} \times 0.8 \\ &= \underline{2.4\text{V}} \end{aligned}$$

Calculate

❖ If binary input is 1100

$$R_3 = 37.5\text{K}\Omega, R_4 = 18.75\text{K}\Omega, R_F = 20\text{K}\Omega, V_{\text{ref}} = 3\text{V}$$

$$R_T = R_3 // R_4 = 12.5\text{K}\Omega$$

$$\text{Voltage Gain } (A_V) = \frac{R_F}{R_T} = \frac{20\text{K}\Omega}{12.5\text{K}\Omega} = 1.6$$

$$\begin{aligned} V_{\text{out}} &= V_{\text{ref}} \times A_V \\ &= 3\text{V} \times 1.6 \\ &= \underline{4.8} \end{aligned}$$

Simply that we can see the resulting output is shown in the table below

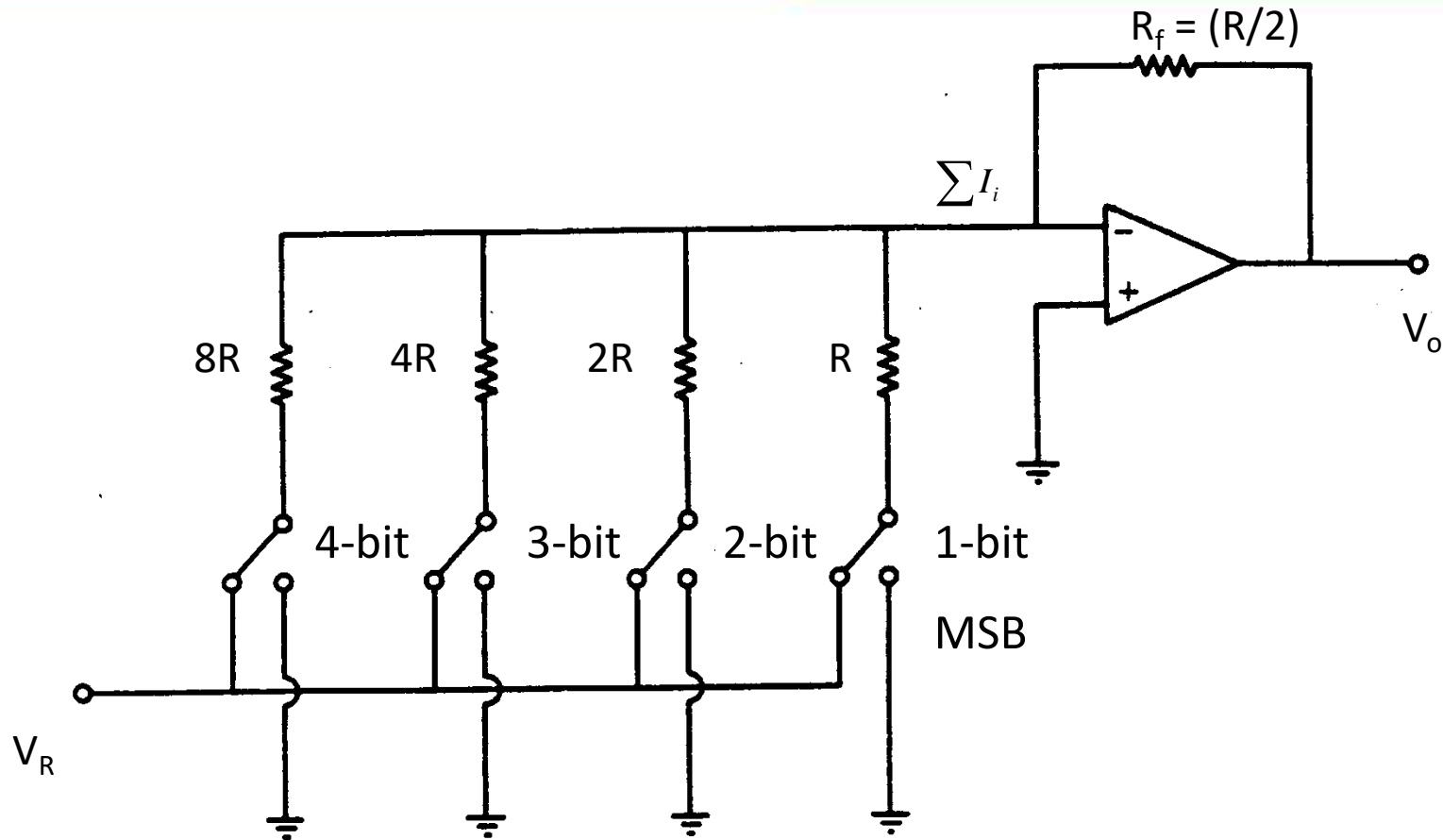
Decimal	Digital input				$V_{out} (V)$
	D	C	B	A	
0	0	0	0	0	0
1	0	0	0	1	0.4
2	0	0	1	0	0.8
3	0	0	1	1	1.2
4	0	1	0	0	1.6
5	0	1	0	1	2.0
6	0	1	1	0	2.4
7	0	1	1	1	2.8
8	1	0	0	0	3.2
9	1	0	0	1	3.6
10	1	0	1	0	4.0
11	1	0	1	1	4.4
12	1	1	0	0	4.8
13	1	1	0	1	5.2
14	1	1	1	0	5.6
15	1	1	1	1	6.0

Example

Find output voltage and current for a binary weighted resistor DAC of 4 bits where :

$R = 10 \text{ k Ohms}$, $R_f = 5 \text{ k Ohms}$ and $V_R = 10 \text{ Volts}$. Applied binary word is 1001.

Solution



Solution Cont'd

$$I_o = -\frac{10\text{ V}}{\Omega} \left[\frac{1}{2^0 * 10^4} + \frac{0}{2^1 * 10^4} + \frac{0}{2^2 * 10^4} + \frac{1}{2^3 * 10^4} \right]$$

$$I_o = -0.001125\text{ A}$$

$$V_o = -R_f I_o$$

$$V_o = -(5 * 10^3 \Omega)(-0.001125\text{ A}) = 5.625\text{ V}$$

Solution Cont'd

Binary input = 1001 = 9

From example, $V_0 = 5.625\text{V}$

$$V_0/V_R = 5.625\text{V}/10\text{V} = 9/16$$

Binary Weighted Resistor

❖ Advantages

- Simple Construction/Analysis
- Fast Conversion

❖ Disadvantages

- Requires large range of resistors (2000:1 for 12-bit DAC) with necessary high precision for low resistors
- Requires low switch resistances in transistors
- Can be expensive. Therefore, usually limited to 8-bit resolution.

Limitations of binary weighted

Has problems if bit length is longer than 8 bits

For example, if $R = 10 \text{ k Ohms}$

$$R_8 = 2^{8-1}(10 \text{ k Ohms}) = 1280 \text{ k Ohms}$$

If $V_R = 10 \text{ Volts}$,

$$I_8 = 10\text{V}/1280 \text{ k Ohms} = 7.8 \mu\text{A}$$

Op-amps to handle those currents are expensive because this is usually below the current noise threshold.

Limitations Cont'd

If $R = 10 \text{ Ohms}$ and $V_{\text{ref}} = 10 \text{ V}$

$$I = V_R/R = 10\text{V}/10 \text{ Ohms} = 1 \text{ A}$$

This current is more than a typical op-amp can handle.

Large resistors more error

Questions



Quick Quiz

Why the switches used in weighted resistor DAC are of single pole double throw (SPDT) type?

- a) To connect the resistance to reference voltage
- b) To connect the resistance to ground
- c) To connect the resistance to either reference voltage or ground
- d) To connect the resistance to output

Quick Quiz



What is the disadvantage of binary weighted type DAC?

- a) Require wide range of resistors
- b) High operating frequency
- c) High power consumption
- d) Slow switching

Quick Quiz

- The smallest resistor in a 12 bit weighted resistor DAC is $2.5\text{k}\Omega$, what will be the largest resistor value?
 - a) $40.96\text{M}\Omega$
 - b) $10.24\text{M}\Omega$
 - c) $61.44\text{ M}\Omega$
 - d) $18.43\text{M}\Omega$