

FLIP FLOP CONVERSIONS

- SR to D
- SR to JK
- SR to T
- JK to T
- JK to D
- JK to SR
- D to T
- D to SR
- T to D

PROCEDURE FOR CONVERSION

1. Draw the block diagram of the target flip flop from the given problem.
2. Write truth table for the target flip-flop.
3. Write excitation table for the available flip-flop.
4. Draw k-map for target flip-flop.
5. Draw the block diagram.

SR(Available) to D(Target) Flip flop Conversion

- Truth table

Input	Present state	Next state
D	Q_n	Q_{n+1}
0	0	0
0	1	0
1	0	1
1	1	1

- Excitation table

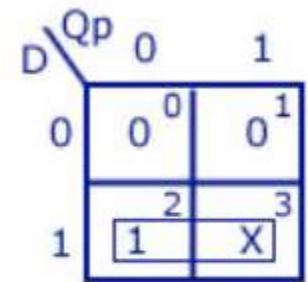
Present state	Next state	Flip flop Inputs	
Q_n	Q_{n+1}	S	R
0	0	0	X
0	0	0	1
0	1	1	0
1	1	X	0

SR to D Flip flop Conversion

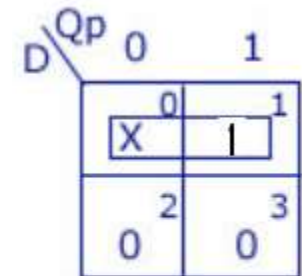
Conversion Table

Input	Present state	Next state	Flip flop Inputs	
D	Q_n	Q_{n+1}	S	R
0	0	0	0	X
0	1	0	0	1
1	0	1	1	0
1	1	1	X	0

K- MAP SIMPLIFICATION



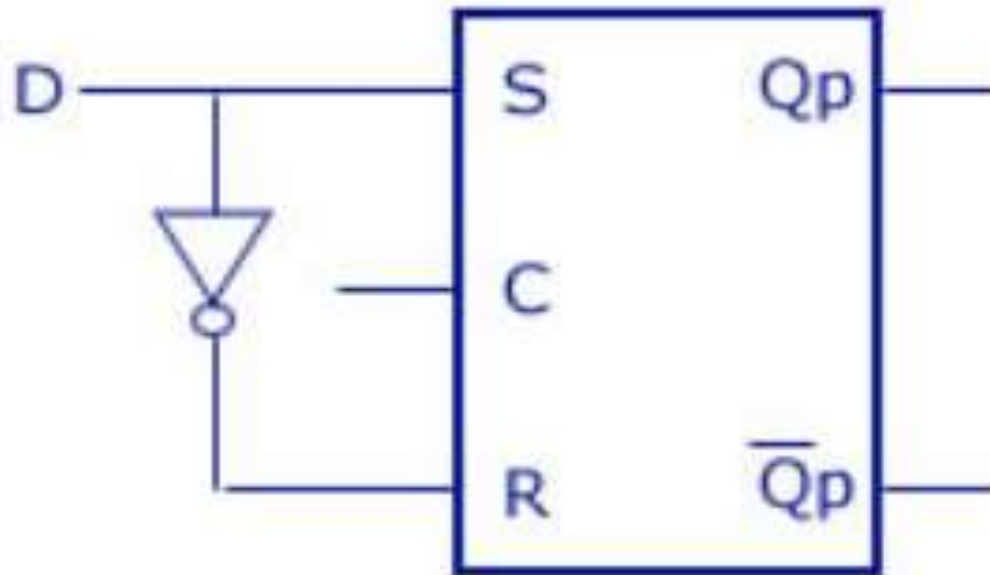
$$S = D$$



$$R = \overline{D}$$

SR to D

Logic Diagram



SR(Available) to JK(Target) Flip-Flop

Conversion Table

Input		Present State	Next State	Flip-Flop Inputs	
J	K	Q n	Qn+1	S	R
0	0	0	0	0	X
0	0	1	1	X	0
0	1	0	0	0	X
0	1	1	0	0	1
1	0	0	1	1	0
1	0	1	1	X	0
1	1	0	1	1	0
1	1	1	0	0	1

SR to JK

- K-map Simplification

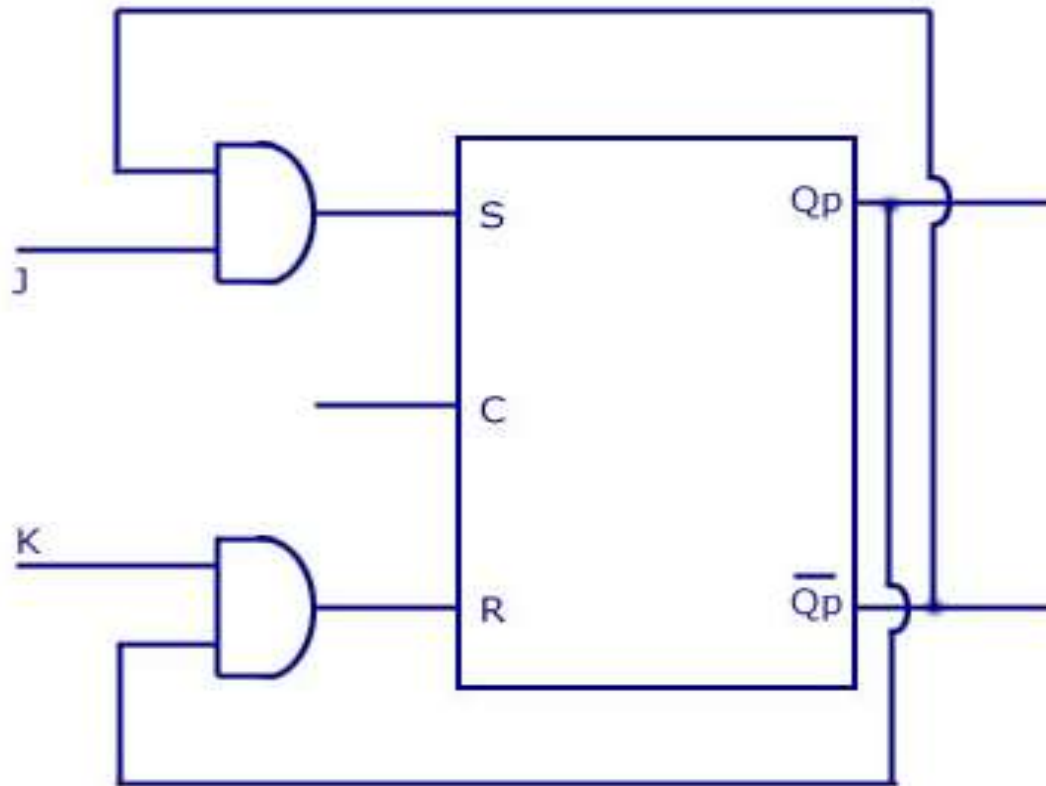
		KQ _p			
		00	01	11	10
J	0	0 ⁰ 0	1 ¹ X	3 ³ 0	2 ² 0
	1	4 ⁴ 1	5 ⁵ X	7 ⁷ 0	6 ⁶ 1

$$S = \overline{J}Q_p$$

		KQ _p			
		00	01	11	10
J	0	0 ⁰ X	1 ¹ 0	3 ³ 1	2 ² X
	1	4 ⁴ 0	5 ⁵ 0	7 ⁷ 1	6 ⁶ 0

$$R = KQ_p$$

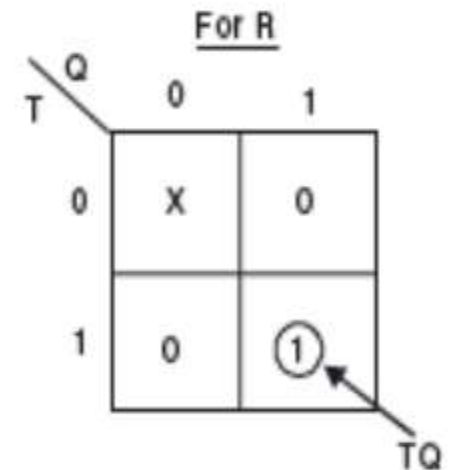
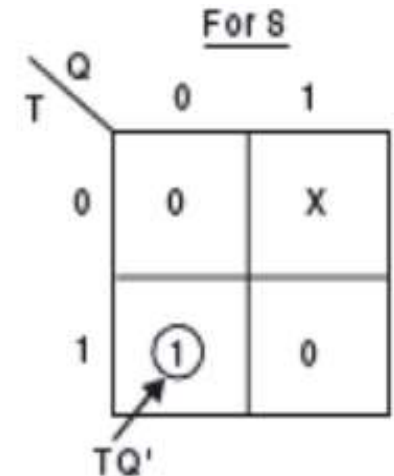
Logic Diagram (SR to JK)



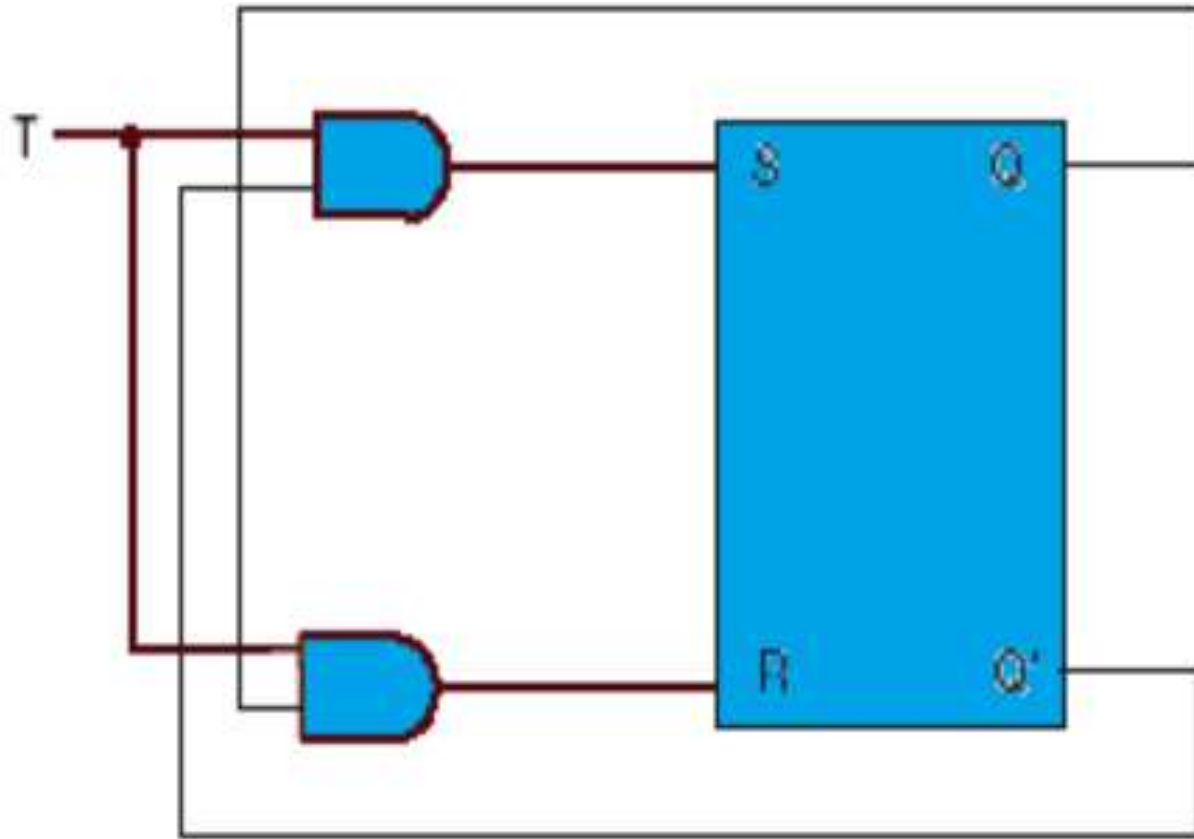
SR(Available) to T(Target) Conversion Table

Input	Present state	Next state	Flip flop Inputs	
T	Q_n	Q_{n+1}	S	R
0	0	0	0	X
0	1	1	X	0
1	0	1	1	0
1	1	0	0	1

K- MAP SIMPLIFICATION



Logic Diagram (SR to T)



A T flip-flop using S-R flip-flop.

JK(Available) to T (Target) Conversion Conversion Table

Input	Present state	Next state	Flip flop Inputs	
T	Q_n	Q_{n+1}	J	K
0	0	0	0	X
0	1	1	X	0
1	0	1	1	x
1	1	0	x	1

K- MAP SIMPLIFICATION

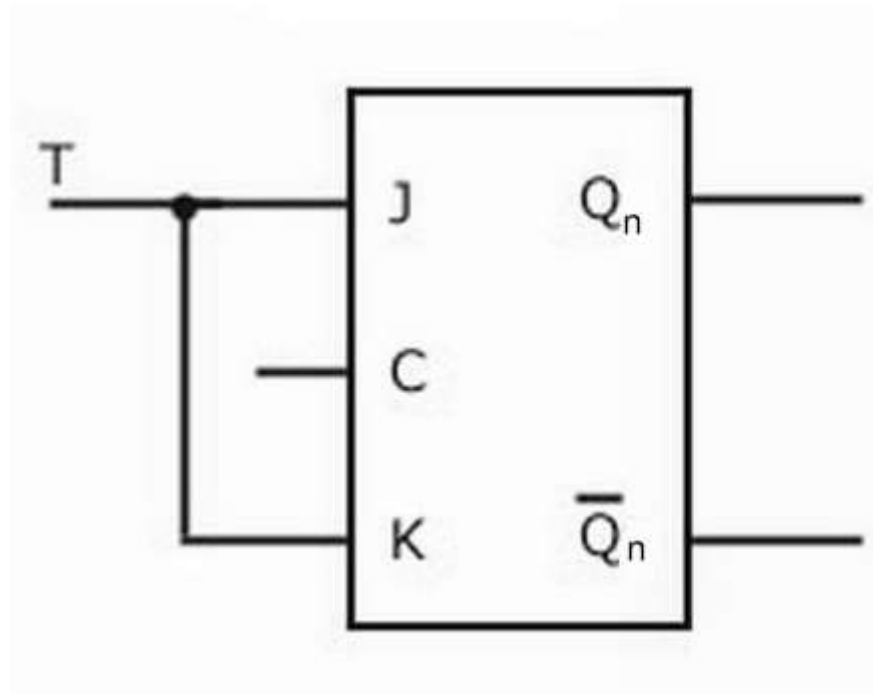
Q_n T	0	1
	0	1
0	0	X
1	1	X

$J=T$

Q_n T	0	1
	0	1
0	X	0
1	X	1

$K=T$

Logic Diagram (JK to T)

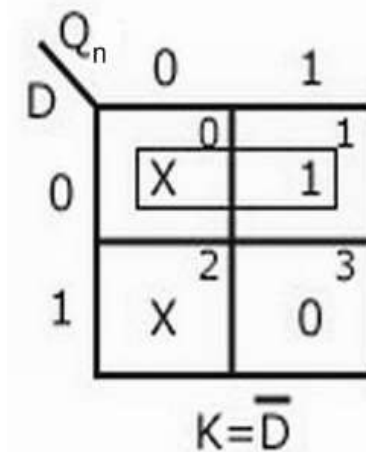
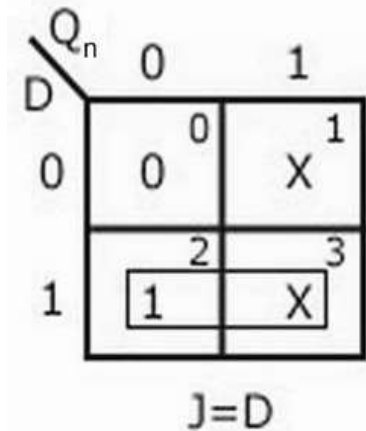


JK(Available) to D(Target) Flip-flop Conversion

Conversion Table

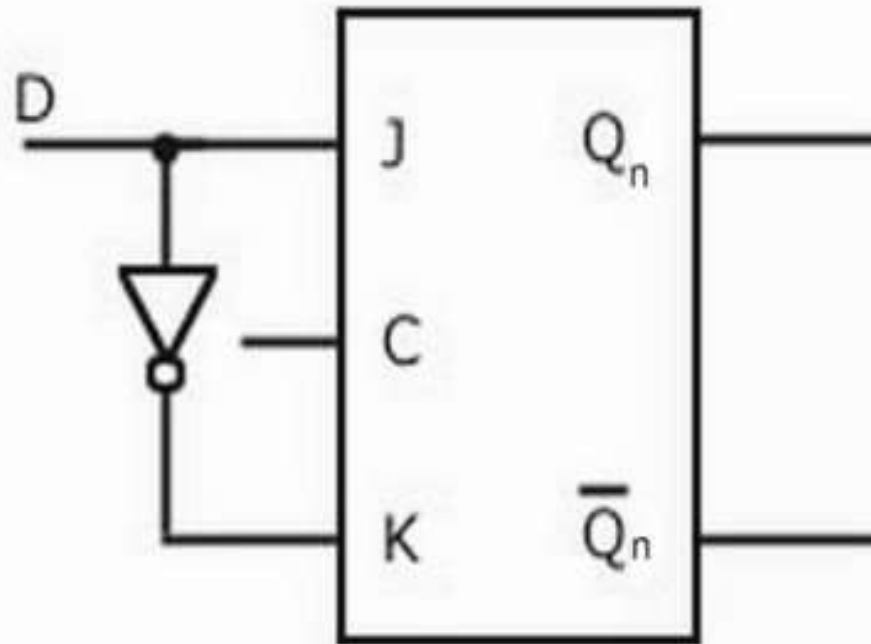
Input	Present state	Next state	Flip flop Inputs	
D	Q_n	Q_{n+1}	J	K
0	0	0	0	X
0	1	0	X	1
1	0	1	1	x
1	1	1	x	0

K- MAP
SIMPLIFICATION



Logic Diagram (JK to D)

Logic Diagram



D(Available) to T(Target)Flip-Flop

Conversion Table

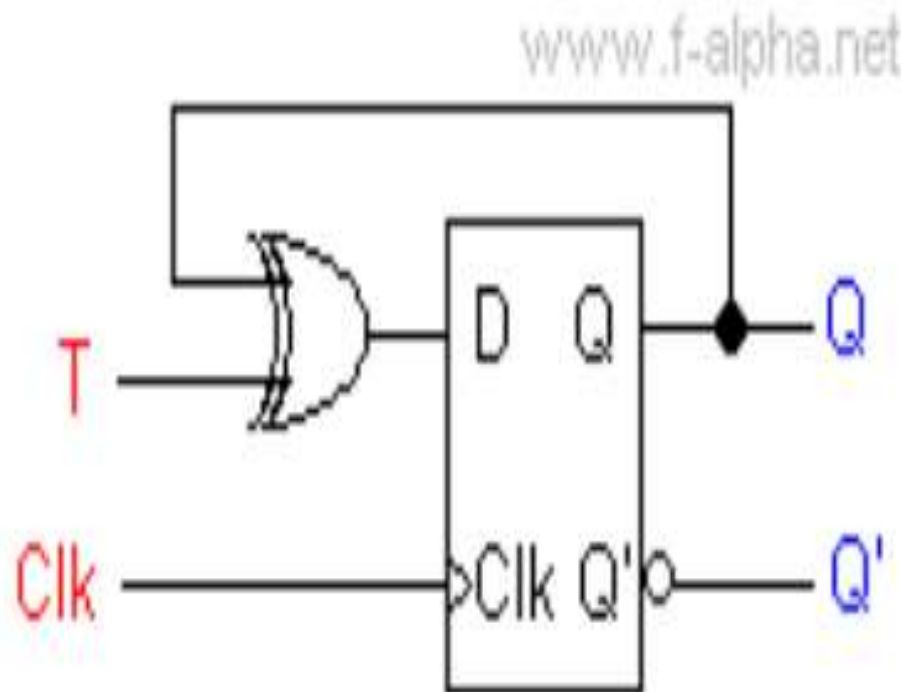
Input	Present state	Next state	Flip flop Inputs
T	Q _n	Q _{n+1}	D
0	0	0	0
0	1	1	1
1	0	1	1
1	1	0	0

K- MAP SIMPLIFICATION

		Q _n	
		0	1
T	0	0	1
	1	1	0

$$D = T'Q_n + TQ_n'$$

Logic Diagram(D to T)



T (Available) to D(Target) Flip-flop Conversion

Conversion Table

Input	Present state	Next state	Flip flop Inputs
D	Q_n	Q_{n+1}	T
0	0	0	0
0	1	0	1
1	0	1	1
1	1	1	0

K- MAP SIMPLIFICATION

		Q_n	
		0	1
D:	0	0	1
	1	1	0

$$T = DQ_n' + D'Q_n$$

JK(Available) to SR(Target) Flip-flop conversion

Conversion Table

Input		Present State	Next State	Flip-Flop Inputs	
S	R	Q n	Qn+1	J	K
0	0	0	0	0	X
0	0	1	1	X	0
0	1	0	0	0	X
0	1	1	0	X	1
1	0	0	1	1	X
1	0	1	1	X	0
1	1	0	X	X	X
1	1	1	X	X	X

JK(Available) to SR(Target) Flip-flop conversion

SR	Qn	
	0	1
00	0	X
01	0	X
11	X	X
10	1	X

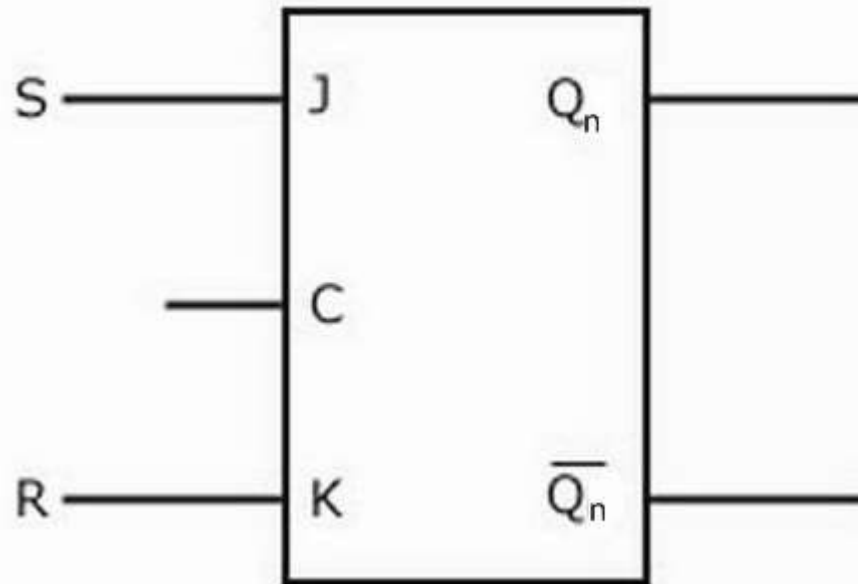
J=S

SR	Qn	
	0	1
00	X	0
01	X	1
11	X	X
10	0	X

K=R

JK to SR

- Logic Diagram



D(Available) to SR(Target) Flip-Flop Conversion

Conversion Table

Input		Present State	Next State	Flip-Flop Inputs	
S	R	Q _n	Q _{n+1}	J	K
0	0	0	0	0	0
0	0	1	1	1	1
0	1	0	0	0	0
0	1	1	0	0	0
1	0	0	1	1	1
1	0	1	1	1	1
1	1	0	X	X	X
1	1	1	X	X	X

D to SR

K- MAP SIMPLIFICATION

R Qn		00	01	11	10
S					
0		0	1	0	0
1		1	1	X	X

$$D = R'Q_n + S$$

Logic Diagram For D to SR

