



Fast Multipliers (Wallace and Dadda)

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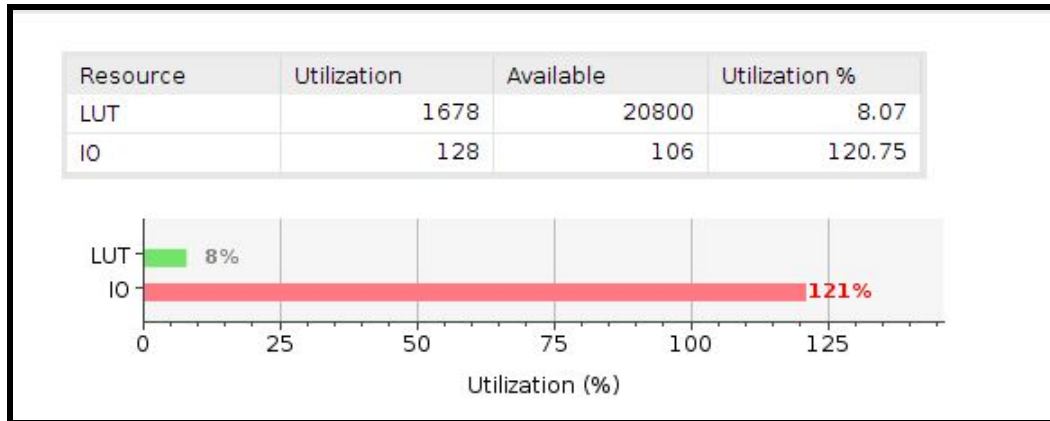
Aman Dhawan (2018CSB1070)

Overview

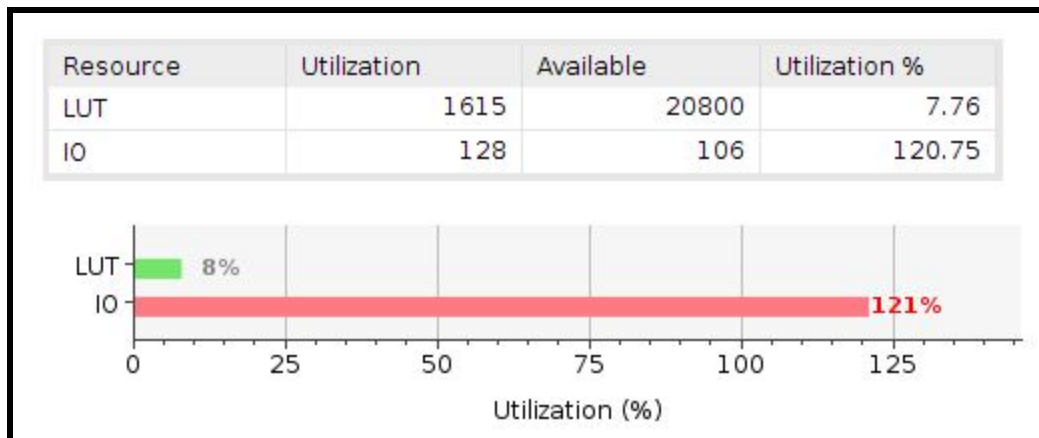
Implementation of two fast multipliers namely Wallace Tree and Dadda Tree Multiplier and its comparison with classical multiplier in terms of power, gates used and LUT utilization.

#Utilization Summary

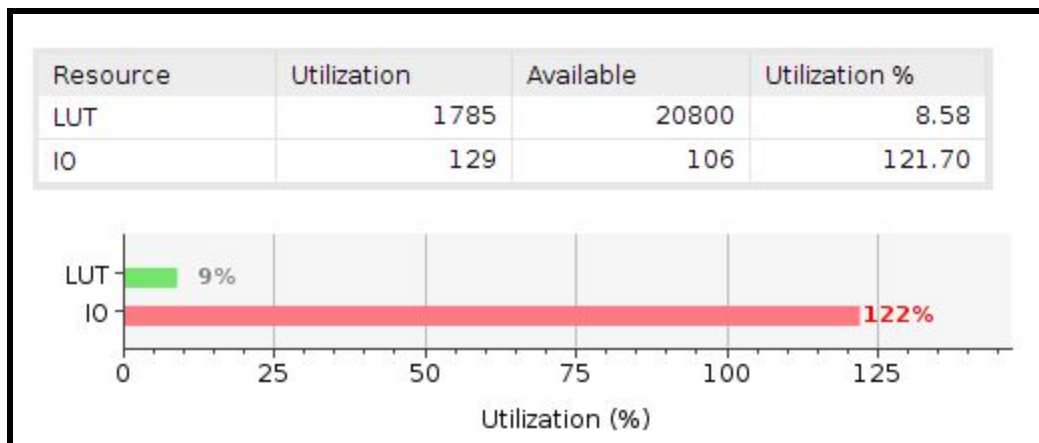
Wallace Multiplier



Dadda Multiplier

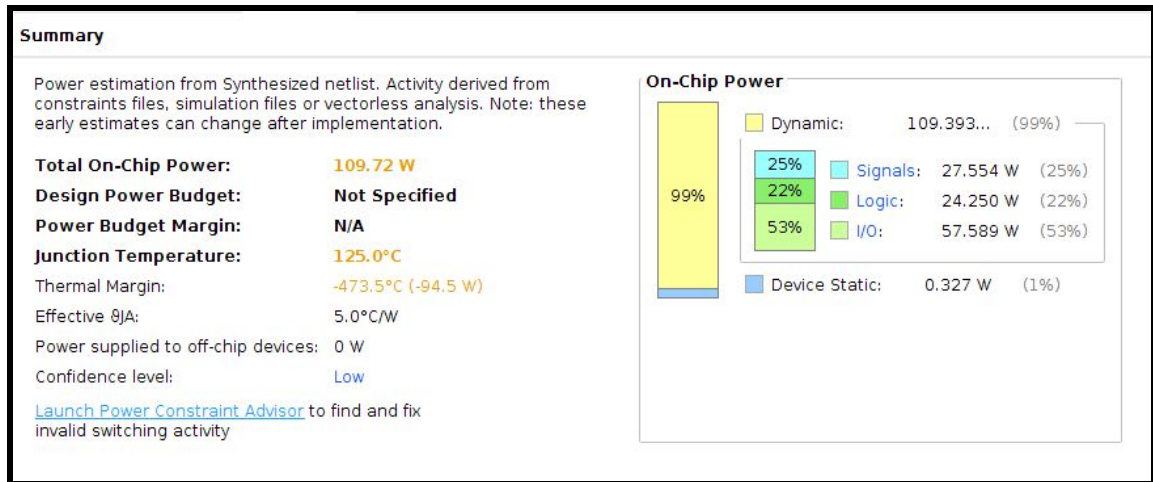


Classical Multiplier

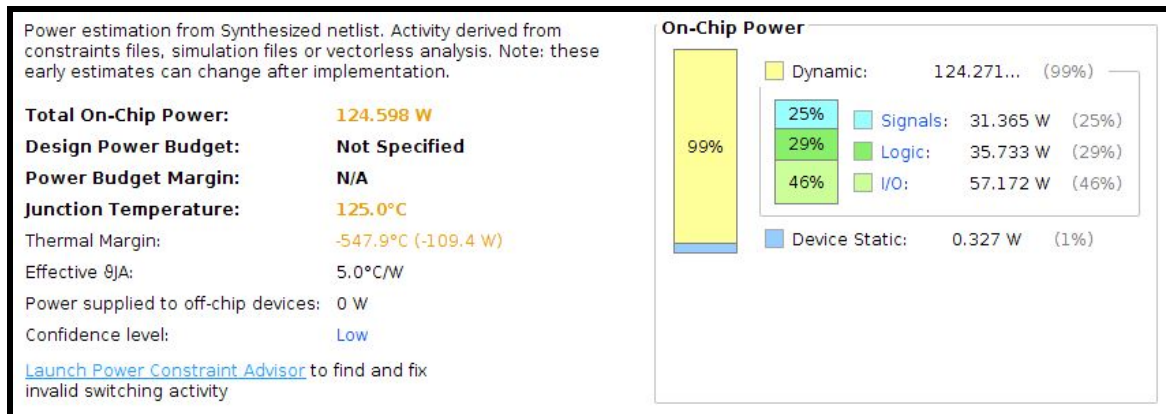


#Power Summary

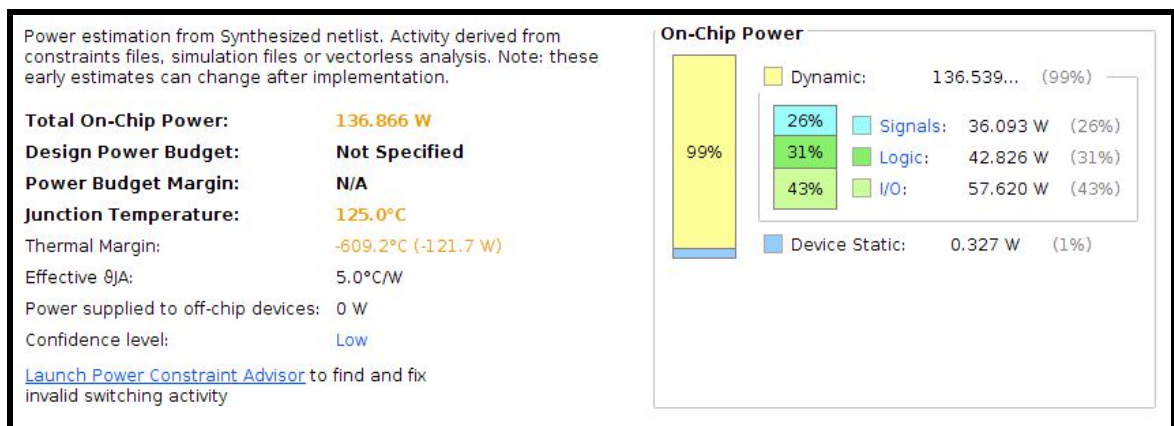
Wallace Multiplier



Dadda Multiplier



Classical Multiplier



Conclusion

~Clearly from the above data power consumption is maximum for Classical Multiplier followed by Dadda and Wallace multiplier.

~In terms of resource utilization , classical have maximum LUT utilization followed by Wallace and Dadda multiplier.