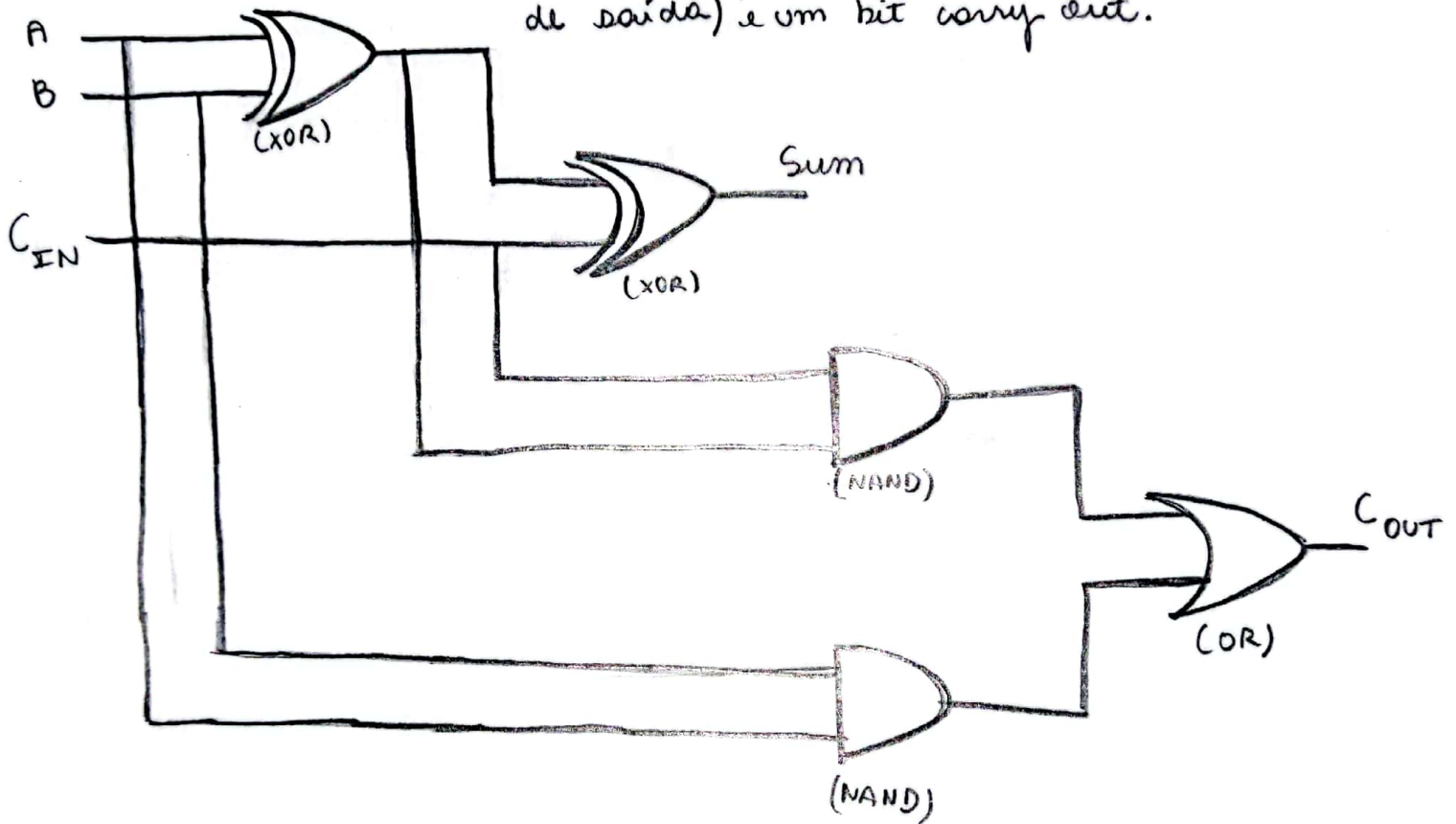
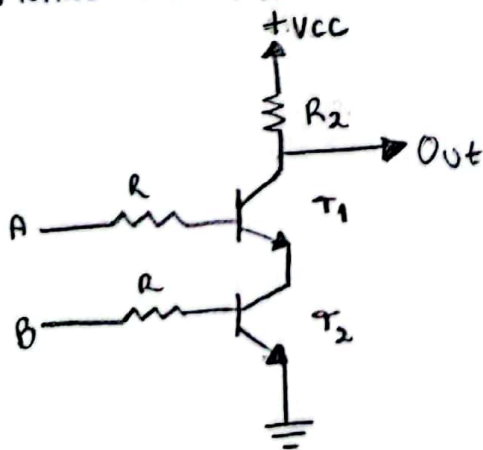


TAREFA 3: Full Adder com transistores.

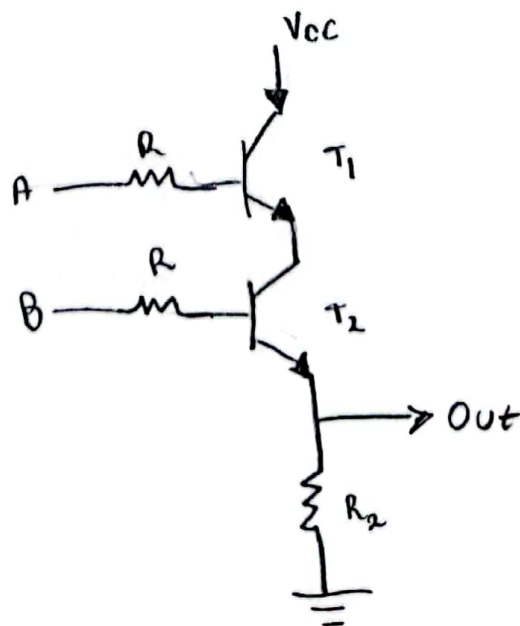
- Somador completo: adiciona 2 bits de entrada (A e B) e um bit carry in. Realiza a soma desses três bits de entrada e produz uma sum (soma de dois bits de saída) e um bit carry out.



• NAND com Transistores



• OR com Transistores



- Xor com transistores :

