

4-Bit Counter (0-15) Using 8085 Microprocessor

In the world of digital electronics and microprocessors, creating a digital counter is a fundamental and exciting project that demonstrates the practical applications of microprocessors and the manipulation of binary numbers. The "Counter 0-15 Using 8085 Microprocessor" project, enhanced with 4 LEDs and a 1-kilohm resistor, is designed to provide a hands-on learning experience and a visual representation of binary counting.

Project Overview:

The core of this project is the 8085 microprocessor, a widely used and versatile microprocessor in the early days of computing. The 8085 microprocessor processes data in binary format, making it ideal for implementing a binary counter. In this project, we will use the 8085 microprocessor to create a 4-bit binary counter, capable of counting from 0 to 15 (in binary, 0000 to 1111).

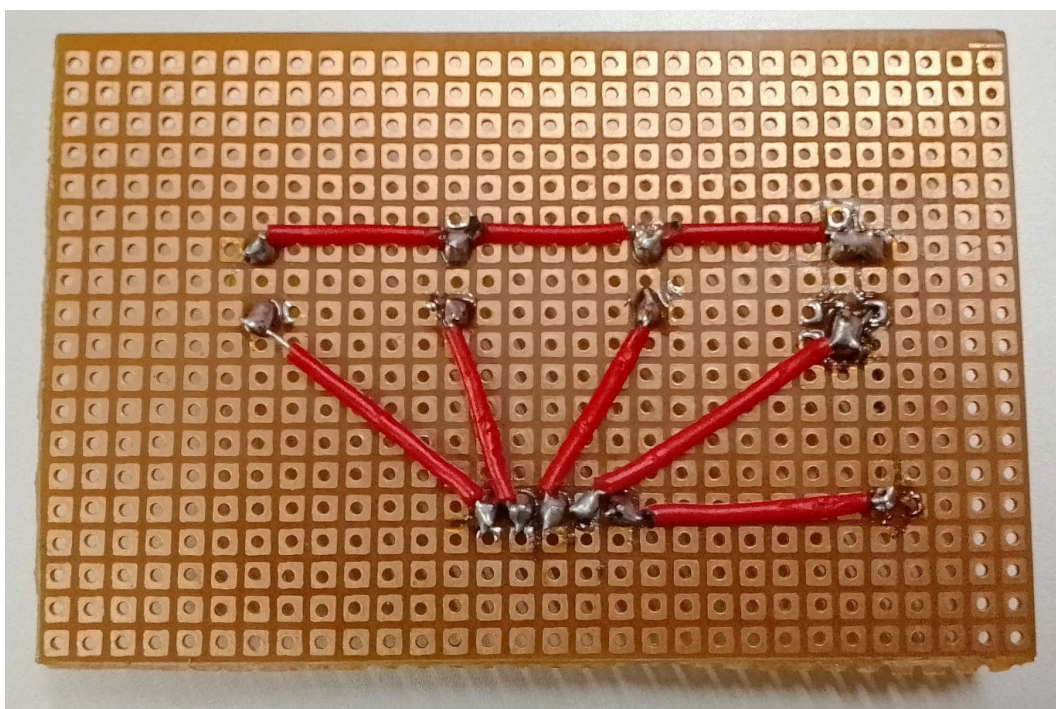
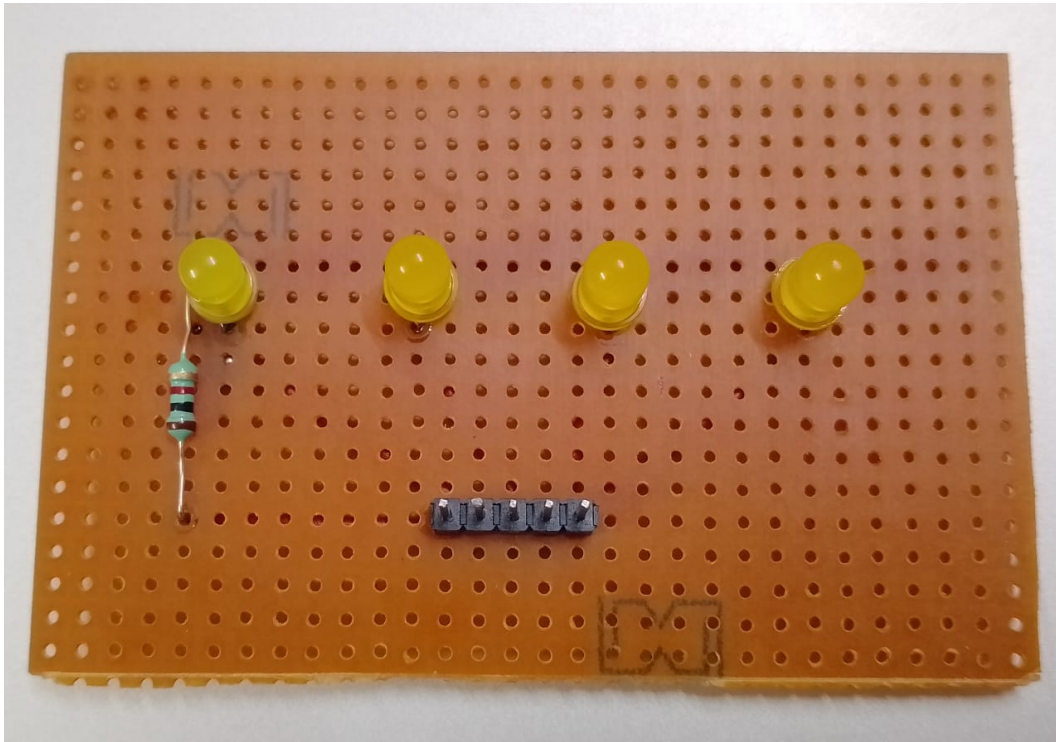
Components Used:

1. 8085 Microprocessor: The brain of our project, responsible for processing and controlling the counting operation.
2. 4 LEDs (Light Emitting Diodes): These LEDs represent the 4 bits of the binary count. Each LED will be associated with one bit, allowing us to visualize the binary count as it progresses.
3. 1-Kilohm Resistor: Used to limit the current flowing through the LEDs and protect them from burning out.
4. Power Supply: The project requires a stable power supply to operate the 8085 microprocessor and light up the LEDs.

Project Functionality:

The 8085 microprocessor will be programmed to count from 0 to 15 in binary. As it counts, the appropriate LEDs will light up to display the binary value currently held in the counter. For example, when the counter is at 3, the LEDs will represent "0011," and as it progresses, the LEDs will change to reflect the binary value of the count. This provides a visual representation of the binary counting process and allows for easy verification of the counter's operation.

4-BIT COUNTER (0-15) USING 8085 MICROPROCESSOR



Name

Program

Trainer Kit

Fabrication of PCB - (Counter 0-15) Project

8085

Trainer kit

Date

Page

| Label | Address | Instructions | Opcode | Comments |
|-------|---------|--------------|--------|-----------------------------------|
| loop3 | 2000 | MVI A, 80H | 3E 80 | Move content 80 to Register A |
| | 2001 | OUT 03 | D3 03 | Give OP to Control Register |
| | 2002 | MVIC 0FH | 0E 0F | Move content 0F to Register C |
| | 2003 | MOV A to C | 79 | Move A to C |
| loop | 2004 | OUT 00 | D3 00 | Give OP to Control Register |
| | 2005 | Delay | CD BC | Introduce Delay |
| | 2006 | Delay | CD BC | " |
| | 2007 | Delay | CD BC | " |
| | 2008 | Delay | CD BC | " |
| | 2009 | Delay | CD BC | " |
| | 200A | Delay | CD BC | " |
| | 200B | Delay | CD BC | " |
| | 200C | Delay | CD BC | " |
| | 200D | Delay | CD BC | " |
| | 200E | Delay | CD BC | " |
| | 200F | Delay | CD BC | " |
| | 2010 | DCRC | 0D | Decrement C |
| | 2011 | JNZ loop | C2 | Jump if not zero to 2006 |
| | 2012 | | 06 | |
| | 2013 | | 20 | |
| | 2014 | MVI A, 00 | 3E 00 | Move the content 00 to Register A |
| | 2015 | OUT 00 | D3 00 | Give output |
| | 2016 | Delay | CD BC | Introduce Delay |
| | 2017 | Delay | CD BC | " |
| | 2018 | Delay | CD BC | " |
| | 2019 | Delay | CD BC | " |
| | 201A | Delay | CD BC | " |
| | 201B | JMP loop2 | C3 60 | Jump to loop 2 to 2060 |
| | 201C | loop2 | 60 | |
| | 201D | | 21 | |
| | 201E | HLT | 76 | Halt |
| | 201F | | | |
| | 2020 | | | |

EIC SECTION Microprocessor interfacing worksheet

Name _____

Program _____

Trainer Kit _____

Date: _____

Page: _____

| Label | Address | Instructions | Opcode | Comments |
|-------|---------|--------------|--------|--------------------|
| loop2 | 2160 | MVI A, 3E | 3E | Move the content |
| | 2161 | 80 | 80 | 80 to Register A |
| | 2162 | OUT, 03 | D3 | Give OP to Control |
| | 2163 | | 03 | Register |
| | 2164 | MVIC, 0F | 0E | Move the content |
| | 2165 | | 0F | 0F to Register C |
| | 2166 | MVI B, 00 | 06 | Move the content |
| | 2167 | | 00 | 00 to Register B |
| loop1 | 2168 | MOV A, B | 78 | Move A to B |
| | 216A | OUT 00 | D3 | OUTPUT |
| | 216B | | 00 | |
| | 216C | Delay | 0D | Introduce Delay |
| | 216D | | BC | |
| | 216E | | 03 | |
| | 216F | IN B | 04 | Increment B by 1 |
| | 2170 | DCR C | 0D | Decrement C by 1 |
| | 2171 | JNZ loop | C2 | Jump if not zero |
| | 2172 | 1 | 68 | 2006 |
| | 2173 | | 21 | |
| | 2174 | JMP | C3 | Jump not to |
| | 2175 | loop3 | 00 | 2000 |
| | 2176 | | 20 | |
| | 2177 | HLT | 76 | HALT |