

#### **8.24.10.11 SUBSCRIBE\_PREPARETX**

Address offset: 0x0A4

## Subscribe configuration for task PREPARETX

#### 8.24.10.12 SUBSCRIBE DMA

## Subscribe configuration for tasks

#### 8.24.10.12.1 SUBSCRIBE DMA.RX

## Subscribe configuration for tasks

#### **8.24.10.12.1.1 SUBSCRIBE\_DMA.RX.ENABLEMATCH[n] (n=0..3)**

Address offset: 0x0B0 + (n × 0x4)

Subscribe configuration for task `ENABLEMATCH[n]`

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	B	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A			
<b>Reset 0x00000000</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>			
ID	R/W	Field	Value ID	Value	Description																											
A	RW	CHIDX	[0..255]	DPPI channel that task <a href="#">ENABLEMATCH[n]</a> will subscribe to																												
B	RW	EN	Disabled	0	Disable subscription																											
			Enabled	1	Enable subscription																											

#### **8.24.10.12.1.2 SUBSCRIBE\_DMA.RX.DISABLEMATCH[n] (n=0..3)**

Address offset:  $0x0C0 + (n \times 0x4)$

## Subscribe configuration for task DISABLEMATCH[n]