

7.8.5.5.14 FEATURE.GRTC.CC[n] (n=0..23)

Address offset: 0xD00 + (n × 0x4)

Security configuration for CC n of GRTC

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																			
ID																												B				A							
Reset 0x00100010				0 0 0 0 0 0 0 0 0 0 0 0 1 0																																			

7.8.5.5.15 FEATURE.GRTC.PWMCONFIG

Address offset: 0xD74

Security Configuration for PWMCONFIG of GRTC

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID																												B				A			
Reset 0x00100010				0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	SECATTR			SECATTR feature																														
			NonSecure	0	Feature is available for non-secure usage																														
			Secure	1	Feature is reserved for secure usage																														
B	RW	LOCK W1S			LOCK feature																														
			Unlocked	0	Feature permissions can be updated																														
			Locked	1	Feature permissions can not be changed until the next reset																														
					When Locked, it remains Locked until the next reset cycle.																														

7.8.5.5.16 FEATURE.GRTC.CLK

Address offset: 0xD78

Security configuration for CLKOUT/CLKCFG of GRTC