

5.4.3.2 TASKS_XOSTOP

Address offset: 0x004

Stop crystal oscillator (HFXO)

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	TASKS_XOSTOP			Stop crystal oscillator (HFXO)
		Trigger		1	Trigger task

5.4.3.3 TASKS_PLLSTART

Address offset: 0x008

Start PLL and keep it running, regardless of the automatic clock requests

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	TASKS_PLLSTART			Start PLL and keep it running, regardless of the automatic clock requests
		Trigger		1	Trigger task

5.4.3.4 TASKS_PLLSTOP

Address offset: 0x00C

Stop PLL

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	TASKS_PLLSTOP			Stop PLL
		Trigger		1	Trigger task

5.4.3.5 TASKS_LFCLKSTART

Address offset: 0x010

Start LFCLK source as selected in LFCLK.SRC

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	TASKS_LFCLKSTART			Start LFCLK source as selected in LFCLK.SRC
		Trigger		1	Trigger task

5.4.3.6 TASKS_LFCLKSTOP

Address offset: 0x014