## 6.8 AC Switching Characteristics (continued)

parameters valid over –40°C ≤ $T_J$ ≤ 150 °C range (unless otherwise noted)

| PARAMETER | | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|---|---|---|---|---|---|---|
| $t_{SCKL}$ | SCK, SPI clock low (1) | See Figure 7-7 | 125 | | | ns |
| $t_{ACC}$ | First read access time from chip select (1) | See Figure 7-7 | 50 | | | ns |
| $t_{CSS}$ | Chip select setup time (1) | See Figure 7-7 | 100 | | | ns |
| $t_{CSH}$ | Chip select hold time (1) | See Figure 7-7 | 100 | | | ns |
| $t_{CSD}$ | Chip select disable time (1) | See Figure 7-7 | 50 | | | ns |
| $t_{SISU}$ | Data in setup time (1) | See Figure 7-7 | 50 | | | ns |
| $t_{SIH}$ | Data in hold time (1) | See Figure 7-7 | 50 | | | ns |
| $t_{SOV}$ | Data out valid (1) | See Figure 7-7 | | | 80 | ns |
| $t_{RSO}$ | SO rise time (1) | See Figure 7-7 | | | 40 | ns |
| $t_{FSO}$ | SO fall time (1) | See Figure 7-7 | | | 40 | ns |

(1) Specified by design
(2) This parameter is valid only when register 11h[7:6] = 11b
(3) This is the minimum pulse width for a WAKE pin input that device will detect as a good pulse. Values between the min $t_{WK\_WIDTH\_MIN}$ and max $t_{WK\_WIDTH\_INVALID}$ is indeterminant and may or may not be considered valid.
(4) This parameter is set based upon the programmed value for $t_{WK\_WIDTH\_INVALID}$ register 11h[3:2]

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