

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A-B	RW	SEQEND[i]_STOP (i=0..1)			Shortcut between event <a href="#">SEQEND[n]</a> and task <a href="#">STOP</a>																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
C-D	RW	LOOPSDONE_DMA_SEQ[i]_START (i=0..1)			Shortcut between event <a href="#">LOOPSDONE</a> and task <a href="#">DMA.SEQ[n].START</a>																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
E	RW	LOOPSDONE_STOP			Shortcut between event <a href="#">LOOPSDONE</a> and task <a href="#">STOP</a>																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
F	RW	RAMUNDERFLOW_STOP			Shortcut between event <a href="#">RAMUNDERFLOW</a> and task <a href="#">STOP</a>																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
G-H	RW	DMA_SEQ[i]_BUSERROR_STOP (i=0..1)			Shortcut between event <a href="#">DMA.SEQ[n].BUSERROR</a> and task <a href="#">STOP</a>																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														

### 8.15.5.24 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	RW	STOPPED			Enable or disable interrupt for event <a href="#">STOPPED</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
B-C	RW	SEQSTARTED[i] (i=0..1)			Enable or disable interrupt for event <a href="#">SEQSTARTED[i]</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
D-E	RW	SEQEND[i] (i=0..1)			Enable or disable interrupt for event <a href="#">SEQEND[i]</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
F	RW	PWMPERIODEND			Enable or disable interrupt for event <a href="#">PWMPERIODEND</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
G	RW	LOOPSDONE			Enable or disable interrupt for event <a href="#">LOOPSDONE</a>																													
					This event triggers after the last SEQ[1] completion of the loop, and only if looping was enabled (LOOP > 0) when the sequence playback was started.																													
			Disabled	0	Disable																													
H	RW	RAMUNDERFLOW			Enable or disable interrupt for event <a href="#">RAMUNDERFLOW</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
I	RW	DMASEQOEND			Enable or disable interrupt for event <a href="#">DMASEQOEND</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
J	RW	DMASEQOREADY			Enable or disable interrupt for event <a href="#">DMASEQOREADY</a>																													