

- [TASKS\\_PWMSTOP](#)
- [EVENTS\\_PWMPERIODEND](#)
- [EVENTS\\_PWMREADY](#)
- [PUBLISH\\_PWMREADY](#)
- [STATUS.PWM](#)
- Registers, tasks, and events associated with the CLKOUT and CLKCFG functions have configurable security attribute.
  - [CLKOUT](#)
  - [CLKCFG](#)
  - [EVENTS\\_CLKOUTREADY](#)
  - [PUBLISH\\_CLKOUTREADY](#)
  - [STATUS.CLKOUT](#)

[INTERVAL](#) has same security attribute as the [CC\[0\]](#) register group.

For more information on GRTC split ownership and security attributes, see [Split Security](#) on page 300.

## Interrupts

GRTC provides multiple interrupts. Each interrupt is associated with its own set of [INTENm/INTENSETm/INTENCLRm/INTENPENDm](#) register. All events are routed to each of [INTENm/INTENSETm/INTENCLRm/INTENPENDm](#) registers, however only those events matching the security attributes can be accessible using the [INTENm/INTENSETm/INTENCLRm/INTENPENDm](#) registers.

### 8.10.6 Task priority

If the START task and the STOP task are triggered at the same time, meaning within the same period of LFCLK, the STOP task is prioritized.

If one or more of the CAPTURE tasks and the CLEAR task are triggered at the same time, that is, within the same period of LFCLK, the CAPTURE tasks are prioritized. This means that the CC registers will capture the counter value before the CLEAR tasks are triggered.

The [STATUS.LFTIMER.READY](#) indicates busy while handling the tasks [TASKS\\_START](#), [TASKS\\_STOP](#) and [TASKS\\_CLEAR](#). These tasks must be triggered only when the [STATUS.LFTIMER.READY](#) status indicates ready.

### 8.10.7 Registers

#### Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
GRTC : S	GLOBAL	0x500E2000	US	S	NA	Yes	Global RTC GRTC
GRTC : NS		0x400E2000					