

Register	Offset	TZ	Description
SEEDLOCK	0x440		Lock the access to the SEED register.
PROTECTEDDRAMLOCK	0x444		Lock the access to the protected RAM.

7.8.1.6.1 EVENTS_CRYPTOMASTER

Address offset: 0x100

Event indicating that interrupt triggered at Cryptomaster

The interrupt source must be cleared at Cryptomaaster before clearing this event.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																															A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	RW	EVENTS_CRYPTOMASTER			Event indicating that interrupt triggered at Cryptomaster																											
					The interrupt source must be cleared at Cryptomaster before clearing this event.																											
		NotGenerated	0		Event not generated																											
		Generated	1		Event generated																											

7.8.1.6.2 EVENTS RNG

Address offset: 0x104

Event indicating that interrupt triggered at RNG

The interrupt source must be cleared at RNG before clearing this event.

7.8.1.6.3 EVENTS PKEIKG

Address offset: 0x108

Event indicating that interrupt triggered at PKE or IKG

The interrupt source must be cleared at PKE or IKG before clearing this event.