

Bit number					31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID					G F E D C B A																															
Reset 0x00000000					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value	Description																															
D	RW	DONE	Enabled	1	Enable																															
			Disabled	0	Enable or disable interrupt for event DONE																															
			Enabled	1	Disable																															
E	RW	XOTUNED			Enable																															
			Disabled	0	Disable																															
			Enabled	1	Enable																															
F	RW	XOTUNEERROR			Enable or disable interrupt for event XOTUNEERROR																															
			Disabled	0	Disable																															
			Enabled	1	Enable																															
G	RW	XOTUNEFAILED			Enable or disable interrupt for event XOTUNEFAILED																															
			Disabled	0	Disable																															
			Enabled	1	Enable																															

5.4.3.25 INTENSET

Address offset: 0x304

Enable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	XOSTARTED W1S			Write '1' to enable interrupt for event XOSTARTED																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	PLLSTARTED W1S			Write '1' to enable interrupt for event PLLSTARTED																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
C	RW	LFCLKSTARTED W1S			Write '1' to enable interrupt for event LFCLKSTARTED																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
D	RW	DONE W1S			Write '1' to enable interrupt for event DONE																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
E	RW	XOTUNED W1S			Write '1' to enable interrupt for event XOTUNED																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														