

8.17.14.17 TASKS_SOFTRESET

Address offset: 0x0A4

Reset all public registers, but with these exceptions: DMA registers and EVENT/INTEN/SUBSCRIBE/PUBLISH registers. Only to be used in DISABLED state.

8.17.14.18 SUBSCRIBE TXEN

Address offset: 0x100

Subscribe configuration for task TXEN

8.17.14.19 SUBSCRIBE RXEN

Address offset: 0x104

Subscribe configuration for task RXEN