

SUSPEND task has been issued, TWI traffic is now suspended.

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID					A																															
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																															
A	RW	EVENTS_SUSPENDED			SUSPEND task has been issued, TWI traffic is now suspended.																															
			NotGenerated	0	Event not generated																															
			Generated	1	Event generated																															

8.23.10.12 EVENTS_LASTRX

Address offset: 0x134

Byte boundary, starting to receive the last byte

Bit number					31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
ID					A																																	
Reset 0x00000000					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																																	
A	RW	EVENTS_LASTRX			Byte boundary, starting to receive the last byte																																	
			NotGenerated	0	Event not generated																																	
			Generated	1	Event generated																																	

8.23.10.13 EVENTS_LASTTX

Address offset: 0x138

Byte boundary, starting to transmit the last byte

Bit number					31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
ID					A																																	
Reset 0x00000000					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																																	
A	RW	EVENTS_LASTTX			Byte boundary, starting to transmit the last byte																																	
			NotGenerated	0	Event not generated																																	
			Generated	1	Event generated																																	

8.23.10.14 EVENTS_DMA

Peripheral events.

8.23.10.14.1 EVENTS_DMA.RX

Peripheral events.

8.23.10.14.1.1 EVENTS_DMA.RX.END

Address offset: 0x14C

Indicates that the transfer of MAXCNT bytes between memory and the peripheral has been fully completed.