

Pin	Clock pin	Name	Function	Description	Dedicated function
		<b>EXTREF</b>	Digital I/O Analog input	GRTC CLKOUTFAST External reference for SAADC	
G1		<b>P0.00</b>	Digital I/O	General purpose I/O	
G2		<b>P2.10</b>  <b>TRACEDATA[3]</b>	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.10 Trace data SPIM CSN SPIS CSN UARTE RTS	FLPR Trace SPIM00/21 SPIS00/21 UARTE00/21
G3		<b>VDD</b>	Power	Power supply	
G4		<b>VSS</b>	Power	Ground	
G5		<b>P2.05</b>	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O SPIM CSN SPIS CSN UARTE RTS FLPR.5 QSPI CSN	SPIM00/20 SPIS00/20 UARTE00/20 FLPR FLPR (QSPI)
G6		<b>P2.00</b>	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O SPIM DCX UARTE RXD FLPR.4 QSPI D3	SPIM00/20 UARTE00/20 FLPR FLPR (QSPI)
G7	Yes	<b>P2.01</b>	Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O SPIM SCK SPIS SCK FLPR.0 QSPI SCK	SPIM00/20 SPIS00/20 FLPR FLPR (QSPI)

Table 81: CSP47 pin assignments

For the device to function properly, the exposed die pad (pin 49) must be connected to ground (**VSS**, pins 32 and 44).