

Starts operation using easyDMA to load the values. See peripheral description for operation using easyDMA.

		Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID			A		
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	START			Starts operation using easyDMA to load the values. See peripheral description for operation using easyDMA.
		Trigger	1		Trigger task

8.25.13.2.2.2 TASKS_DMA.TX.STOP

Address offset: 0x054

Stops operation using easyDMA. This does not trigger an END event.

		Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID			A		
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	W	STOP			Stops operation using easyDMA. This does not trigger an END event.
		Trigger	1		Trigger task

8.25.13.3 SUBSCRIBE_FLUSHRX

Address offset: 0x09C

Subscribe configuration for task [FLUSHRX](#)

		Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID		B	A A A A A A A A		
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	RW	CHIDX	[0..255]		DPPI channel that task FLUSHRX will subscribe to
B	RW	EN			
		Disabled	0		Disable subscription
		Enabled	1		Enable subscription

8.25.13.4 SUBSCRIBE_DMA

Subscribe configuration for tasks

8.25.13.4.1 SUBSCRIBE_DMA.RX

Subscribe configuration for tasks

8.25.13.4.1.1 SUBSCRIBE_DMA.RX.START

Address offset: 0x0A8

Subscribe configuration for task [START](#)