

Status to indicate if data sent from the device to the debugger has been read.

9.6.5.1.11 INFO.PARTNO

Address offset: 0x030

Part number of the device

This register is retained on system on idle.

9.6.5.1.12 INFO.HWREVISION

Address offset: 0x034

Hardware Revision of the device

This register is retained on system on idle.

9.6.5.1.13 IDR

Address offset: 0x0FC

CTRL-AP Identification Register, IDR