

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
ID				B																												A	A	A	A	A	A	A	A
Reset 0x00000000				0																																			
ID	R/W	Field	Value ID	Value		Description																																	
A	RW	CHIDX		[0..255]		DPPI channel that task STOP will subscribe to																																	
B	RW	EN																																					
			Disabled	0	Disable subscription																																		
			Enabled	1	Enable subscription																																		

8.18.11.8 SUBSCRIBE_CALIBRATEOFFSET

Address offset: 0x08C

Subscribe configuration for task **CALIBRATEOFFSET**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID				B																								A				A	A	A	A	A	A	A
Reset 0x00000000				0																																		
ID	R/W	Field	Value ID	Value		Description																																
A	RW	CHIDX		[0..255]		DPPI channel that task CALIBRATEOFFSET will subscribe to																																
B	RW	EN																																				
			Disabled	0	Disable subscription																																	
			Enabled	1	Enable subscription																																	

8.18.11.9 EVENTS_STARTED

Address offset: 0x100

The ADC DMA has started

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	EVENTS_STARTED			The ADC DMA has started																														
			NotGenerated	0	Event not generated																														
			Generated	1	Event generated																														

8.18.11.10 EVENTS_END

Address offset: 0x104

The ADC has filled up the Result buffer

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	EVENTS_END			The ADC has filled up the Result buffer																														
			NotGenerated	0	Event not generated																														
			Generated	1	Event generated																														

8.18.11.11 EVENTS_DONE

Address offset: 0x108