

Pin	Clock pin	Name	Function	Description	Dedicated function
		AIN3	Analog input	Analog input	
F1		P0 . 01	Digital I/O	General purpose I/O	
F2		SWDIO	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
F3		P2 . 09 TRACEDATA[2]	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.9 Trace data SPIM SDI SPIS SDI UARTE CTS	FLPR Trace SPIM00/21 SPIS00/21 UARTE00/21
F4	Yes	P2 . 06 TRACECLK	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.6 SPIM SCK SPIS SCK Trace clock	FLPR SPIM00/21 SPIS00/21 Trace
F5		P2 . 04	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O SPIM SDI SPIS SDI UARTE CTS FLPR.2 QSPI D1	SPIM00/20 SPIS00/20 UARTE00/20 FLPR FLPR (QSPI)
F6		P2 . 02	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O SPIM SDO SPIS SDO UARTE TXD FLPR.1 QSPI D0	SPIM00/20 SPIS00/20 UARTE00/20 FLPR FLPR (QSPI)
F7	Yes	P1 . 08	Digital I/O	General purpose I/O	