

- The *I²C bus specification* allows a maximum line capacitance of 400 pF.
- The value of internal pull-up resistor (R_{PU}) for nRF54L15/10/05 can be found in [GPIO — General purpose input/output](#) on page 274.

8.23.10 Registers

Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
TWIM20 : S	GLOBAL	0x500C6000	US	S	SA	No	Two-wire interface controller
TWIM20 : NS		0x400C6000					TWIM20
TWIM21 : S	GLOBAL	0x500C7000	US	S	SA	No	Two-wire interface controller
TWIM21 : NS		0x400C7000					TWIM21
TWIM22 : S	GLOBAL	0x500C8000	US	S	SA	No	Two-wire interface controller
TWIM22 : NS		0x400C8000					TWIM22
TWIM30 : S	GLOBAL	0x50104000	US	S	SA	No	Two-wire interface controller
TWIM30 : NS		0x40104000					TWIM30

Configuration

Instance	Domain	Configuration
TWIM20 : S	GLOBAL	Use GPIO port P1
TWIM20 : NS		
TWIM21 : S	GLOBAL	Use GPIO port P1
TWIM21 : NS		
TWIM22 : S	GLOBAL	Use GPIO port P1
TWIM22 : NS		
TWIM30 : S	GLOBAL	Use GPIO port P0
TWIM30 : NS		

Register overview

Register	Offset	TZ	Description
TASKS_STOP	0x004		Stop TWI transaction. Must be issued while the TWI master is not suspended.
TASKS_SUSPEND	0x00C		Suspend TWI transaction
TASKS_RESUME	0x010		Resume TWI transaction
TASKS_DMA.RX.START	0x028		Starts operation using easyDMA to load the values. See peripheral description for operation using easyDMA.
TASKS_DMA.RX.STOP	0x02C		Stops operation using easyDMA. This does not trigger an END event.
TASKS_DMA.RX.ENABLEMATCH[n]	0x030		Enables the MATCH[n] event by setting the ENABLE[n] bit in the CONFIG register.
TASKS_DMA.RX.DISABLEMATCH[n]	0x040		Disables the MATCH[n] event by clearing the ENABLE[n] bit in the CONFIG register.
TASKS_DMA.TX.START	0x050		Starts operation using easyDMA to load the values. See peripheral description for operation using easyDMA.
TASKS_DMA.TX.STOP	0x054		Stops operation using easyDMA. This does not trigger an END event.
SUBSCRIBE_STOP	0x084		Subscribe configuration for task STOP
SUBSCRIBE_SUSPEND	0x08C		Subscribe configuration for task SUSPEND
SUBSCRIBE_RESUME	0x090		Subscribe configuration for task RESUME
SUBSCRIBE_DMA.RX.START	0x0A8		Subscribe configuration for task START
SUBSCRIBE_DMA.RX.STOP	0x0AC		Subscribe configuration for task STOP
SUBSCRIBE_DMA.RX.ENABLEMATCH[n]	0x0B0		Subscribe configuration for task ENABLEMATCH[n]