

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																			
ID																																G F E D C B				A A A A			
Reset 0x00000000				0 0																																			
ID	R/W	Field	Value ID	Value	Description																																		
D	RW	INSTPRIORITY			Prioritize instruction trace bit. Controls if a trace unit can prioritize instruction trace when the instruction trace buffer space is less than LEVEL.																																		
			Disabled	0	The trace unit must not prioritize instruction trace.																																		
			Enabled	1	The trace unit can prioritize instruction trace. A trace unit might prioritize instruction trace by preventing output of data trace, or other means which ensure that the instruction trace has a higher priority than the data trace.																																		
E	RW	DATADISCARDLOAD			Data discard field. Controls if a trace unit can discard data trace elements on a load when the data trace buffer space is less than LEVEL.																																		
			Disabled	0	The trace unit must not discard any data trace elements.																																		
			Enabled	1	The trace unit can discard P1 and P2 elements associated with data loads.																																		
F	RW	DATADISCARDSTORE			Data discard field. Controls if a trace unit can discard data trace elements on a store when the data trace buffer space is less than LEVEL.																																		
			Disabled	0	The trace unit must not discard any data trace elements.																																		
			Enabled	1	The trace unit can discard P1 and P2 elements associated with data stores.																																		
G	RW	NOOVERFLOW			Trace overflow prevention bit.																																		
			Disabled	0	Trace overflow prevention is disabled.																																		
			Enabled	1	Trace overflow prevention is enabled. This might cause a significant performance impact.																																		

9.8.1.8 TRCTSCTLR

Address offset: 0x30

Controls the insertion of global timestamps in the trace streams.

When the selected event is triggered, the trace unit inserts a global timestamp into the trace streams.

Might ignore writes when the trace unit is enabled or not idle.

Must be programmed if TRCONFIGR.TS == 1.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																									
ID																														A				A				A				A			
Reset 0x00000000				0 0																																									
ID	R/W	Field	Value ID	Value	Description																																								
A	RW	EVENT		[0:255]	Select which event should generate time stamps.																																								

9.8.1.9 TRCSYNCPR

Address offset: 0x34

Controls how often trace synchronization requests occur.

Might ignore writes when the trace unit is enabled or not idle.

If writes are permitted then the register must be programmed.