

Bit number		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ID																																		
Reset 0x00000000		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	TZ	Field		Value ID		Value		Description																									
				Enabled		1		Read: Enabled																										
J	RW	S	PORTOSECURE					Write '1' to disable interrupt for event PORTOSECURE																										
			W1C																															
				Clear		1		Disable																										
				Disabled		0		Read: Disabled																										
				Enabled		1		Read: Enabled																										

8.9.5.15 CONFIG[n] (n=0..7)

Address offset: 0x510 + (n × 0x4)

Configuration for OUT[n], SET[n], and CLR[n] tasks and IN[n] event

Bit number		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID																E	D	D	C	C	C	B	B	B	B	A	A	A								
Reset 0x00000000		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
ID	R/W	Field		Value ID		Value		Description																												
A	RW	MODE						Mode																												
				Disabled		0		Disabled. Pin specified by PSEL will not be acquired by the GPIOTE module.																												
				Event		1		Event mode																												
				Task		3		The pin specified by PSEL will be configured as an input and the IN[n] event will be generated if operation specified in POLARITY occurs on the pin.																												
								Task mode																												
								The GPIO specified by PSEL will be configured as an output and triggering the SET[n], CLR[n] or OUT[n] task will perform the operation specified by POLARITY on the pin. When enabled as a task the GPIOTE module will acquire the pin and the pin can no longer be written as a regular output pin from the GPIO module.																												
B	RW	PSEL			[0..31]			GPIO number associated with SET[n], CLR[n], and OUT[n] tasks and IN[n] event																												
C	RW	PORT			[0..15]			Port number																												
D	RW	POLARITY						When In task mode: Operation to be performed on output when OUT[n] task is triggered. When In event mode: Operation on input that shall trigger IN[n] event.																												
								None		0																										
								Task mode: No effect on pin from OUT[n] task. Event mode: no IN[n] event generated on pin activity.																												
								LoToHi		1																										
								HiToLo		2																										
								Toggle		3																										
E	RW	OUTINIT						When in task mode: Initial value of the output when the GPIOTE channel is configured. When in event mode: No effect.																												
								Low		0																										
								High		1																										