

8.12.4.21 ENABLE

Address offset: 0x500

Enable LPCOMP

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	ENABLE			Enable or disable LPCOMP																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														

8.12.4.22 PSEL

Address offset: 0x504

Input pin select

The pin is selected based on PSEL.PORT

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																				
ID																													B	B	B	B					A	A	A	A	A
Reset 0x00000000					0 0																																				
ID	R/W	Field	Value ID	Value	Description																																				
A	RW	PIN			Analog pin select																																				
B	RW	PORT			GPIO Port selection																																				

8.12.4.23 REFSEL

Address offset: 0x508

Reference select

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A A A																															
Reset 0x00000004				0 1 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	REFSEL			Reference select																														
			Ref1_8Vdd	0	VDD * 1/8 selected as reference																														
			Ref2_8Vdd	1	VDD * 2/8 selected as reference																														
			Ref3_8Vdd	2	VDD * 3/8 selected as reference																														
			Ref4_8Vdd	3	VDD * 4/8 selected as reference																														
			Ref5_8Vdd	4	VDD * 5/8 selected as reference																														
			Ref6_8Vdd	5	VDD * 6/8 selected as reference																														
			Ref7_8Vdd	6	VDD * 7/8 selected as reference																														
			ARef	7	External analog reference selected																														
			Ref1_16Vdd	8	VDD * 1/16 selected as reference																														
			Ref3_16Vdd	9	VDD * 3/16 selected as reference																														
			Ref5_16Vdd	10	VDD * 5/16 selected as reference																														
			Ref7_16Vdd	11	VDD * 7/16 selected as reference																														
			Ref9_16Vdd	12	VDD * 9/16 selected as reference																														
			Ref11_16Vdd	13	VDD * 11/16 selected as reference																														
			Ref13_16Vdd	14	VDD * 13/16 selected as reference																														