

8.17.14.104 DATAWHITE

Address offset: 0x540

Data whitening configuration

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	B	A	A	A	A	A	A	A	A	A	A	A	A			
Reset 0x00890040	0	0	0	0	0	0	0	1	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	RW	IV			Whitening initial value																											
					Data whitening initial value.																											
B	RW	POLY			Whitening polynomial																											
					Data whitening polynomial. Bit 0 is always interpreted as 1.																											

8.17.14.105 AUXDATA.CNF[n] (n=0..1)

Address offset: $0x548 + (n \times 0x4)$

AUXDATA configuration

This register will not be reset by the SOFTRESET task