

field of the packet payload exceeds [PCNF1.STATLEN](#), and the LENGTH field in the packet specifies a packet larger than configured in [PCNF1.MAXLEN](#), the payload will be truncated to the length specified in [PCNF1.MAXLEN](#).

Note: The [PCNF1.MAXLEN](#) field includes the payload and the add-on, but excludes the size occupied by the S0, LENGTH, and S1 fields. This has to be taken into account when allocating RAM.

If the payload and add-on length is specified larger than [PCNF1.MAXLEN](#), RADIO will transmit or receive in the same way as before, except the payload is now truncated to [PCNF1.MAXLEN](#). The packet's LENGTH field will not be altered when the payload is truncated. RADIO will calculate CRC as if the packet length is equal to [PCNF1.MAXLEN](#).

Note: If [PACKETPTR](#) is not pointing to the RAM region, an EasyDMA transfer may result in a HardFault or RAM corruption. See [Memory](#) on page 13 for more information about the different memory regions.

The [END](#) event indicates that the last bit has been processed by RADIO. The [DISABLED](#) event is issued to acknowledge that the [DISABLE](#) task is done.

8.17.14 Registers

Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
RADIO : S	GLOBAL	0x5008A000	US	S	SA	No	2.4 GHz radio RADIO
RADIO : NS		0x4008A000					See pinout for GPIO options for DFE antenna switch control

Configuration

Instance	Domain	Configuration
RADIO : S	GLOBAL	For the PSEL registers, use only dedicated pins on port P1
RADIO : NS		No internal instantiation of DmaChannelPeripheral

Register overview

Register	Offset	TZ	Description
TASKS_TXEN	0x000		Enable RADIO in TX mode
TASKS_RXEN	0x004		Enable RADIO in RX mode
TASKS_START	0x008		Start RADIO
TASKS_STOP	0x00C		Stop RADIO
TASKS_DISABLE	0x010		Disable RADIO
TASKS_RSSISTART	0x014		Start the RSSI and take one single sample of the receive signal strength
TASKS_BCSTART	0x018		Start the bit counter
TASKS_BCSTOP	0x01C		Stop the bit counter
TASKS_EDSTART	0x020		Start the energy detect measurement used in IEEE 802.15.4 mode
TASKS_EDSTOP	0x024		Stop the energy detect measurement
TASKS_CCSTART	0x028		Start the clear channel assessment used in IEEE 802.15.4 mode
TASKS_CCSTOP	0x02C		Stop the clear channel assessment
TASKS_AUXDATADMASTART	0x038		Start DMA transaction of acquisition