

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A																															
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value	Description																											
A	W	CLEARPATTERN			Clear the GPIO pattern array for antenna control																											
					Behaves as a task register, but does not have PPI nor IRQ																											

8.17.14.126 PSEL.DFEGPIO[n] (n=0..7)

Address offset: 0xD30 + (n × 0x4)

Pin select for DFE pin n

Note: Must be set before enabling the radio

8.17.14.127 DFEPACKET

DFE packet EasyDMA channel

8.17.14.127.1 DFEPACKET.PTR

Address offset: 0xD50

Data pointer

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	RW	PTR			Data pointer																											
						See the memory chapter for details about which memories are available for EasyDMA.																										

8.17.14.127.2 DFEPACKET.MAXCNT

Address offset: 0xD54

Maximum number of bytes to transfer

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																																
Reset 0x00004000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0													
ID	R/W	Field	Value ID	Value	Description																											
A	RW	MAXCNT			Maximum number of bytes to transfer																											