8.17.14.62 PUBLISH_READY

Address offset: 0x300

Publish configuration for event READY

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event READY will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.63 PUBLISH_TXREADY

Address offset: 0x304

Publish configuration for event TXREADY

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event TXREADY will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.64 PUBLISH_RXREADY

Address offset: 0x308

Publish configuration for event RXREADY

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event RXREADY will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.65 PUBLISH_ADDRESS

Address offset: 0x30C

Publish configuration for event ADDRESS