Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID						E D C B A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
B	RW W1C	REPORTRDY			Write '1' to disable interrupt for event REPORTRDY	
					Event generated when REPORTPER number of samples has been accumulated in the ACC register and the content of the ACC register is not equal to 0. (Thus, this event is only generated if a motion is detected since the previous clearing of the ACC register).	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
C	RW W1C	ACCOF			Write '1' to disable interrupt for event ACCOF	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
D	RW W1C	DBLRDY			Write '1' to disable interrupt for event DBLRDY	
					Event generated when REPORTPER number of samples has been accumulated and the content of the ACCDBL register is not equal to 0. (Thus, this event is only generated if a double transition is detected since the previous clearing of the ACCDBL register).	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
E	RW W1C	STOPPED			Write '1' to disable interrupt for event STOPPED	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	

8.16.7.24 ENABLE

Address offset: 0x500

Enable the quadrature decoder

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID						A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A-	RW	ENABLE			Enable or disable the quadrature decoder	
					When enabled the decoder pins will be active. When disabled the quadrature decoder pins are not active and can be used as GPIO .	
			Disabled	0	Disable	
			Enabled	1	Enable	

8.16.7.25 LEDPOL

Address offset: 0x504

NORDIC
SEMICONDUCTOR