Pin	Clock pin	Name	Function	Description	Dedicated function
25		`P0.00`	Digital I/O	General purpose I/O	
26		`P0.01`	Digital I/O	General purpose I/O	
27		`SWDIO`	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
28		`SWDCLK`	Debug	Serial wire clock. Input with on-chip pull-down.	
29		`P0.02`	Digital I/O	General purpose I/O	
30	Yes	`P0.03`	Digital I/O Digital I/O	General purpose I/O GRTC PWM	GRTC
31	Yes	`P0.04`	Digital I/O Digital I/O	General purpose I/O GRTC CLKOUT32K	GRTC
32		`P0.05`	Digital I/O	General purpose I/O	
33		`nRESET`	Reset	Pin reset with on-chip pull-up	
34		`ANT`	RF	Single ended radio antenna connection	See Reference circuitry on page 889 for guidelines on how to ensure good RF performance
35		`VSS_PA`	Power	Ground (radio supply)	
36		`DECRF`	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See Reference circuitry on page 889.
37		`XC1`	Analog input	Connection for 32 MHz crystal	
38		`XC2`	Analog input	Connection for 32 MHz crystal	
39		`VDD`	Power	Power supply	

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