



Figure 8-13. UV<sub>CC</sub> flow chart

#### Note

If an over-voltage or short circuit event takes place while the device is in fail-safe mode due to a under-voltage event on V<sub>CC</sub>, the device will behave as shown in the OV<sub>CC</sub> and VCC<sub>SC</sub> flow charts.

For an over-voltage event, OV<sub>CC</sub>, the device turns off the V<sub>CC</sub> LDO, and transitions to either sleep mode, fail-safe mode disabled, or fail-safe mode. When a wake event takes place, the V<sub>CC</sub> LDO is turned on for t<sub>LDOON</sub> to determine if the over-voltage is still present. If cleared, the device enters restart mode from either sleep or fail-safe modes. When in fail-safe mode, if the over-voltage has not cleared when the wake event takes place the device transitions to sleep mode.