

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																										B	A	A	A			
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value		Description																										
A	R	SWITCHINGSTATE					Internal state of switching state machine																									
		Idle		0			Switching state Idle																									
		Offset		1			Switching state Offset																									
		Guard		2			Switching state Guard																									
		Ref		3			Switching state Ref																									
		Switching		4			Switching state Switching																									
		Ending		5			Switching state Ending																									
B	R	SAMPLINGSTATE					Internal state of sampling state machine																									
		Idle		0			Sampling state Idle																									
		Sampling		1			Sampling state Sampling																									

8.17.14.122 DFECTRL1

Address offset: 0xD10

Various configuration for Direction finding