

8.15.5.27 INTPEND

Address offset: 0x30C

Pending interrupts

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C	B	A														
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID																															
A			R																											Description				
			STOPPED																											Read pending status of interrupt for event STOPPED				
			NotPending																										Read: Not pending					
			Pending																										Read: Pending					
B-C			R																											SEQSTARTED[i] (i=0..1)				
			NotPending																										Read pending status of interrupt for event SEQSTARTED[i]					
			Pending																										Read: Pending					
D-E			R																										SEQEND[i]					
			NotPending																										Read: Not pending					
			Pending																									Read: Pending						