

### 8.26.3.72 NORDIC.OUTBRW

Address offset: 0xBC9

Word reversed register OUTB

This register is shadow to register OUTB and holds frame reversed in the whole word of OUTB

Bit number										31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID										A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
Reset 0x00000000										0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field		Value ID	Value					Description																																		
A	RW	VAL			0x0..0xFFFFFFFF																																							

### 8.26.3.73 NORDIC.INBRB

Address offset: 0xBCA

Byte reversed register INB

This register is shadow to register INB and holds frame reversed in each byte of OUTB

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value				Description																											
A	R	VAL		0x0..0xFFFFFFFF																															

### 8.26.3.74 NORDIC.SHIFTCTRLB

Address offset: 0xBCB

Buffered IO shift control

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID														C <sub>A</sub> C <sub>A</sub>		B <sub>B</sub> B <sub>B</sub> B <sub>B</sub> B <sub>B</sub>				B <sub>A</sub> B <sub>A</sub> B <sub>A</sub>			A <sub>A</sub> A <sub>A</sub> A <sub>A</sub> A <sub>A</sub> A <sub>A</sub> A <sub>A</sub>												
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
D	R/W	Field	Value ID	Value		Description																													
A	RW	SHIFTCNTB																																	
A <sub>A</sub>	RW	SHIFTCNTB.VALUE		0..32		Alias to SHIFTCNTB.VALUE register																													
B	RW	OUTMODEB																																	
B <sub>A</sub>	RW	OUTMODEB.MODE				Alias to OUTMODEB.MODE register																													
		NoShifting		0x0	No shifting																														
		OutBBuf		0x2	Only OUTB used for buffering																														
		OutBBufToggleClk		0x4	Only OUTB used for buffering, auto-toggle clock line																														
B <sub>B</sub>	RW	OUTMODEB.FRAMEWIDTH		0..16		Alias to OUTMODEB.FRAMEWIDTH register																													
C	RW	INMODEB																																	
C <sub>A</sub>	RW	INMODEB.MODE				Alias to INMODEB.MODE register																													
		CONTINUOUS		0x0	Continuous sampling (if CPU is not sleeping)																														
		EVENT		0x1	Sampling on Counter1 event																														
		SHIFT		0x2	Sampling and shifting on Counter1 event																														

### 8.26.3.75 NORDIC.SHIFTCNTIN

Address offset: 0xBCD

Number of frames to be shifted from INB before new data is required