

Security configuration for interrupt o of GPIO[n]

#### 7.8.5.5.12 FEATURE.GPIO[n].PIN[o] (n=0..2) (o=0..31)

Address offset:  $0x800 + (n \times 0x80) + (o \times 0x4)$

## Security configuration for GPIO[n] PIN[o]

### 7.8.5.5.13 FEATURE.CRACEN.SEED

Address offset: 0x980

## Configuration for CRACEN SEED