

8.3.11 HSSC or FSO (High-side Switch Control or Function Output)

In pin control mode, this pin is the high-side switch control pin. When in SPI control mode, the pin becomes a function output pin that can be selected from register 8'h29[3:1]. In SPI mode this pin can be switched back to HSSC input by using register 8'h1E[6:4] = 101b.

8.3.12 WKRQ or INH (Wake Request or Inhibit)

Upon power up, the state of this pin determines if it is WKRQ or INH. When externally pulled low with a 100 kΩ resistor, the WKRQ function is enabled which is an active high, digital output supporting the internal voltage rail (V_{INT}) or V_{CC} as described in [PIN or nCS \(Pin Watchdog Select or SPI Chip Select\)](#). When left floating or pulled low by a 1 MΩ resistor, this pin becomes the high voltage inhibit (INH) output which is used to support the enable pin of a power device. If a capacitor to ground is used off of this pin, it must be less than or equal to 50 pF. When WKRQ is selected, the pin behavior is based off of the LDO, so any event that causes the LDO to be turned off will turn off the WKRQ pin.

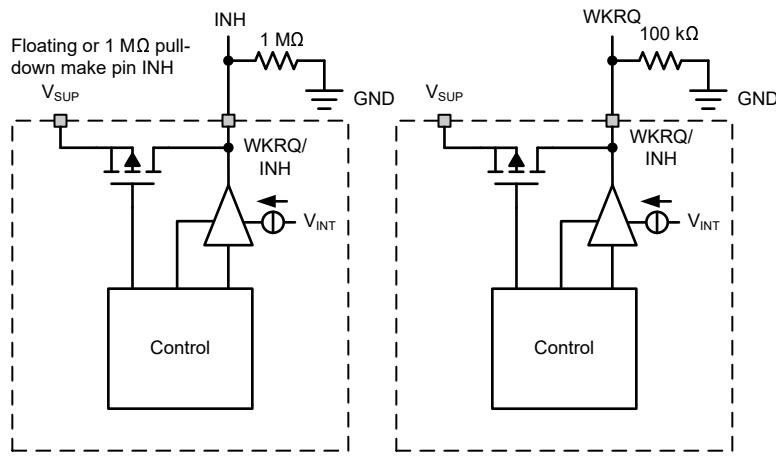


Figure 8-7. WKRQ or INH Pin Select

8.3.13 PV

This output pin is the divided down value from V_{BAT} . The output is buffered to keep the output from exceeding the specified values when V_{BAT} exceeds the recommended value. It is connected directly to the ADC of the microcontroller. It is connected by either an RC network or with just a capacitor to GND, see [Figure 8-8](#). It is switched on when a high is present on the DIV_ON pin. When off, the PV pin is in a high-Z state.

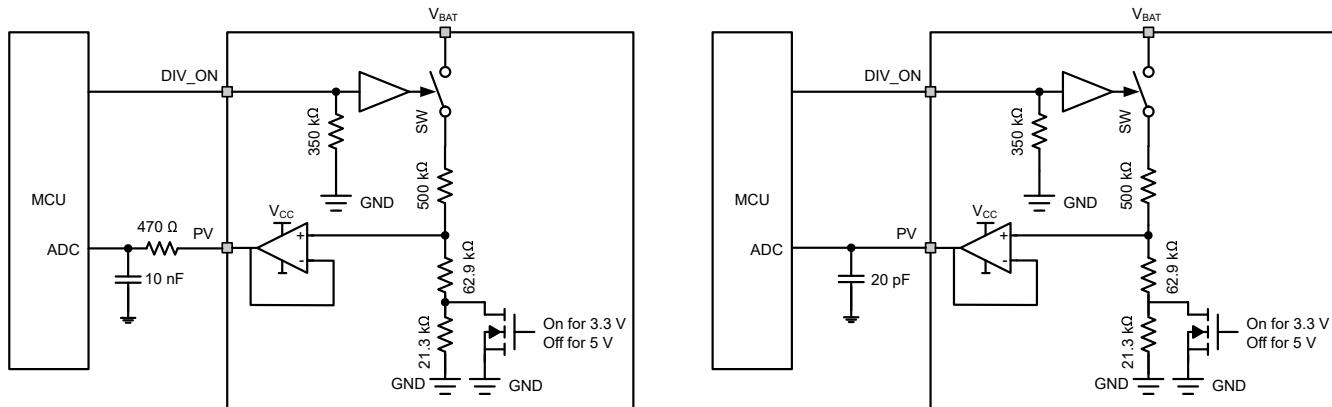


Figure 8-8. PV Connections to MCU

8.3.14 DIV_ON

This is a logic input pin used to enable the voltage divider PV output. This is an active high pin and is disabled in certain modes of operation.