

	Memory model	IDAU configuration	Typical SAU configuration	Resulting TrustZone attribute	MPC override configuration
0xE000 0000	Private peripheral bus	Not Applicable		Not Applicable	
Device					
0xA000 0000	Device	Non-Secure		Non-Secure	
RAM					
0x8000 0000	RAM				
0x6000 0000			Non-Secure SAU region 3		
0x5000 0000...	Peripheral	Secure		Secure	
0x4000 0000				Non-Secure	Non-secure RAM region
SRAM					
0x2000 0000		Non-Secure			
0x02F1 0000	CACHE		No region - Secure	Secure	
0x02F0 0000	SICR		NSC - SAU region 2	NSC	
0x00FF E000	UICR		No region - Secure	Secure	
0x00FF D000	FICR		Non-Secure SAU region 1	Non-Secure	Non-secure RRAM region
0x00FF C000			No region - Secure	Secure	
Code			NSC SAU region 0	NSC	
0x0000 0000			No region - Secure	Secure	

Figure 29: Example memory map security attribution

TrustZone security access

The Arm Cortex-M TrustZone security module generates a CPU SecureFault exception when access is not allowed. The following table shows combinations of TrustZone security attributes.