

### 7.8.1.6.6 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	CRYPTOMASTER W1C			Write '1' to disable interrupt for event <span>CRYPTOMASTER</span>																														
					The interrupt source must be cleared at Cryptomaaster before clearing this event.																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	RNG W1C			Write '1' to disable interrupt for event <span>RNG</span>																														
					The interrupt source must be cleared at RNG before clearing this event.																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
C	RW	PKEIKG W1C			Write '1' to disable interrupt for event <span>PKEIKG</span>																														
					The interrupt source must be cleared at PKE or IKG before clearing this event.																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														

### 7.8.1.6.7 INTPEND

Address offset: 0x30C

Pending interrupts

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			C B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	R	CRYPTOMASTER			Read pending status of interrupt for event <span>CRYPTOMASTER</span>																													
					The interrupt source must be cleared at Cryptomaaster before clearing this event.																													
			NotPending	0	Read: Not pending																													
			Pending	1	Read: Pending																													
B	R	RNG			Read pending status of interrupt for event <span>RNG</span>																													
					The interrupt source must be cleared at RNG before clearing this event.																													
			NotPending	0	Read: Not pending																													
			Pending	1	Read: Pending																													
C	R	PKEIKG			Read pending status of interrupt for event <span>PKEIKG</span>																													
					The interrupt source must be cleared at PKE or IKG before clearing this event.																													
			NotPending	0	Read: Not pending																													
			Pending	1	Read: Pending																													