

Pin	Clock pin	Name	Function	Description	Dedicated function
A1		XC1	Analog input	Connection for 32 MHz crystal	
A2		XC2	Analog input	Connection for 32 MHz crystal	
A3	Yes	P1.12 ASI[3] RADIO[3] AIN5	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 input RADIO DFEGPIO Analog input	TAMPC RADIO
A4		DECA	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
A5		DECD	Power	0.9 V regulator supply decoupling	
A6		VSS	Power	Ground	
A7		DCC	Power	DC/DC regulator output	
B1		DECRF	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See <a href="#">Reference circuitry</a> on page 889.
B3		P1.09 ASO[2] RADIO[0]	Digital I/O Digital I/O Digital I/O	General purpose I/O TAMPC active shield 2 output RADIO DFEGPIO	TAMPC RADIO
B4		P1.13 RADIO[4] AIN6	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
B5		P1.14 RADIO[5] AIN7	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
B6		P1.15	Digital I/O	General purpose I/O	
B7		VDD	Power	Power supply	