

Pin	Clock pin	Name	Function	Description	Dedicated function
28		<b>DECRF</b>	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See <a href="#">Reference circuitry</a> on page 889.
29		<b>XC1</b>	Analog input	Connection for 32 MHz crystal	
30		<b>XC2</b>	Analog input	Connection for 32 MHz crystal	
31		<b>VDD</b>	Power	Power supply	
32	Yes	<b>P1.11</b> <b>ASO[3]</b> <b>RADIO[2]</b> <b>AIN4</b>	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 output RADIO DFEGPIO Analog input	TAMPC RADIO
33	Yes	<b>P1.12</b> <b>ASI[3]</b> <b>RADIO[3]</b> <b>AIN5</b>	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 input RADIO DFEGPIO Analog input	TAMPC RADIO
34		<b>P1.13</b> <b>RADIO[4]</b> <b>AIN6</b>	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
35		<b>P1.14</b> <b>RADIO[5]</b> <b>AIN7</b>	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
36		<b>DECA</b>	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
37		<b>VSS</b>	Power	Ground	
38		<b>DECD</b>	Power	0.9 V regulator supply decoupling	
39		<b>DCC</b>	Power	DC/DC regulator output	