Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	P O N M L K J I H G F E D C B A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
O	R	PWMREADY			Read pending status of interrupt for event PWMREADY
			NotPending	0	Read: Not pending
			Pending	1	Read: Pending
P	R	CLKOUTREADY			Read pending status of interrupt for event CLKOUTREADY
			NotPending	0	Read: Not pending
			Pending	1	Read: Pending

8.10.7.33 EVTEN

Address offset: 0x400

Enable or disable event routing

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A	RW	PWMPERIODEND			Enable or disable event routing for event PWMPERIODEND
			Disabled	0	Disable
			Enabled	1	Enable

8.10.7.34 EVTENSET

Address offset: 0x404

Enable event routing

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A	RW	PWMPERIODEND			Write '1' to enable event routing for event PWMPERIODEND
			Disabled	0	Read: Disabled
			Enabled	1	Read: Enabled
			Set	1	Enable

8.10.7.35 EVTENCLR

Address offset: 0x408

Disable event routing

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A	RW	PWMPERIODEND			Write '1' to disable event routing for event PWMPERIODEND
			Disabled	0	Read: Disabled
			Enabled	1	Read: Enabled
			Clear	1	Disable

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