Figure 8-46. WAKE Pin Pulse Behavior
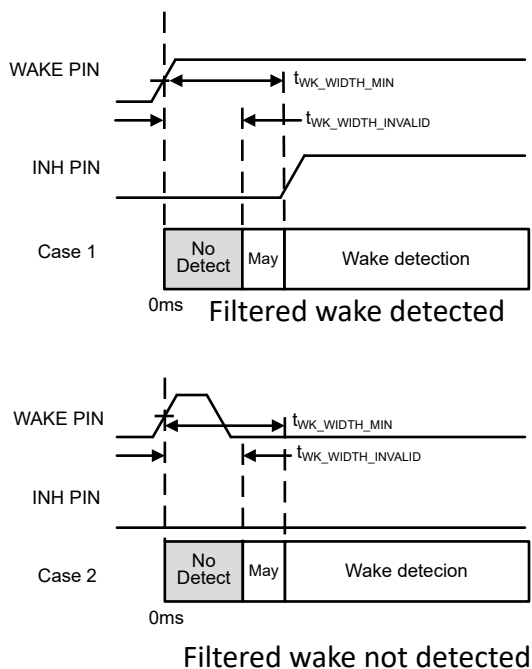

Figure 8-47. WAKE Pin Filtered Behavior

8.4.8.2.2 Cyclic Sense Wake

Cyclic sense wake is a method using the high-side switch with the WAKE input pin to periodically check for a WAKE pin state change in standby and sleep modes. In sleep mode, cyclic sense wake reduces the quiescent current of the device by reducing the WAKE circuitry to be active only during the on time of the HSS pin, see Figure 8-48 as an example for this. Periodically, the HSS pin turns on applying V_{SUP} to the external local wake circuitry and the device samples the state of the WAKE pin. Each time the WAKE pin is sampled, the current