

## Register overview

Register	Offset	TZ	Description
CRYPTMSTRDMA.FETCHADDRLSB	0x000		Fetch Address Least Significant Word
CRYPTMSTRDMA.FETCHADDRMSB	0x004		Fetch Address Most Significant Word
CRYPTMSTRDMA.FETCHLEN	0x008		Fetch DMA Length (only used in direct mode)
CRYPTMSTRDMA.FETCHTAG	0x00C		Fetch User Tag (only used in direct mode)
CRYPTMSTRDMA.PUSHADDRLSB	0x010		Push Address Least Significant Word
CRYPTMSTRDMA.PUSHADDRMSB	0x014		Push Address Most Significant Word
CRYPTMSTRDMA.PUSHLEN	0x018		Push Length (only used in direct mode)
CRYPTMSTRDMA.INTEN	0x01C		Interrupt Enable mask
CRYPTMSTRDMA.INTENSET	0x020		Interrupt Set
CRYPTMSTRDMA.INTENCLR	0x024		Interrupt Clear
CRYPTMSTRDMA.INTSTATRAW	0x028		Interrupt Status Raw
CRYPTMSTRDMA.INTSTAT	0x02C		Interrupt Status
CRYPTMSTRDMA.INTSTATCLR	0x030		Interrupt Status Clear
CRYPTMSTRDMA.CONFIG	0x034		Cryptomaster configuration
CRYPTMSTRDMA.START	0x038		Start
CRYPTMSTRDMA.STATUS	0x03C		Status
CRYPTMSTRHW.INCLIPSHWCFG	0x400		Included IPs Hardware configuration
CRYPTMSTRHW.BA411EAESHWCFG1	0x404		Generic g_AesModesPoss value.
CRYPTMSTRHW.BA411EAESHWCFG2	0x408		Generic g_CtrSize value.
CRYPTMSTRHW.BA413HASHHWCFG	0x40C		Generic g_Hash value
CRYPTMSTRHW.BA418SHA3HWCFG	0x410		Generic g_Sha3CtxEn value.
CRYPTMSTRHW.BA419SM4HWCFG	0x414		Generic g_SM4ModesPoss value.
CRYPTMSTRHW.BA424ARIAHWCFG	0x418		Generic g_aria_modePoss value.
RNGCONTROL.CONTROL	0x1000		Control register
RNGCONTROL.FIFOLEVEL	0x1004		FIFO level register.
RNGCONTROL.FIFOTHRESHOLD	0x1008		FIFO threshold register.
RNGCONTROL.FIFODEPTH	0x100C		FIFO depth register.
RNGCONTROL.KEY[n]	0x1010		Key register.
RNGCONTROL.TESTDATA	0x1020		Test data register.
RNGCONTROL.REPEATTHRESHOLD	0x1024		Repetition Test Count Cut-Off value.
RNGCONTROL.PROPTHRESHOLD	0x1028		Adaptive Proportion Test (1024-sample window) Cut-Off value.
RNGCONTROL.STATUS	0x1030		Status register.
RNGCONTROL.INITWAITVAL	0x1034		Initial wait counter value.
RNGCONTROL.DISABLEOSC[n]	0x1038		Disable oscillator rings $\#n \cdot 32$ to $\#((n+1) \cdot 32) - 1$ .
RNGCONTROL.SWOFFTMRVAL	0x1040		Switch off timer value.
RNGCONTROL.CLKDIV	0x1044		Sample clock divider.
RNGCONTROL.AIS31CONF0	0x1048		AIS31 configuration register 0.
RNGCONTROL.AIS31CONF1	0x104C		AIS31 configuration register 1.
RNGCONTROL.AIS31CONF2	0x1050		AIS31 configuration register 2.
RNGCONTROL.AIS31STATUS	0x1054		AIS31 status register.
RNGCONTROL.HWCONFIG	0x1058		Hardware configuration register.
RNGCONTROL.FIFO[n]	0x1080		FIFO data
PK.POINTERS	0x2000		Pointers register.
PK.COMMAND	0x2004		Command register.
PK.CONTROL	0x2008		Command register.
PK.STATUS	0x200C		Status register.
PK.TIMER	0x2014		Timer register.
PK.HWCONFIG	0x2018		Hardware configuration register.
PK.OPSIZE	0x201C		Operand size register.
PK.RAMERRORINJECT	0x2040		RAM error injection register.
PK.RAMERRORSTATUS	0x2044		RAM error status register.