8.5.7.18 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					D C B A	
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
ID	R/W	Field	Value ID	Value	Description	
A	RW W1C	READY			Write '1' to disable interrupt for event READY	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
B	RW W1C	DOWN			Write '1' to disable interrupt for event DOWN	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
C	RW W1C	UP			Write '1' to disable interrupt for event UP	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
D	RW W1C	CROSS			Write '1' to disable interrupt for event CROSS	
			Clear	1	Disable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	

8.5.7.19 INTPEND

Address offset: 0x30C

Pending interrupts

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					D C B A	
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
ID	R/W	Field	Value ID	Value	Description	
A	R	READY			Read pending status of interrupt for event READY	
			NotPending	0	Read: Not pending	
			Pending	1	Read: Pending	
B	R	DOWN			Read pending status of interrupt for event DOWN	
			NotPending	0	Read: Not pending	
			Pending	1	Read: Pending	
C	R	UP			Read pending status of interrupt for event UP	
			NotPending	0	Read: Not pending	
			Pending	1	Read: Pending	
D	R	CROSS			Read pending status of interrupt for event CROSS	
			NotPending	0	Read: Not pending	
			Pending	1	Read: Pending	

NORDIC
SEMICONDUCTOR