

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value		Description																													
A	W	EVENTSB				Write to EVENTSB (if not dirty)																													
A	R	DIRTYBIT				Read EVENTSB Dirty status																													
			CLEAN	0x0		Buffer is clean																													
			DIRTY	0x1		Buffer is dirty																													

8.26.3.63 NORDIC.OUT

Address offset: 0xBC0

GPIO Output value. Real Time Peripherals VIO.

Shares a physical register with MINSTRET (upper 16 bits) and EVENTS (lower 16 bits).

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID																				P O N M L K J I H G F E D C B A															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A-P	RW	PIN[i] (i=0..15)																																	
			LOW	0x0				Pin driver is low																											
			HIGH	0x1				Pin driver is high																											

8.26.3.64 NORDIC.DIR

Address offset: 0xBC1

GPIO pin Direction. Real Time Peripherals VIO.

Shares a physical register with MINSTRETH (upper 16 bits) and PUBLISH (lower 16 bits).

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A-P	RW	PIN[i] (i=0..15)																																	
			INPUT	0x0				Pin is set as input																											
			OUTPUT	0x1				Pin is set as output																											

8.26.3.65 NORDIC.IN

Address offset: 0xBC2

GPIO Input. Real Time Peripherals VIO.

Shares a physical register with TASKS (lower 16 bits).

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A-P	R	PIN[i] (i=0..15)																																	
			LOW	0x0				Pin is Low																											
			HIGH	0x1				Pin is High																											