

8.15.5.21.1.2 PUBLISH_DMA.SEQ[n].READY (n=0..1)

Address offset: 0x1A8 + (n × 0xC)

Publish configuration for event **READY**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID				B																								A				A	A	A	A	A	A	A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value		Description																																
A	RW	CHIDX		[0..255]		DPPI channel that event READY will publish to																																
B	RW	EN																																				
			Disabled	0	Disable publishing																																	
			Enabled	1	Enable publishing																																	

8.15.5.21.1.3 PUBLISH_DMA.SEQ[n].BUSERROR (n=0..1)

Address offset: 0x1AC + (n × 0xC)

Publish configuration for event **BUSERROR**

When this event is generated, the address which caused the error can be read from the **BUSERRORADDRESS** register.

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0						
ID				B																								A				A		A		A		A		A	
Reset 0x00000000				0																																					
ID	R/W	Field	Value ID	Value		Description																																			
A	RW	CHIDX		[0..255]		DPPI channel that event BUSERROR will publish to																																			
B	RW	EN																																							
			Disabled	0	Disable publishing																																				
			Enabled	1	Enable publishing																																				

8.15.5.22 PUBLISH_COMPAREMATCH[n] (n=0..3)

Address offset: 0x1BC + (n × 0x4)

Publish configuration for event **COMPAREMATCH[n]**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0								
ID				B																								A				A		A		A		A		A		A	
Reset 0x00000000				0																																							
ID	R/W	Field	Value ID	Value		Description																																					
A	RW	CHIDX		[0..255]		DPPI channel that event COMPAREMATCH[n] will publish to																																					
B	RW	EN																																									
			Disabled	0	Disable publishing																																						
			Enabled	1	Enable publishing																																						

8.15.5.23 SHORTS

Address offset: 0x200

Shortcuts between local events and tasks