

8.9.5.9 PUBLISH_IN[n] (n=0..7)

Address offset: $0x180 + (n \times 0x4)$

Publish configuration for event IN[n]

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	B																								A	A	A	A	A	A	A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	RW	CHIDX		[0..255]	DPPI channel that event IN[n] will publish to																											
B	RW	EN																														
		Disabled	0	Disable publishing																												
		Enabled	1	Enable publishing																												

8.9.5.10 PUBLISH_PORT[n] (n=0..0)

Publish configuration for events

8.9.5.10.1 PUBLISH_PORT[n].NONSECURE (n=0..0)

Address offset: $0x1C0 + (n \times 0x8)$

Publish configuration for event PORT[n].NONSECURE

8.9.5.10.2 PUBLISH_PORT[n].SECURE (n=0..0)

Address offset: 0x1C4 + (n × 0x8)

Publish configuration for event PORT[n].SECURE