

8.17.14.56 EVENTS_SYNC

Address offset: 0x258

Initial sync detected

MODE=Ble_LR125Kbit, Ble_LR500Kbit, or ieee802154_250Kbit: A possible preamble has been received. However, due to the sporadic reception of noise, this event can be falsely triggered.

For MODE=Nrf_1Mbit, Nrf_2Mbit, Ble_1Mbit, or Ble_2Mbit: A possible preamble and the first two bytes of the address field has been received. The event can be generated falsely also in this mode.

It is also possible that the event is not generated, or not generated before the ADDRESS event.

8.17.14.57 EVENTS CTEPRESENT

Address offset: 0x25C

CTEInfo byte is received

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																															A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value	Description																											
A	RW	EVENTS_CTEPRESENT			CTEInfo byte is received																											
		NotGenerated	0	Event not generated																												
		Generated	1	Event generated																												

8.17.14.58 EVENTS PLLREADY

Address offset: 0x2B0