

Pin	Clock pin	Name	Function	Description	Dedicated function
A1		<b>XC1</b>	Analog input	Connection for 32 MHz crystal	
A2		<b>XC2</b>	Analog input	Connection for 32 MHz crystal	
A3	Yes	<b>P1.12</b>	Digital I/O	General purpose I/O	
		<b>ASI[3]</b>	Digital I/O	TAMPC active shield 3 input	TAMPC
		<b>RADIO[3]</b>	Digital I/O	RADIO DFEGPIO	RADIO
		<b>AIN5</b>	Analog input	Analog input	
A4		<b>DECA</b>	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
A5		<b>DEC'D</b>	Power	0.9 V regulator supply decoupling	
A6		<b>VSS</b>	Power	Ground	
A7		<b>DCC</b>	Power	DC/DC regulator output	
B1		<b>DECRF</b>	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See <a href="#">Reference circuitry</a> on page 889.
B3		<b>P1.09</b>	Digital I/O	General purpose I/O	
		<b>ASO[2]</b>	Digital I/O	TAMPC active shield 2 output	TAMPC
		<b>RADIO[0]</b>	Digital I/O	RADIO DFEGPIO	RADIO
B4		<b>P1.13</b>	Digital I/O	General purpose I/O	
		<b>RADIO[4]</b>	Digital I/O	RADIO DFEGPIO	RADIO
		<b>AIN6</b>	Analog input	Analog input	
B5		<b>P1.14</b>	Digital I/O	General purpose I/O	
		<b>RADIO[5]</b>	Digital I/O	RADIO DFEGPIO	RADIO
		<b>AIN7</b>	Analog input	Analog input	
B6		<b>P1.15</b>	Digital I/O	General purpose I/O	
B7		<b>VDD</b>	Power	Power supply	