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- Erase all
  - Mailbox interface
  - **INFO.PARTNO** and **INFO.HWREVISION** registers

The CTRL-AP peripheral has the following types of registers:

- Peripheral registers – Accessed by a CPU using the APB interface
- Debugger registers – Accessed by an external debugger using the CTRL-AP interface

### 9.6.1 Reset request

The debugger can request the device to perform a reset.

The register **RESET** is used to request the reset. Once the reset is performed, the reset reason is accessible through the **RESETREAS** register. For more information about reset, see [RESET — Reset control](#) on page 101.

### 9.6.2 Erase all

The erase all function lets the debugger trigger an erase of non-volatile memory, user information configuration registers (UICR), secure information configuration region (SICR), RAM, all peripheral settings, and also temporarily removes the access port protection.

To trigger an erase all function, follow the sequence in the following flowchart. After the sequence has completed, the access port protection is removed until the next pin reset, power-on reset, brownout reset, or watchdog timer reset.