11.13.10 Jitter

Symbol	Description	Min.	Typ.	Max.	Units
$t_{DISABLEDJITTER}$	Jitter on DISABLED event relative to END event when shortcut between END and DISABLE is enabled		0.25		µs
$t_{READYJITTER}$	Jitter on READY event relative to TXEN and RXEN task		0.25		µs

11.13.11 IEEE 802.15.4 mode energy detection constants

Symbol	Description	Min.	Typ.	Max.	Units
ED_RSSISCALE	Scaling value when converting between hardware-reported value and dBm	4	4	4	
ED_RSSIOFFS	Offset value when converting between hardware-reported value and dBm	-92	-92	-92	

11.14 REGULATORS Electrical specification

11.14.1 Recommended operating conditions

Symbol	Description	Min.	Typ.	Max.	Units
$V_{DD,POR}$	VDD supply voltage needed during power-on reset.	1.75			V
V_{DD}	VDD supply voltage.	1.7		3.6	V
$V_{DD,EXT}$	VDD supply voltage under extended operating temperature.	1.7		3.4	V

11.14.2 Power-fail comparator

Symbol	Description	Min.	Typ.	Max.	Units
V_{POF}	Voltage level warning thresholds (falling supply voltage). Levels are configurable between min. and max. in increments of 100 mV.	1.7		3.2	V
V_{POFTOL}	Threshold voltage tolerance.	-2		2	%
$V_{POFHYST}$	Threshold voltage hysteresis.	40	50	55	mV
$V_{BOR,OFF}$	Brownout reset voltage range System OFF mode. Brownout only applies to the voltage on VDD.	1.56		1.64	V
$V_{BOR,ON}$	Brownout reset voltage range System ON mode. Brownout only applies to the voltage on VDD.	1.57		1.64	V

11.15 RESET Electrical specification

11.15.1 Startup times

Symbol	Description	Min.	Typ.	Max.	Units
$t_{POR,10us}$	Time measured as time in power-on reset after supply reaches minimum operating voltage, with VDD rise time from 1 µs to 100ms.		0.2	2	ms
t_{PINR}	Reset time when using pin reset, depending on pin capacitance		..		
$t_{PINR,500nF}$	500 nF capacitance at reset pin		..		ms
$t_{PINR,10uF}$	10 µF capacitance at reset pin		..		ms