8.3.5.7 EVENTS_NOTRESOLVED

Address offset: 0x108

Address not resolved

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID						A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	EVENTS_NOTRESOLVED			Address not resolved	
			NotGenerated	0	Event not generated	
			Generated	1	Event generated	

8.3.5.8 EVENTS_ERROR

Address offset: 0x10C

Operation aborted because of a STOP task or due to an error

This event does not generate an interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID						A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	EVENTS_ERROR			Operation aborted because of a STOP task or due to an error	
					This event does not generate an interrupt	
			NotGenerated	0	Event not generated	
			Generated	1	Event generated	

8.3.5.9 PUBLISH_END

Address offset: 0x180

Publish configuration for event END

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event END will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.3.5.10 PUBLISH_RESOLVED

Address offset: 0x184

Publish configuration for event RESOLVED

NORDIC
SEMICONDUCTOR