## 7.8.1.6.8 ENABLE

Address offset: 0x400

Enable CRACEN peripheral modules.

Each module of CRACEN can be enabled individually. When any of these modules are not in use, it can be disabled to save power. The module you want to use must first be enabled. Any ongoing crypto operations will be finished even if the module is disabled during the operation.

| Bit number | | | | | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 |
|---|---|---|---|---|---|
| ID | | | | | C B A |
| **Reset 0x00000000** | | | | | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| ID | R/W | Field | Value ID | Value | Description |
| A | RW | CRYPTOMASTER | | | Enable cryptomaster |
| | | | Disabled | 0 | Cryptomaster disabled. |
| | | | Enabled | 1 | Cryptomaster enabled. |
| B | RW | RNG | | | Enable RNG |
| | | | Disabled | 0 | RNG disabled. |
| | | | Enabled | 1 | RNG enabled. |
| C | RW | PKEIKG | | | Enable PKE and IKG |
| | | | Disabled | 0 | PKE and IKG disabled. |
| | | | Enabled | 1 | PKE and IKG enabled. |

## 7.8.1.6.9 SEEDVALID

Address offset: 0x404

Marks the SEED register as valid.

This register must be written after the SEED register is written, and before the IKG is started.

| Bit number | | | | | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 |
|---|---|---|---|---|---|
| ID | | | | | A |
| **Reset 0x00000000** | | | | | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| ID | R/W | Field | Value ID | Value | Description |
| A | RW | VALID | | | Marks the SEED as valid |
| | | | Disabled | 0 | Valid disabled. |
| | | | Enabled | 1 | Valid enabled. |

## 7.8.1.6.10 SEED[n] (n=0..11)

Address offset: 0x410 + (n × 0x4)

Seed word [n] for symmetric and asymmetric key generation.

This register is only writable from KMU.

| Bit number | | | | | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0 |
|---|---|---|---|---|---|
| ID | | | | | A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A |
| **Reset 0x00000000** | | | | | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
| ID | R/W | Field | Value ID | Value | Description |
| A | W | VAL | | | Seed value |

## 7.8.1.6.11 SEEDLOCK

Address offset: 0x440