

Pin	Clock pin	Name	Function	Description	Dedicated function
25		<b>SWDIO</b>	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
26		<b>SWDCLK</b>	Debug	Serial wire clock. Input with on-chip pull-down.	
27		<b>P0 . 02</b>	Digital I/O	General purpose I/O	
28	Yes	<b>P0 . 03</b>	Digital I/O Digital I/O	General purpose I/O GRTC PWM	GRTC
29	Yes	<b>P0 . 04</b>	Digital I/O Digital I/O	General purpose I/O GRTC CLKOUT32K	GRTC
30		<b>nRESET</b>	Reset	Pin reset with on-chip pull-up	
31		<b>ANT</b>	RF	Single ended radio antenna connection	See <a href="#">Reference circuitry</a> on page 889 for guidelines on how to ensure good RF performance
32		<b>VSS_PA</b>	Power	Ground (radio supply)	
33		<b>DECRF</b>	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See <a href="#">Reference circuitry</a> on page 889.
34		<b>XC1</b>	Analog input	Connection for 32 MHz crystal	
35		<b>XC2</b>	Analog input	Connection for 32 MHz crystal	
36		<b>VDD</b>	Power	Power supply	
37		<b>P1 . 09</b> <b>ASO [2]</b> <b>RADIO [0]</b>	Digital I/O Digital I/O Digital I/O	General purpose I/O TAMPC active shield 2 output RADIO DFEGPIO	TAMPC RADIO