ID	Base address	Instance	TrustZone			Split access	Description
			Map	Att	DMA		
262	0x50106000	LPCOMP : S	US	S	NA	No	Low-power comparator LPCOMP
	0x40106000	LPCOMP : NS					
264	0x50108000	WDT30	HF	S	NA	No	Watchdog timer WDT30
265	0x50109000	WDT31 : S	US	S	NA	No	Watchdog timer WDT31
	0x40109000	WDT31 : NS					
266	0x5010A000	P0 : S	US	S	NA	Yes	General purpose input and output, port P0
	0x4010A000	P0 : NS					
268	0x5010C000	GPIOTE30 : S	US	S	NA	Yes	4 channels and 2 interrupts for GPIO port P0
	0x4010C000	GPIOTE30 : NS					GPIO tasks and events GPIOTE30
270	0x5010E000	CLOCK : S	US	S	NA	No	Clock control
	0x4010E000	CLOCK : NS					
270	0x5010E000	POWER : S	US	S	NA	No	Power control
	0x4010E000	POWER : NS					
270	0x5010E000	RESET : S	US	S	NA	No	Reset status
	0x4010E000	RESET : NS					
288	0x50120000	OSCILLATORS : S	US	S	NA	No	Oscillator control
	0x40120000	OSCILLATORS : NS					
288	0x50120000	REGULATORS : S	US	S	NA	No	Regulator control
	0x40120000	REGULATORS : NS					
N/A	0x00FFC000	FICR	HF	NS	NA	No	Factory information configuration
N/A	0x00FFD000	UICR	HF	S	NA	No	User information configuration
N/A	0x00FFE000	SICR	HF	S	NA	No	Secure information configuration region
N/A	0x51800000	CRACENCORE	HF	S	NSA	No	CRACEN core

Table 11: Instantiation table

NORDIC
SEMICONDUCTOR