



Figure 8-13. UVCC flow chart

Note

If an over-voltage or short circuit event takes place while the device is in fail-safe mode due to a under-voltage event on V_{CC} , the device will behave as shown in the OV_{CC} and VCC_{SC} flow charts.

For an over-voltage event, OV_{CC} , the device turns off the V_{CC} LDO, and transitions to either sleep mode, fail-safe mode disabled, or fail-safe mode. When a wake event takes place, the V_{CC} LDO is turned on for t_{LDOON} to determine if the over-voltage is still present. If cleared, the device enters restart mode from either sleep or fail-safe modes. When in fail-safe mode, if the over-voltage has not cleared when the wake event takes place the device transitions to sleep mode.