

Peripheral	Signal	Clock pin required
SPIM/SPIS	SDO	
	SDI	
	SCK	Yes
	CSN	
TWIM/TWIS	DCX	
	SCL	Yes
PDM	SDA	
	DIN	
I2S	CLK	Yes
	MCK	Yes
	LRCK	
	SCK	Yes
TRACE	SDIN	
	SDOUT	
	TRACEDATA[]	
GRTC	TRACECLK	Yes (dedicated pin)
	CLKOUT32K	Yes (dedicated pin)
	PWMOUT	Yes (dedicated pin)
	CLKOUTFAST	Yes (dedicated pin)

Table 77: List of peripheral signals and clock pin requirement

10.1.3 QFN40 (QDAA) package pin assignments

The QFN40 pin assignment figure and table describe the pinouts for this variant of the device.

Pins that can be used as clock signals are shown in red in the figure. For more information about clock pins, see [Clock pins](#) on page 860.