

## Register overview

Register	Offset	TZ	Description
SYSPWRUPREQ	0x400		System power-up request
DBGPWRUPREQ	0x404		Debug power-up request
ENABLE	0x500		Enable debug domain and aquire selected GPIOs
TRACEPORTSPEED	0x518		Trace port speed
			This register is retained.

## 9.7.1.1 SYSPWRUPREQ

Address offset: 0x400

## System power-up request

### 9.7.1.2 DBGPWRUPREQ

Address offset: 0x404

## Debug power-up request

### 9.7.1.3 ENABLE

Address offset: 0x500

Enable debug domain and aquire selected GPIOs

#### 9.7.1.4 TRACEPORTSPEED (Retained)

Address offset: 0x518