

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	H	H	H	H	H	H	H	H	H	G	G	F	F	E	D	D	D	D	D	D	D	C	B	B	B	B	B	A				
Reset 0x3FC30000	0	0	1	1	1	1	1	1	1	1	0	0	0	1	1	0																
ID	R/W	Field	Value ID	Value	Description																											
F	R	TRIG	EdgeTriggered	1	Trigger type and polarity for each interrupt input. Interrupts are edge-triggered																											
G	R	MODE	MachineMode	3	Privilege mode. Machine mode																											
H	RW	PRIORITY	PRIOLEVEL0	0x3F	Interrupt priority level Priority level 0																											
			PRIOLEVEL1	0x7F	Priority level 1																											
			PRIOLEVEL2	0xBF	Priority level 2																											
			PRIOLEVEL3	0xFF	Priority level 3																											

8.26.3 Registers

Instances

Instance	Domain	Base address	Description
VPRCSR	FLPR		VPR CSR registers