

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																	
ID				B																								A				A	A	A	A	A	A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																																	
ID	R/W	Field	Value ID	Value	Description																																
A	RW	CHIDX		[0..255]	DPPI channel that task <b>START</b> will subscribe to																																
B	RW	EN																																			
			Disabled	0	Disable subscription																																
			Enabled	1	Enable subscription																																

#### 8.11.10.4 SUBSCRIBE\_STOP

Address offset: 0x084

Subscribe configuration for task **STOP**

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																	
ID				B																								A				A	A	A	A	A	A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																																	
ID	R/W	Field	Value ID	Value		Description																															
A	RW	CHIDX		[0..255]		DPPI channel that task <span>STOP</span> will subscribe to																															
B	RW	EN																																			
			Disabled	0	Disable subscription																																
			Enabled	1	Enable subscription																																

#### 8.11.10.5 EVENTS\_RXPTRUPD

Address offset: 0x104

The RXD.PTR register has been copied to internal double-buffers. When the I2S module is started and RX is enabled, this event will be generated for every  $\text{ceil}(\text{RXTXD.MAXCNT}/4)$  words received on the SDIN pin.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	EVENTS_RXPTRUPD			The RXD.PTR register has been copied to internal double-buffers. When the I2S module is started and RX is enabled, this event will be generated for every ceil(RXTXD.MAXCNT/4) words received on the SDIN pin.																														
			NotGenerated	0	Event not generated																														
			Generated	1	Event generated																														

#### 8.11.10.6 EVENTS\_STOPPED

Address offset: 0x108

I2S transfer stopped.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	EVENTS_STOPPED			I2S transfer stopped.																														
			NotGenerated	0	Event not generated																														
			Generated	1	Event generated																														