

8.23.10.27 FREQUENCY

Address offset: 0x524

TWI frequency. Accuracy depends on the HFCLK source selected.

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|-------------------------|-----|-----------|------------|-----------|----------------------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| ID | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | A | | |
| Reset 0x04000000 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| ID | R/W | Field | Value ID | Value | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | FREQUENCY | | | TWI master clock frequency | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | K100 | 0x01980000 | 100 kbps | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | K250 | 0x04000000 | 250 kbps | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | K400 | 0x06400000 | 400 kbps | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | K1000 | 0x0FF00000 | 1000 kbps | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.23.10.28 ADDRESS

Address offset: 0x588

Address used in the TWI transfer

8.23.10.29 PSEL.SCL

Address offset: 0x600

Pin select for SCL signal

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|-------------------------|-----|---------|--------------|---------|-------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| ID | C | | | | | | | | | | | | | | | | | | | | | | | | B | B | A | A | A | A | A | |
| Reset 0xFFFFFFFF | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| ID | R/W | Field | Value ID | Value | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | PIN | | [0..31] | Pin number | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| B | RW | PORT | | [0..7] | Port number | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| C | RW | CONNECT | | | Connection | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Disconnected | 1 | Disconnect | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Connected | 0 | Connect | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.23.10.30 PSEL.SDA

Address offset: 0x604

Pin select for SDA signal