

## Register overview

Register	Offset	TZ	Description
<a href="#">SYSPWRUPREQ</a>	0x400		System power-up request
<a href="#">DBGPWRUPREQ</a>	0x404		Debug power-up request
<a href="#">ENABLE</a>	0x500		Enable debug domain and aquire selected GPIOs
<a href="#">TRACEPORTSPEED</a>	0x518		Trace port speed
			This register is retained.

### 9.7.1.1 SYSPWRUPREQ

Address offset: 0x400

System power-up request

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				A																															
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value		Description																													
A	RW	ACTIVE																																	
			NotActive	0	Power-up request not active																														
			Active	1	Power-up request active																														

### 9.7.1.2 DBGPWRUPREQ

Address offset: 0x404

Debug power-up request

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
ID				A																																			
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID	Value		Description																																	
A	RW	ACTIVE																																					
			NotActive	0	Power-up request not active																																		
			Active	1	Power-up request active																																		

### 9.7.1.3 ENABLE

Address offset: 0x500

Enable debug domain and aquire selected GPIOs

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	RW	ENABLE																																	
			DISABLED	0				Disable debug domain and release selected GPIOs																											
			ENABLED	1				Enable debug domain and aquire selected GPIOs																											

### 9.7.1.4 TRACEPORTSPEED (Retained)

Address offset: 0x518