

Pin	Clock pin	Name	Function	Description	Dedicated function
		AIN3	Analog input	Analog input	
F1		P0.01	Digital I/O	General purpose I/O	
F2		SWDIO	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
F3		P2.09	Digital I/O	General purpose I/O	
		TRACEDATA[2]	Digital I/O	FLPR.9	FLPR
			Digital I/O	Trace data	Trace
			Digital I/O	SPIM SDI	SPIM00/21
			Digital I/O	SPIS SDI	SPIS00/21
			Digital I/O	UARTE CTS	UARTE00/21
F4	Yes	P2.06	Digital I/O	General purpose I/O	
			Digital I/O	FLPR.6	FLPR
			Digital I/O	SPIM SCK	SPIM00/21
			Digital I/O	SPIS SCK	SPIS00/21
		TRACECLK	Digital I/O	Trace clock	Trace
F5		P2.04	Digital I/O	General purpose I/O	
			Digital I/O	SPIM SDI	SPIM00/20
			Digital I/O	SPIS SDI	SPIS00/20
			Digital I/O	UARTE CTS	UARTE00/20
			Digital I/O	FLPR.2	FLPR
			Digital I/O	QSPI D1	FLPR (QSPI)
F6		P2.02	Digital I/O	General purpose I/O	
			Digital I/O	SPIM SDO	SPIM00/20
			Digital I/O	SPIS SDO	SPIS00/20
			Digital I/O	UARTE TXD	UARTE00/20
			Digital I/O	FLPR.1	FLPR
			Digital I/O	QSPI D0	FLPR (QSPI)
F7	Yes	P1.08	Digital I/O	General purpose I/O	