| Registers | Description |
|---|---|
| UICR.AUXAPPROTECT | Hardware control of AUX-AP debug access. A device reset is required for this configuration to take effect. |
| | `Unprotected` – CPU controls AUX-AP DBGEN, lock disabled. |
| | Other values – AUX-AP DBGEN disabled and locked. |
| TAMPC.PROTECT.AP[0].DBGEN | Software control of AUX-AP debug access. The registers can be locked. |

*Table 73: VPR AUX-AP debug access*

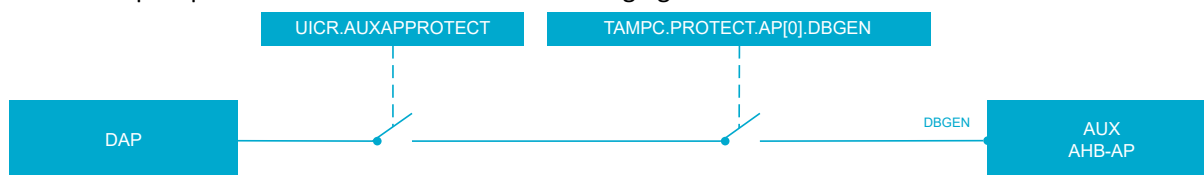The access port protection is illustrated in the following figures.
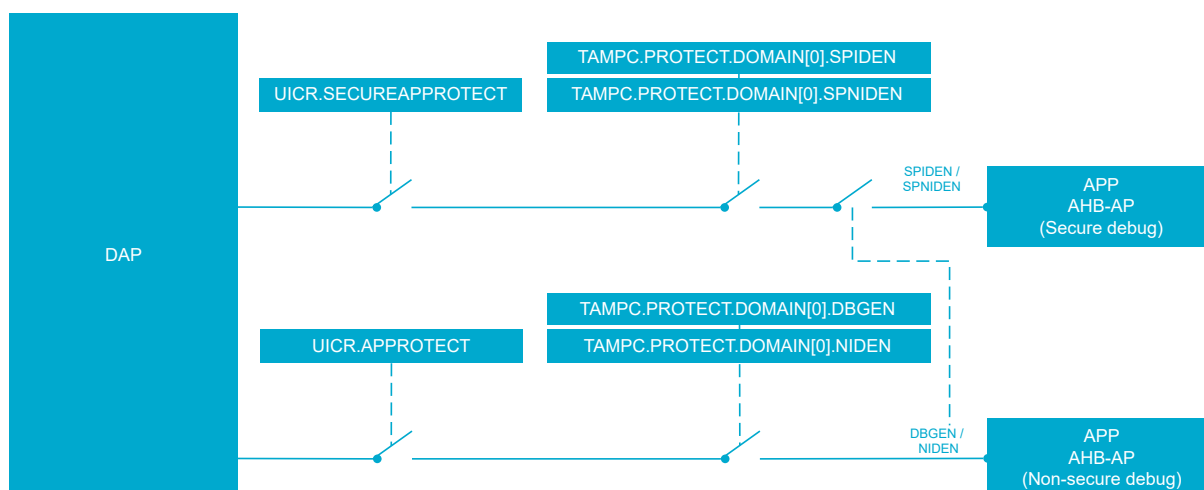


*Figure 165: AUX access port protection overview*



*Figure 166: Arm Cortex-M33 AHB-AP access port protection overview*

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