Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A	W	DIRB			Write to DIRB (if not dirty)
A	R	DIRTYBIT			Read Buffer Dirty status
			CLEAN	0x0	Buffer is clean
			DIRTY	0x1	Buffer is dirty

8.26.3.86 NORDIC.DIROUTBS

Address offset: 0xBDA

Combination of DIRB and OUTB Dirty Status

Reads combination (OR) of DIRB and OUTB dirty status. Writes DIRB and OUTB[15:0] in the same operation (sets dirty status of both buffers). Real Time Peripherals VIO

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A	W	DIROUTB			Write to DIROUTB (if not dirty)
A	R	DIRTYBIT			Read Combination (OR) of DIRB and OUTB Dirty status
			CLEAN	0x0	Buffer is clean
			DIRTY	0x1	Buffer is dirty

8.26.3.87 NORDIC.OUTBD

Address offset: 0xBE0

Concatenation of Buffered GPIO Output and GPIO Output

Addresses OUTB[15:0] and OUT in the same operation (sets OUTB dirty status). Real Time Peripherals VIO

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				B B B B B B B B B B B B B B B B A A A A A A A A A A A A A A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A	W	OUT			GPIO Output Shares a physical register with MINSTRET (upper 16 bits) and EVENTS (lower 16 bits)
B	W	OUTB			Buffered GPIO Output Shares a physical register with MINSTRET (lower 16 bits) and EVENTSB (lower 16 bits).

8.26.3.88 NORDIC.OUTBDTGL

Address offset: 0xBE1

OUTBD Toggle

Toggles bits in OUTB[15:0] and OUT in the same operation (sets dirty status). Real Time Peripherals VIO