

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
			Enabled	1				Read: Enabled																											

### 8.15.5.26 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	RW	STOPPED W1C			Write '1' to disable interrupt for event STOPPED																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
B-C	RW	SEQSTARTED[i] (i=0..1) W1C			Write '1' to disable interrupt for event SEQSTARTED[i]																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
D-E	RW	SEQEND[i] (i=0..1) W1C			Write '1' to disable interrupt for event SEQEND[i]																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
F	RW	PWMPERIODEND W1C			Write '1' to disable interrupt for event PWMPERIODEND																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
G	RW	LOOPSDONE W1C			Write '1' to disable interrupt for event LOOPSDONE																													
					This event triggers after the last SEQ[1] completion of the loop, and only if looping was enabled (LOOP > 0) when the sequence playback was started.																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
	Enabled	1	Read: Enabled																															
H	RW	RAMUNDERFLOW W1C			Write '1' to disable interrupt for event RAMUNDERFLOW																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
I	RW	DMASEQ0END W1C			Write '1' to disable interrupt for event DMASEQ0END																													
			Clear	1	Disable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
J	RW	DMASEQ0READY W1C			Write '1' to disable interrupt for event DMASEQ0READY																													