

#### 4.2.4.1.6 TRIMCNF[n].ADDR (n=0..63)

Address offset:  $0x400 + (n \times 0x8)$

Address of the register which will be written

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A		
<b>Reset OxFFFFFFF</b>	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
ID	R/W	Field	Value ID																								Description					
A	R	Address																									Address					

#### 4.2.4.1.7 TRIMCNF[n].DATA (n=0..63)

Address offset:  $0x404 + (n \times 0x8)$

Data to be written into the register

#### 4.2.4.1.8 NFC.TAGHEADER0

Address offset: 0x600

Default header for NFC Tag. Software can read these values to populate NFCID1\_3RD\_LAST, NFCID1\_2ND\_LAST and NFCID1\_LAST.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	D	D	D	D	D	D	D	C	C	C	C	C	C	C	C	B	B	B	B	B	B	B	A	A	A	A	A	A	A	A		
<b>Reset 0xFFFFF5F</b>	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1	1		
ID	R/W	Field	Value ID	Value	Description																											
A	R	MFGID			Default Manufacturer ID: Nordic Semiconductor ASA has ICM 0x5F																											
B	R	UD1			Unique identifier byte 1																											
C	R	UD2			Unique identifier byte 2																											
D	R	UD3			Unique identifier byte 3																											

#### 4.2.4.1.9 NFC.TAGHEADER1

Address offset: 0x604

Default header for NFC Tag. Software can read these values to populate NFCID1\_3RD\_LAST, NFCID1\_2ND\_LAST and NFCID1\_LAST.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	D	D	D	D	D	D	D	D	C	C	C	C	C	C	C	B	B	B	B	B	B	B	A	A	A	A	A	A	A	A		
<b>Reset OxFFFFFFF</b>	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
ID	R/W	Field	Value ID	Value	Description																											
A-D	R	UDI[i] (i=4..7)		Unique identifier byte i																												

#### 4.2.4.1.10 NFC-TAGHEADER2

Address offset: 0x608