

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
K	RW	ENDTX			Enable or disable interrupt for event ENDTX																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
L	RW	AUTOCOLRESSTARTED			Enable or disable interrupt for event AUTOCOLRESSTARTED																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
M	RW	COLLISION			Enable or disable interrupt for event COLLISION																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
N	RW	SELECTED			Enable or disable interrupt for event SELECTED																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
O	RW	STARTED			Enable or disable interrupt for event STARTED																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														

8.13.14.49 INTENSET

Address offset: 0x304

Enable interrupt

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			O N M L K J I H G F E D C B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	RW	READY W1S			Write '1' to enable interrupt for event READY																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
B	RW	FIELDDETECTED W1S			Write '1' to enable interrupt for event FIELDDETECTED																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
C	RW	FIELDLOST W1S			Write '1' to enable interrupt for event FIELDLOST																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
D	RW	TXFRAMESTART W1S			Write '1' to enable interrupt for event TXFRAMESTART																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
E	RW	TXFRAMEEND W1S			Write '1' to enable interrupt for event TXFRAMEEND																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													