

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				D D D D D D D D C C C C C C C C B B B B B B B B A A A																															
Reset 0x052D0000				0 0 0 0 0 1 0 1 0 0 1 0 1 1 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	CCAMODE			CCA mode of operation																														
			EdMode	0	Energy above threshold																														
					Will report busy whenever energy is detected above CCAEDTHRES																														
			CarrierMode	1	Carrier seen																														
					Will report busy whenever compliant IEEE 802.15.4 signal is seen																														
			CarrierAndEdMode	2	Energy above threshold AND carrier seen																														
		CarrierOrEdMode	3	Energy above threshold OR carrier seen																															
		EdModeTest1	4	Energy above threshold test mode that will abort when first ED measurement over threshold is seen. No averaging.																															
B	RW	CCAEDTHRES			CCA energy busy threshold. Used in all the CCA modes except CarrierMode. Must be converted from IEEE 802.15.4 range by dividing by factor ED_RSSISCALE - similar to EDSAMPLE register																														
C	RW	CCACORRTHRES			CCA correlator busy threshold. Only relevant to CarrierMode, CarrierAndEdMode, and CarrierOrEdMode.																														
D	RW	CCACORRCNT			Limit for occurrences above CCACORRTHRES. When not equal to zero the corrolator based signal detect is enabled.																														

8.17.14.104 DATAWHITE

Address offset: 0x540

Data whitening configuration

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B B B B B B B B B B B B B B																A A A A A A A A A A A A A A A A															
Reset 0x00890040				0 0 0 0 0 0 0 0 0 1 0 0 0 1 0 0 1 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	RW	IV						Whitening initial value																											
								Data whitening initial value.																											
B	RW	POLY						Whitening polynomial																											
								Data whitening polynomial. Bit 0 is always interpreted as 1.																											

8.17.14.105 AUXDATA.CNF[n] (n=0..1)

Address offset: 0x548 + (n × 0x4)

AUXDATA configuration

This register will not be reset by the SOFTRESET task

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				B																								A				A	A	A	A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value				Description																											
A	RW	ACQMODE						Acquisition mode (data from RADIO written to memory)																											
			Rtt	7				Baseband Channel Sounding RTT Data																											
B	RW	DIR						Data acquisition or injection																											
			Acq	0				Peripheral to memory																											
			Inj	1				Memory to peripheral																											