

| Bit number              |     | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0                |          |       |   |
|-------------------------|-----|--|----------|-------|---|
| ID                      |     | A  |          |       |   |
| <b>Reset 0x00000000</b> |     | <b>0 0</b> |          |       |   |
| ID                      | R/W | Field  | Value ID | Value | Description   |
| A                       | W   | TASKS_SENSE  |          |       | Enable NFC sense field mode, change state to sense mode |
|                         |     | Trigger  |          | 1     | Trigger task  |

#### 8.13.14.4 TASKS\_STARTTX

Address offset: 0x00C

Start transmission of an outgoing frame, change state to transmit

| Bit number              |     | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0                |          |       |   |
|-------------------------|-----|--|----------|-------|---|
| ID                      |     | A  |          |       |   |
| <b>Reset 0x00000000</b> |     | <b>0 0</b> |          |       |   |
| ID                      | R/W | Field  | Value ID | Value | Description   |
| A                       | W   | TASKS_STARTTX  |          |       | Start transmission of an outgoing frame, change state to transmit |
|                         |     | Trigger  |          | 1     | Trigger task  |

#### 8.13.14.5 TASKS\_STOPTX

Address offset: 0x010

Stops an issued transmission of a frame

| Bit number              |     | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0                |          |       |   |
|-------------------------|-----|--|----------|-------|---|
| ID                      |     | A  |          |       |   |
| <b>Reset 0x00000000</b> |     | <b>0 0</b> |          |       |   |
| ID                      | R/W | Field  | Value ID | Value | Description                             |
| A                       | W   | TASKS_STOPTX   |          |       | Stops an issued transmission of a frame |
|                         |     | Trigger  |          | 1     | Trigger task                            |

#### 8.13.14.6 TASKS\_ENABLERXDATA

Address offset: 0x01C

Initializes the EasyDMA for receive.

| Bit number              |     | 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0                |          |       |                                      |
|-------------------------|-----|--|----------|-------|--------------------------------------|
| ID                      |     | A  |          |       |                                      |
| <b>Reset 0x00000000</b> |     | <b>0 0</b> |          |       |                                      |
| ID                      | R/W | Field  | Value ID | Value | Description                          |
| A                       | W   | TASKS_ENABLERXDATA   |          |       | Initializes the EasyDMA for receive. |
|                         |     | Trigger  |          | 1     | Trigger task                         |

#### 8.13.14.7 TASKS\_GOIDLE

Address offset: 0x024

Force state machine to IDLE state