	ID = 4 bytes	ID = 7 bytes	ID = 10 bytes
NFCID1.Q			$nfcid1_0$
NFCID1.R			$nfcid1_1$
NFCID1.S			$nfcid1_2$
NFCID1.T		$nfcid1_0$	$nfcid1_3$
NFCID1.U		$nfcid1_1$	$nfcid1_4$
NFCID1.V		$nfcid1_2$	$nfcid1_5$
NFCID1.W	$nfcid1_0$	$nfcid1_3$	$nfcid1_6$
NFCID1.X	$nfcid1_1$	$nfcid1_4$	$nfcid1_7$
NFCID1.Y	$nfcid1_2$	$nfcid1_5$	$nfcid1_8$
NFCID1.Z	$nfcid1_3$	$nfcid1_6$	$nfcid1_9$

Table 45: NFCID1 byte allocation (top sent first on air)

The hardware implementation can handle the states from IDLE to ACTIVE_A automatically as defined in the *NFC Forum, NFC Activity Technical Specification*, and the other states are to be handled by software. The software keeps track of the state through events. The collision resolution will trigger an AUTOCOLRESSTARTED event when it has started. Reaching the ACTIVE_A state is indicated by the SELECTED event.

If collision resolution fails, a COLLISION event is triggered. Note that errors occurring during automatic collision resolution may also cause ERROR and/or RXERROR events to be generated. Other events may also get generated. It is recommended that the software ignores any event except COLLISION, SELECTED and FIELDLOST during automatic collision resolution. Software shall also make sure that any unwanted SHORT or PPI shortcut is disabled during automatic collision resolution.

The automatic collision resolution will be restarted, if the packets are received with CRC or parity errors while in ACTIVE_A state. The automatic collision resolution feature can be disabled while in ACTIVE_A state to avoid this.

The SLP_REQ is automatically handled by the NFCT peripheral when the automatic collision resolution is enabled. However, this results in an ERROR event (with FRAMEDELAYTIMEOUT cause in ERRORSTATUS) since the SLP_REQ has no response. This error must be ignored until the SELECTED event is triggered and this error should be cleared by the software when the SELECTED event is triggered.

8.13.9 Antenna interface

In ACTIVATED state, an amplitude regulator will adjust the voltage swing on the antenna pins to a value that is within the V_{swing} limit.

Refer to NFCT Electrical Specification on page 901.

8.13.10 NFCT antenna recommendations

The NFCT antenna coil must be connected differential between `NFC1` and `NFC2` pins of the device.

Two external capacitors should be used to tune the resonance of the antenna circuit to 13.56 MHz.