

Pin	Clock pin	Name	Function	Description	Dedicated function
25		SWDIO	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
26		SWDCLK	Debug	Serial wire clock. Input with on-chip pull-down.	
27		P0.02	Digital I/O	General purpose I/O	
28	Yes	P0.03	Digital I/O	General purpose I/O	
			Digital I/O	GRTC PWM	GRTC
29	Yes	P0.04	Digital I/O	General purpose I/O	
			Digital I/O	GRTC CLKOUT32K	GRTC
30		nRESET	Reset	Pin reset with on-chip pull-up	
31		ANT	RF	Single ended radio antenna connection	See Reference circuitry on page 889 for guidelines on how to ensure good RF performance
32		VSS_PA	Power	Ground (radio supply)	
33		DECRF	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See Reference circuitry on page 889 .
34		XC1	Analog input	Connection for 32 MHz crystal	
35		XC2	Analog input	Connection for 32 MHz crystal	
36		VDD	Power	Power supply	
37		P1.09	Digital I/O	General purpose I/O	
		ASO[2]	Digital I/O	TAMPC active shield 2 output	TAMPC
		RADIO[0]	Digital I/O	RADIO DFEGPIO	RADIO