

Pin	Clock pin	Name	Function	Description	Dedicated function
		EXTREF	Digital I/O Analog input	GRTC CLKOUTFAST External reference for SAADC	
G1		P0.00	Digital I/O	General purpose I/O	
G2		P2.10 TRACEDATA[3]	Digital I/O	General purpose I/O	
			Digital I/O	FLPR.10	FLPR
			Digital I/O	Trace data	Trace
			Digital I/O	SPIM CSN	SPIM00/21
			Digital I/O	SPIS CSN	SPIS00/21
			Digital I/O	UARTE RTS	UARTE00/21
G3		VDD	Power	Power supply	
G4		VSS	Power	Ground	
G5		P2.05	Digital I/O	General purpose I/O	
			Digital I/O	SPIM CSN	SPIM00/20
			Digital I/O	SPIS CSN	SPIS00/20
			Digital I/O	UARTE RTS	UARTE00/20
			Digital I/O	FLPR.5	FLPR
			Digital I/O	QSPI CSN	FLPR (QSPI)
G6		P2.00	Digital I/O	General purpose I/O	
			Digital I/O	SPIM DCX	SPIM00/20
			Digital I/O	UARTE RXD	UARTE00/20
			Digital I/O	FLPR.4	FLPR
			Digital I/O	QSPI D3	FLPR (QSPI)
G7	Yes	P2.01	Digital I/O	General purpose I/O	
			Digital I/O	SPIM SCK	SPIM00/20
			Digital I/O	SPIS SCK	SPIS00/20
			Digital I/O	FLPR.0	FLPR
			Digital I/O	QSPI SCK	FLPR (QSPI)

Table 81: CSP47 pin assignments

For the device to function properly, the exposed die pad (pin 49) must be connected to ground (**VSS**, pins 32 and 44).