

## 8.25.13 Registers

### Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
UARTE00 : S	GLOBAL	0x5004A000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE00
UARTE00 : NS		0x4004A000					
UARTE20 : S	GLOBAL	0x500C6000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE20
UARTE20 : NS		0x400C6000					
UARTE21 : S	GLOBAL	0x500C7000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE21
UARTE21 : NS		0x400C7000					
UARTE22 : S	GLOBAL	0x500C8000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE22
UARTE22 : NS		0x400C8000					
UARTE30 : S	GLOBAL	0x50104000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE30
UARTE30 : NS		0x40104000					

### Configuration

Instance	Domain	Configuration
UARTE00 : S	GLOBAL	Use dedicated pins on GPIO port P2
UARTE00 : NS		The core frequency scales with the CPU frequency, see <a href="#">PLL.FREQ (Retained)</a> on page 91
UARTE20 : S	GLOBAL	Supports up to 4 Mbps
UARTE20 : NS		Timeout interrupt is included.
UARTE21 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE21 : NS		Peripheral clock frequency is 128 MHz.
UARTE30 : S	GLOBAL	Use GPIO port P1, or dedicated pins on P2
UARTE30 : NS		Timeout interrupt is included.
UARTE22 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE22 : NS		Peripheral clock frequency is 16 MHz.
UARTE21 : S	GLOBAL	Use GPIO port P1
UARTE21 : NS		Timeout interrupt is included.
UARTE20 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE20 : NS		Peripheral clock frequency is 16 MHz.
UARTE19 : S	GLOBAL	Use GPIO port P0
UARTE19 : NS		Timeout interrupt is included.
UARTE18 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE18 : NS		Peripheral clock frequency is 16 MHz.
UARTE17 : S	GLOBAL	Use GPIO port P0
UARTE17 : NS		Timeout interrupt is included.
UARTE16 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE16 : NS		Peripheral clock frequency is 16 MHz.
UARTE15 : S	GLOBAL	Use GPIO port P0
UARTE15 : NS		Timeout interrupt is included.
UARTE14 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE14 : NS		Peripheral clock frequency is 16 MHz.
UARTE13 : S	GLOBAL	Use GPIO port P0
UARTE13 : NS		Timeout interrupt is included.
UARTE12 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE12 : NS		Peripheral clock frequency is 16 MHz.
UARTE11 : S	GLOBAL	Use GPIO port P0
UARTE11 : NS		Timeout interrupt is included.
UARTE10 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE10 : NS		Peripheral clock frequency is 16 MHz.
UARTE9 : S	GLOBAL	Use GPIO port P0
UARTE9 : NS		Timeout interrupt is included.
UARTE8 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE8 : NS		Peripheral clock frequency is 16 MHz.
UARTE7 : S	GLOBAL	Use GPIO port P0
UARTE7 : NS		Timeout interrupt is included.
UARTE6 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE6 : NS		Peripheral clock frequency is 16 MHz.
UARTE5 : S	GLOBAL	Use GPIO port P0
UARTE5 : NS		Timeout interrupt is included.
UARTE4 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE4 : NS		Peripheral clock frequency is 16 MHz.
UARTE3 : S	GLOBAL	Use GPIO port P0
UARTE3 : NS		Timeout interrupt is included.
UARTE2 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE2 : NS		Peripheral clock frequency is 16 MHz.
UARTE1 : S	GLOBAL	Use GPIO port P0
UARTE1 : NS		Timeout interrupt is included.
UARTE0 : S	GLOBAL	Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE0 : NS		Peripheral clock frequency is 16 MHz.