

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
ID																																				B	A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																																
A	RW	POWER			Indicates ETM is powered up																																
			NotPoweredUp	0	ETM is not powered up. All registers are not accessible.																																
			PoweredUp	1	ETM is powered up. All registers are accessible.																																
B	RW	STICKYPD			Sticky power down state.																																
					This bit is set to 1 when power to the ETM registers is removed, to indicate that programming state has been lost. It is cleared after a read of the TRCPDSR																																
			NotPoweredDown	0	Trace register power has not been removed since the TRCPDSR was last read.																																
			PoweredDown	1	Trace register power has been removed since the TRCPDSR was last read.																																

9.8.1.34 TRCITATBIDR

Address offset: 0xEE4

Sets the state of output pins.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				G F E D C B A																															
Reset 0x00000000				0 0																															

9.8.1.35 TRCITIATBINR

Address offset: 0xEF4

Reads the state of the input pins.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	RW	ATVALID						Returns the value of the ATVALIDMI input pin.																											
B	RW	AFREADY						Returns the value of the AFREADYMI input pin.																											

9.8.1.36 TRCITIATBOUTr

Address offset: 0xEFC

Sets the state of the output pins.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B A																															
Reset 0x00000000				0 0																															