

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B																								A A A A A A A A							
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value		Description																													
A	RW	CHIDX		[0..255]		DPPI channel that event <a href="#">PORT[n].SECURE</a> will publish to																													
B	RW	EN																																	
			Disabled	0	Disable publishing																														
			Enabled	1	Enable publishing																														

### 8.9.5.11 INTENSET0

Address offset: 0x304

Enable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				J I																H G F E D C B A															
Reset 0x00000000				0 0																															
ID	R/W	TZ	Field	Value ID	Value	Description																													
A-H	RW	TZ	IN[i] (i=0..7)			Write '1' to enable interrupt for event <a href="#">IN[i]</a>																													
				W1S																															
				Set	1	Enable																													
				Disabled	0	Read: Disabled																													
				Enabled	1	Read: Enabled																													
I	RW	NS	PORTONONSECURE			Write '1' to enable interrupt for event <a href="#">PORTONONSECURE</a>																													
				W1S																															
				Set	1	Enable																													
				Disabled	0	Read: Disabled																													
				Enabled	1	Read: Enabled																													
J	RW	S	PORTOSECURE			Write '1' to enable interrupt for event <a href="#">PORTOSECURE</a>																													
				W1S																															
				Set	1	Enable																													
				Disabled	0	Read: Disabled																													
				Enabled	1	Read: Enabled																													

### 8.9.5.12 INTENCLR0

Address offset: 0x308

Disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				J I																H G F E D C B A															
Reset 0x00000000				0 0																															
ID	R/W	TZ	Field	Value ID	Value	Description																													
A-H	RW	TZ	IN[i] (i=0..7)			Write '1' to disable interrupt for event <a href="#">IN[i]</a>																													
				W1C																															
				Clear	1	Disable																													
				Disabled	0	Read: Disabled																													
				Enabled	1	Read: Enabled																													
I	RW	NS	PORTONONSECURE			Write '1' to disable interrupt for event <a href="#">PORTONONSECURE</a>																													
				W1C																															
				Clear	1	Disable																													
				Disabled	0	Read: Disabled																													
				Enabled	1	Read: Enabled																													