

Register	Offset	TZ	Description
PROFILING.CLEAR	0x418		Clear the profiling counters.
PROFILING.HIT	0x41C		The cache hit counter for cache region.
PROFILING.MISS	0x420		The cache miss counter for cache region.
PROFILING.LMISS	0x424		The cache line miss counter for cache region.
PROFILING.READS	0x428		Number of reads for cache region.
PROFILING.WRITES	0x42C		Number of writes for cache region.
DEBUGLOCK	0x430		Lock debug mode.
WRITELOCK	0x434		Lock cache updates.

#### 4.2.3.4.1 TASKS\_INVALIDATECACHE

Address offset: 0x008

Invalidate the cache.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	W	TASKS_INVALIDATECACHE			Invalidate the cache.																														
					The <b>STATUS</b> is updated for this task. This task can be triggered only when the <b>STATUS</b> is ready.																														
Trigger				1	Trigger task																														

#### 4.2.3.4.2 TASKS\_INVALIDATELINE

Address offset: 0x014

Invalidate the line.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	W	TASKS_INVALIDATELINE			Invalidate the line.																														
					The <b>STATUS</b> is updated for this task. This task can be triggered only when the <b>STATUS</b> is ready.																														
Trigger			1	Trigger task																															

#### 4.2.3.4.3 TASKS\_ERASE

Address offset: 0x020

Erase the cache.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.