

8.25.13 Registers

Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
UARTE00 : S	GLOBAL	0x5004A000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE00
UARTE00 : NS		0x4004A000					
UARTE20 : S	GLOBAL	0x500C6000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE20
UARTE20 : NS		0x400C6000					
UARTE21 : S	GLOBAL	0x500C7000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE21
UARTE21 : NS		0x400C7000					
UARTE22 : S	GLOBAL	0x500C8000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE22
UARTE22 : NS		0x400C8000					
UARTE30 : S	GLOBAL	0x50104000	US	S	SA	No	Universal asynchronous receiver/transmitter UARTE30
UARTE30 : NS		0x40104000					

Configuration

Instance	Domain	Configuration
UARTE00 : S UARTE00 : NS	GLOBAL	Use dedicated pins on GPIO port P2
		The core frequency scales with the CPU frequency, see PLL.FREQ (Retained) on page 91
		Supports up to 4 Mbps
		Timeout interrupt is included.
		Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
UARTE20 : S UARTE20 : NS	GLOBAL	Peripheral clock frequency is 128 MHz.
		Use GPIO port P1, or dedicated pins on P2
		Timeout interrupt is included.
		Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
		Peripheral clock frequency is 16 MHz.
UARTE21 : S UARTE21 : NS	GLOBAL	Use GPIO port P1, or dedicated pins on P2
		Timeout interrupt is included.
		Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
		Peripheral clock frequency is 16 MHz.
		Use GPIO port P1
UARTE22 : S UARTE22 : NS	GLOBAL	Timeout interrupt is included.
		Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
		Peripheral clock frequency is 16 MHz.
		Use GPIO port P0
		Use GPIO port P0
UARTE30 : S UARTE30 : NS	GLOBAL	Timeout interrupt is included.
		Supports data frame sizes 4, 5, 6, 7, 8, and 9 bits.
		Peripheral clock frequency is 16 MHz.
		Use GPIO port P0
		Use GPIO port P0