

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A																															
<b>Reset 0x00000000</b>	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value	Description																											
A	RW	ENABLE																														
		Disabled	0	Disable																												
		Enabled	1	Enable																												

#### **8.19.8.40 DMA.RX.BUSERROVERRIDE**

Address offset: 0x720

Address of transaction that generated the last BUSERROR event.

#### 8.19.8.41 DMA.RX.MATCH

Registers to control the behavior of the pattern matcher engine

### 8.19.8.41.1 DMA.RX.MATCH.CONFIG

Address offset: 0x724

## Configure individual match events

#### 8.19.8.41.2 DMA.RX.MATCH.CANDIDATE[n] (n=0..3)

Address offset: 0x728 + (n × 0x4)

The data to look for - any match will trigger the MATCH[n] event, if enabled.