

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
C	RW	FPUOFC W1S	Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
			Write '1' to enable interrupt for event <a href="#">FPUOFC</a>																																
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
D	RW	FPUUFC W1S	Enabled	1	Read: Enabled																														
			Write '1' to enable interrupt for event <a href="#">FPUUFC</a>																																
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
E	RW	FPUIXC W1S	Write '1' to enable interrupt for event <a href="#">FPUIXC</a>																																
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
			F	RW	FPUIDC W1S	Write '1' to enable interrupt for event <a href="#">FPUIDC</a>																													
Set	1	Enable																																	
Disabled	0	Read: Disabled																																	
Enabled	1	Read: Enabled																																	

#### 4.1.2.1.9 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	FPUIOC W1C			Write '1' to disable interrupt for event <a href="#">FPUIOC</a>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	FPUDZC W1C			Write '1' to disable interrupt for event <a href="#">FPUDZC</a>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
C	RW	FPUOFC W1C			Write '1' to disable interrupt for event <a href="#">FPUOFC</a>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
D	RW	FPUUFC W1C			Write '1' to disable interrupt for event <a href="#">FPUUFC</a>																														