

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	END W1C			Write '1' to disable interrupt for event <span>END</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	ERROR W1C			Write '1' to disable interrupt for event <span>ERROR</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														

### 8.6.3.11 ERRORSTATUS

Address offset: 0x400

Error status

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	R	ERRORSTATUS			Error status when the ERROR event is generated																														
			NoError	0	No errors have occurred																														
			PrematureInptrEnd	1	End of INPTR job list before data structure was read.																														
			PrematureOutptrEnd2		End of OUTPTR job list before data structure was read.																														
			EncryptionTooSlow	3	Encryption aborted due to higher priority peripheral requesting or using the AES module.																														
					This enumerator is deprecated.																														
			Aborted	3	Encryption aborted due to higher priority peripheral requesting or using the AES module.																														
			DmaError	4	Bus error during DMA access.																														

### 8.6.3.12 KEY.VALUE[n] (n=0..3)

Address offset: 0x510 + (n × 0x4)

128-bit AES key

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	W	VALUE						AES 128-bit key value, bits (32*(n+1))-1 : (32*n)																											

### 8.6.3.13 IN

IN EasyDMA channel

#### 8.6.3.13.1 IN.PTR

Address offset: 0x530