

Pin	Clock pin	Name	Function	Description	Dedicated function
28		DECRF	Power	0.9 V regulator supply decoupling	Must be connected to DECA. See Reference circuitry on page 889.
29		XC1	Analog input	Connection for 32 MHz crystal	
30		XC2	Analog input	Connection for 32 MHz crystal	
31		VDD	Power	Power supply	
32	Yes	P1.11 ASO[3] RADIO[2] AIN4	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 output RADIO DFEGPIO Analog input	TAMPC RADIO
33	Yes	P1.12 ASI[3] RADIO[3] AIN5	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 input RADIO DFEGPIO Analog input	TAMPC RADIO
34		P1.13 RADIO[4] AIN6	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
35		P1.14 RADIO[5] AIN7	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
36		DECA	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
37		VSS	Power	Ground	
38		DECD	Power	0.9 V regulator supply decoupling	
39		DCC	Power	DC/DC regulator output	