Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A
Reset 0xFFFFFFFF	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

ID	R/W	Field	Value ID	Value	Description
A	RW1	PALL			
			Protected	0x50FA50FA	Erase protection is enabled and the signal protector is locked.

4.2.9.1.4.2 ERASEPROTECT[n].PROTECT1 (n=0..0)

Address offset: 0x7C + (n × 0x20)

Erase protection

Any other value than `Protected` will leave the TAMPC PROTECT.ERASEPROTECT signal protector unlocked, so that CPU can control its value.

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A
Reset 0xFFFFFFFF	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

ID	R/W	Field	Value ID	Value	Description
A	RW1	PALL			
			Protected	0x50FA50FA	Erase protection is enabled and the signal protector is locked.

4.2.9.1.5 BOOTCONF

Address offset: 0x080

Immutable boot region configuration.

If this register is not equal to 0xFFFFFFFF, RRAMC applies these settings to form the immutable boot region.

For an immutable bootloader, recommended value is READ, EXECUTE, SECURE, WRITEONCE, LOCK, and SIZE. READ permission is needed for the CPU to read constants and instructions.

Unused bits (unused fields) must be set to zero.

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	G G G G F E D C B A
Reset 0xFFFFFFFF	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

ID	R/W	Field	Value ID	Value	Description
A	RW1	READ			Read access. Must be enabled in order for the Arm Cortex CPU to start executing from RRAM.
			NotAllowed	0	Reading from the region is not allowed.
			Allowed	1	Reading from the region is allowed
B	RW1	WRITE			Write access
			NotAllowed	0	Writing to the region is not allowed
			Allowed	1	Writing to the region is allowed
C	RW1	EXECUTE			Execute access
			NotAllowed	0	Executing code from the region is not allowed
			Allowed	1	Executing code from the region is allowed
D	RW1	SECURE			Secure access
			NonSecure	0	Both secure and non-secure access to region is allowed
			Secure	1	Only secure access to region is allowed
E	RW1	WRITEONCE			Write-once
			Disabled	0	Write-once disabled

NORDIC
SEMICONDUCTOR