Table 8-17. WAKE_PIN_CONFIG1 Register Field Descriptions

Bit	Field	Type	Reset	Description
7-6	WAKE_CONFIG	R/W	00b	Wake pin configuration: Note: Pulse requires more programming 00b = Bi-directional - either edge 01b = Rising edge 10b = Falling edge 11b = Pulse
5-4	WAKE_STAT	R/W0C/H	00b	Wake pin status 00b = No change 01b = Rising edge 10b = Falling edge 11b = Pulse
3-2	WAKE_WIDTH_INVALID	R/W	01b	Pulses less than or equal to these pulses are considered invalid 00b = 5 ms and sets $t_{WAKE\_WIDTH\_MIN}$ to 10 ms 01b = 10 ms and sets $t_{WAKE\_WIDTH\_MIN}$ to 20 ms 10b = 20 ms and sets $t_{WAKE\_WIDTH\_MIN}$ to 40 ms 11b = 40 ms and sets $t_{WAKE\_WIDTH\_MIN}$ to 80 ms
1-0	WAKE_WIDTH_MAX	R/W	00b	Maximum WAKE pin input pulse width to be considered valid. 00b = 750 ms 01b = 1000 ms 10b = 1500 ms 11b = 2000 ms

8.6.8 WAKE_PIN_CONFIG2 Register (Address = 12h) [reset = 2h]

WAKE_PIN_CONFIG2 is shown in Figure 8-62 and described in Table 8-18.

Return to Summary Table.

Device wake configuration register

Figure 8-62. WAKE_PIN_CONFIG2 Register

7	6	5	4	3	2	1	0
WAKE_PULSE_CONFIG	WAKE_SENSE	TWK_CYC_SET	nINT_SEL		RXD_WK_CONFIG	WAKE_LEVEL	
R/W-0b	R/W/H-0b	R/W-0b	R/W-0b		R/W-0b	R/W-10b	

Table 8-18. WAKE_PIN_CONFIG2 Register Field Descriptions

Bit	Field	Type	Reset	Description
7	WAKE_PULSE_CONFIG	R/W	0b	Set WAKE pin expected pulse direction 0b = Low –> High –> Low 1b = High –> Low –> High
6	WAKE_SENSE	R/W/H	0b	WAKE pin configured for static or cyclic sensing wake 0b = Static 1b = Cyclic **Note** When Cyclic sensing is selected and the device goes to fail-safe mode it will automatically change to static sensing. If cyclic sensing is needed it will have to be reprogrammed.