

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																													B	A		
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value		Description																										
A	RW	END				Write '1' to disable interrupt for event END																										
		W1C		Clear	1		Disable																									
				Disabled	0		Read: Disabled																									
				Enabled	1		Read: Enabled																									
B	RW	ERROR				Write '1' to disable interrupt for event ERROR																										
		W1C		Clear	1		Disable																									
				Disabled	0		Read: Disabled																									
				Enabled	1		Read: Enabled																									

8.6.3.11 ERRORSTATUS

Address offset: 0x400

Error status

8.6.3.12 KEY.VALUE[n] (n=0..3)

Address offset: $0x510 + (n \times 0x4)$

128-bit AES key

8.6.3.13 IN

IN EasyDMA channel

8.6.3.13.1 IN.PTR

Address offset: 0x530