

### 7.8.1.7.59 IKG.PKECONTROL

Address offset: 0x301C

PKE Control register.

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID					B A																															
Reset 0x00000000					0 0																															
ID	R/W	Field	Value ID	Value	Description																															
A	W	PKESTART			Start the PKE operation or trigger for Secure mode exit.																															
B	W	CLEARIRQ			Clear the IRQ output.																															

### 7.8.1.7.60 IKG.PKECOMMAND

Address offset: 0x3020

PKE Command register.

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			C C B B B B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	RW	SECUREMODE			Secure mode.																													
			DEACTIVATED	0	It is activated as soon as it is set to 1.It is deactivated when it is set to 0 and PKE_Start is set to 1.																													
			ACTIVATED	1																														
B	RW	SELECTEDKEY			Select Generated Private Key for PKE operation.																													
					This Key Index should be between 0 and g_nb_priv_keys-1.																													
C	RW	OPSEL			Select PKE operation with Isolated Key																													
					Note: Value 3 is reserved.																													
			PUBKEY	0	Public Key Generation																													
			ECDSA	1	ECDSA Signature																													
			PTMUL	2	Point Multiplication																													

### 7.8.1.7.61 IKG.PKESTATUS

Address offset: 0x3024

PKE Status register.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				E D C																												B A			
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	R	ERROR			Error because either Private Keys are not stored or the operation is not defined.																														
B	R	STARTERROR			Error because a new operation is started while the previous one is still busy.																														
C	R	IKGPKBUSY			Busy, set when the operation starts and cleared when the operation is finished.																														
D	R	IRQSTATUS			IRQ, set when the operation is finished and cleared when the CPU writes the bit 1 of PKE_Control Register or a new operation is started.																														
E	R	ERASEBUSY			The PKE Data RAM is being erased.																														