Complex bit access types are encoded to fit into small table cells. Table 8-10 shows the codes that are used for access types in this section.

Table 8-10. Device Access Type Codes

Access Type	Code	Description
Read Type		
R	R	Read
RH	H R	Set or cleared by hardware Read
Write Type		
H	H	Set or cleared by hardware
W	W	Write
W1C	1C W	1 to clear Write
Reset or Default Value		
-n		Value after reset or the default value
Register Array Variables		
i,j,k,l,m,n		When these variables are used in a register name, an offset, or an address, they refer to the value of a register array where the register is part of a group of repeating registers. The register groups form a hierarchical structure and the array is represented with a formula.
y		When this variable is used in a register name, an offset, or an address it refers to the value of a register array.

8.6.1 DEVICE_ID_y Register (Address = 0h + formula) [reset = 0h]

DEVICE_ID_y is shown in Figure 8-55 and described in Table 8-11.

Return to Summary Table.

Device Part Number

Offset = 0h + y; where y = 0h to 7h

Figure 8-55. DEVICE_ID_y Register

7	6	5	4	3	2	1	0
DEVICE_ID							
R-0b							