

8.20.7.28 DMA.RX.AMOUNT

Address offset: 0x70C

Number of bytes transferred in the last transaction, updated after the END event.

Also updated after each MATCH event.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A		
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	R	AMOUNT	[1..0xffff]		Number of bytes transferred in the last transaction. In case of NACK error, includes the NACK'ed byte.																											

8.20.7.29 DMA.RX.TERMINATEONBUSERRO

Address offset: 0x71C

Terminate the transaction if a BUSERROR event is detected.

8.20.7.30 DMA_RX_BUSERRORADDRESS

Address offset: 0x720

Address of transaction that generated the last BUSERROR event.

8.20.7.31 DMA.RX.MATCH

Registers to control the behavior of the pattern matcher engine

8.20.7.31.1 DMA RX MATCH CONFIG

Address offset: 0x724

Configure individual match events