

8.12.4.24 EXTREFSEL

Address offset: 0x50C

External reference select

The external reference pin is selected based on EXTREFSEL.PORT

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|-------------------------|-----|-------|----------|-------|--------------------------------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| ID | B | B | B | B | | | | | | | | | | | | | | | | | | | | | A | A | A | A | A | | | |
| Reset 0x00000000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| ID | R/W | Field | Value ID | Value | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | PIN | | | External analog reference pin select | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| B | RW | PORT | | | GPIO Port selection | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.12.4.25 ANADETECT

Address offset: 0x520

Analog detect configuration

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | | | |
|------------------|-----|-----------|----------|--|-----------------------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|---|---|---|--|---|---|
| ID | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | A | A |
| Reset 0x00000000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| ID | R/W | Field | Value ID | Value | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | ANADETECT | | | Analog detect configuration | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | Cross | 0 | Generate ANADETECT on crossing, both upward crossing and downward crossing | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | Up | 1 | Generate ANADETECT on upward crossing only | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | Down | 2 | Generate ANADETECT on downward crossing only | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.12.4.26 HYST

Address offset: 0x538

Comparator hysteresis enable

8.13 NFC – Near field communication tag

The NFCT peripheral is an implementation of an NFC Forum compliant listening device NFC-A.