Subscribe configuration for task CLR[n]

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that task CLR[n] will subscribe to	
B	RW	EN				
			Disabled	0	Disable subscription	
			Enabled	1	Enable subscription	

8.9.5.7 EVENTS_IN[n] (n=0..7)

Address offset: 0x100 + (n × 0x4)

Event from pin specified in CONFIG[n].PSEL

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID						A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	EVENTS_IN			Event from pin specified in CONFIG[n].PSEL	
			NotGenerated	0	Event not generated	
			Generated	1	Event generated	

8.9.5.8 EVENTS_PORT[n] (n=0..0)

Peripheral events.

8.9.5.8.1 EVENTS_PORT[n].NONSECURE (n=0..0)

Address offset: 0x140 + (n × 0x8)

Non-secure port event

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID							A
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	TZ	Field	Value ID	Value	Description	
A	RW	NS	NONSECURE			Non-secure port event	
				NotGenerated	0	Event not generated	
				Generated	1	Event generated	

8.9.5.8.2 EVENTS_PORT[n].SECURE (n=0..0)

Address offset: 0x144 + (n × 0x8)

Secure port event

NORDIC
SEMICONDUCTOR