

Use this to set, or read, which external inputs are resources to the trace unit.

Might ignore writes when the trace unit is enabled or not idle.

Only returns stable data when TRCSTATR.PMSTABLE == 1.

When the sequencer is used, all sequencer state transitions must be programmed with a valid event.

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				D	D	D	D	D	D	D	D	C	C	C	C	C	C	C	B	B	B	B	B	B	B	B	A	A	A	A	A	A	A	A	
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value				Description																											
A-D	RW	SEL[i] (i=0..3)		[0:255]				Each field in this collection selects an external input as a resource for the trace unit.																											

9.8.1.25 TRCCNTRLDVR[n] (n=0..3)

Address offset: 0x140 + (n × 0x4)

This sets or returns the reload count value for counter n.

Might ignore writes when the trace unit is enabled or not idle.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	RW	VALUE		[0:65535]				Contains the reload value for counter n. When a reload event occurs for counter n then the trace unit copies the VALUEn field into counter n.																											

9.8.1.26 TRCCNTCTLR[n] (n=0..3)

Address offset: 0x150 + (n × 0x4)

Controls the operation of counter n.

Might ignore writes when the trace unit is enabled or not idle.

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				D C B B B B B B B A A A A A A A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	CNTEVENT		[0:255]	Selects an event, that when it occurs causes counter n to decrement.																														
B	RW	RLDEVENT		[0:255]	Selects an event, that when it occurs causes a reload event for counter n.																														
C	RW	RLDSELF			Controls whether a reload event occurs for counter n, when counter n reaches zero.																														
			Disabled	0	The counter is in Normal mode.																														
			Enabled	1	The counter is in Self-reload mode.																														
D	RW	CNTCHAIN			For TRCCNTCTLR3 and TRCCNTCTLR1, this bit controls whether counter n decrements when a reload event occurs for counter n-1.																														
			Disabled	0	Counter n does not decrement when a reload event for counter n-1 occurs.																														
			Enabled	1	Counter n decrements when a reload event for counter n-1 occurs. This concatenates counter n and counter n-1, to provide a larger count value.																														

9.8.1.27 TRCCNTVR[n] (n=0..3)

Address offset: 0x160 + (n × 0x4)