

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
P	RW	PHYEND_DISABLE	Enabled	1				Enable shortcut																											
			Disabled	0				Shortcut between event PHYEND and task DISABLE																											
			Enabled	1				Enable shortcut																											
Q	RW	PHYEND_START						Shortcut between event PHYEND and task START																											
			Disabled	0				Disable shortcut																											
			Enabled	1				Enable shortcut																											

8.17.14.90 INTENSET00

Address offset: 0x488

Enable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				W V U T S R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	READY W1S			Write '1' to enable interrupt for event READY																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	TXREADY W1S			Write '1' to enable interrupt for event TXREADY																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
C	RW	RXREADY W1S			Write '1' to enable interrupt for event RXREADY																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
D	RW	ADDRESS W1S			Write '1' to enable interrupt for event ADDRESS																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
E	RW	FRAMESTART W1S			Write '1' to enable interrupt for event FRAMESTART																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
F	RW	PAYLOAD W1S			Write '1' to enable interrupt for event PAYLOAD																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														