

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	B		A A A A A A A		
Reset 0x00000000	0 0				
ID	R/W	Field	Value ID	Value	Description
A	RW	CHIDX		[0..255]	DPPI channel that event WOKENUP will publish to
B	RW	EN			
			Disabled	0	Disable publishing
			Enabled	1	Enable publishing

4.2.6.7.12 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	D C B A				
Reset 0x00000000	0 0				
ID	R/W	Field	Value ID	Value	Description
A	RW	WOKENUP			Enable or disable interrupt for event WOKENUP
					This event is triggered only if waken up by the WAKEUP task
			Disabled	0	Disable
			Enabled	1	Enable
B	RW	READY			Enable or disable interrupt for event READY
					This event is not connected to PPI
			Disabled	0	Disable
			Enabled	1	Enable
C	RW	READYNEXT			Enable or disable interrupt for event READYNEXT
					This event is not connected to PPI
			Disabled	0	Disable
			Enabled	1	Enable
D	RW	ACCESSERROR			Enable or disable interrupt for event ACCESSERROR
					This event is not connected to PPI
			Disabled	0	Disable
			Enabled	1	Enable

4.2.6.7.13 INTENSET

Address offset: 0x304

Enable interrupt

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	D C B A				
Reset 0x00000000	0 0				
ID	R/W	Field	Value ID	Value	Description
A	RW	WOKENUP			Write '1' to enable interrupt for event WOKENUP
		W1S			This event is triggered only if waken up by the WAKEUP task
			Set	1	Enable
			Disabled	0	Read: Disabled
			Enabled	1	Read: Enabled
B	RW	READY			Write '1' to enable interrupt for event READY
		W1S			This event is not connected to PPI