

## 8.20.7.2 TASKS \_ RELEASE

Address offset: 0x018

Release SPI semaphore, enabling the SPI slave to acquire it

### 8.20.7.3 TASKS DMA

### Peripheral tasks.

#### **8.20.7.3.1 TASKS\_DMA.RX**

### Peripheral tasks.

#### **8.20.7.3.1.1 TASKS\_DMA.RX.ENABLEMATCH[n] (n=0..3)**

Address offset:  $0x030 + (n \times 0x4)$

Enables the MATCH[n] event by setting the ENABLE[n] bit in the CONFIG register.

#### 8.20.7.3.1.2 TASKS DMA.RX.DISABLEMATCH[n] (n=0..3)

Address offset: 0x040 + (n × 0x4)

Disables the MATCH[n] event by clearing the ENABLE[n] bit in the CONFIG register.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																															A	
<b>Reset 0x00000000</b>	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	W	DISABLEMATCH			Disables the MATCH[n] event by clearing the ENABLE[n] bit in the CONFIG register.																											
		Trigger	1	Trigger task																												

8 2074 SUBSCRIBE ACQUIRE

Address offset: 0x094

Subscribe configuration for task **ACQUIRE**