

## 8.10.7.20 INTPEND0

Address offset: 0x30C

## Pending interrupts

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			P	O	N	M	L K J I H G F E D C B A																											
<b>Reset 0x00000000</b>			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID																															
A-L	R	COMPARE[i] (i=0..11)																											Read pending status of interrupt for event COMPARE[i]					
			NotPending																											Read: Not pending				
			Pending																										Read: Pending					
M	R	RTCOMPARESYNC																											Read pending status of interrupt for event RTCOMPARESYNC					
			NotPending																										Read: Not pending					
			Pending																									Read: Pending						
N	R	PWMPERIODEND																											Read pending status of interrupt for event PWMPERIODEND					
			NotPending																									Read: Not pending						
			Pending																									Read: Pending						
O	R	PWMREADY																											Read pending status of interrupt for event PWMREADY					
			NotPending																									Read: Not pending						
			Pending																									Read: Pending						
P	R	CLKOUTREADY																										Read pending status of interrupt for event CLKOUTREADY						
			NotPending																									Read: Not pending						