

7.8.1.6.4 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	CRYPTOMASTER			Enable or disable interrupt for event CRYPTOMASTER																														
					The interrupt source must be cleared at Cryptomaaster before clearing this event.																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
B	RW	RNG			Enable or disable interrupt for event RNG																														
					The interrupt source must be cleared at RNG before clearing this event.																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
C	RW	PKEIKG			Enable or disable interrupt for event PKEIKG																														
					The interrupt source must be cleared at PKE or IKG before clearing this event.																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														

7.8.1.6.5 INTENSET

Address offset: 0x304

Enable interrupt

Bit number			31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID			C B A																															
Reset 0x00000000			0 0																															
ID	R/W	Field	Value ID	Value	Description																													
A	RW	CRYPTOMASTER W1S			Write '1' to enable interrupt for event CRYPTOMASTER																													
					The interrupt source must be cleared at Cryptomaaster before clearing this event.																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
B	RW	RNG W1S			Write '1' to enable interrupt for event RNG																													
					The interrupt source must be cleared at RNG before clearing this event.																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													
C	RW	PKEIKG W1S			Write '1' to enable interrupt for event PKEIKG																													
					The interrupt source must be cleared at PKE or IKG before clearing this event.																													
			Set	1	Enable																													
			Disabled	0	Read: Disabled																													
			Enabled	1	Read: Enabled																													