

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A		
<b>Reset OxFFFFFFF</b>	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
ID	R/W	Field	Value ID	Value	Description																											
A	RW1	PALL	Unprotected	0xFFFFFFFF	Leaves TAMPC PROTECT.DOMAIN DBGEN and NIDEN signal protectors unlocked and under CPU control.																											

#### **4.2.9.1.1.2 APPROTECT[n].PROTECT1 (n=0..0)**

Address offset: 0x01C + (n × 0x20)

### Access port protection

Any other value than Unprotected will lock TAMPC PROTECT.DOMAIN signal protectors.

#### 4.2.9.1.2 SECUREAPPROTECT[n] (n=0..0)

## Access Port Protection Registers

#### **4.2.9.1.2.1 SECUREAPPROTECT[n].PROTECT0 (n=0..0)**

Address offset:  $0x020 + (n \times 0x20)$

## Access port protection

Any other value than Unprotected will lock TAMPC PROTECT.DOMAIN signal protectors.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
<b>Reset 0xFFFFFFFF</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>	
ID	R/W	Field	Value ID	Value	Description																											
A	RW1	PALL																														
		Unprotected	0xFFFFFFFF	Leaves TAMPC PROTECT.DOMAIN SPIDEN and SPNIDEN signal protectors unlocked and under CPU control.																												

#### **4.2.9.1.2.2 SECUREAPPROTECT[n].PROTECT1 (n=0..0)**

Address offset: 0x03C + (n × 0x20)

## Access port protection register

Any other value than Unprotected will lock TAMPC PROTECT.DOMAIN signal protectors.