

## Configuration

Instance	Domain	Configuration
TIMER00 : S	GLOBAL	Peripheral clock frequency (PCLK) is 128 MHz
TIMER00 : NS		The system is able to configure the TIMER peripheral input clock frequency (PCLK) before it reaches TIMER, and calculations of PRESCALER value must take the actual PCLK frequency into account
		6 capture compare channels implemented
TIMER10 : S	GLOBAL	Peripheral clock frequency (PCLK) is 32 MHz
TIMER10 : NS		8 capture compare channels implemented
TIMER20 : S	GLOBAL	Peripheral clock frequency (PCLK) is 16 MHz
TIMER20 : NS		6 capture compare channels implemented
TIMER21 : S	GLOBAL	Peripheral clock frequency (PCLK) is 16 MHz
TIMER21 : NS		6 capture compare channels implemented
TIMER22 : S	GLOBAL	Peripheral clock frequency (PCLK) is 16 MHz
TIMER22 : NS		6 capture compare channels implemented
TIMER23 : S	GLOBAL	Peripheral clock frequency (PCLK) is 16 MHz
TIMER23 : NS		6 capture compare channels implemented
TIMER24 : S	GLOBAL	Peripheral clock frequency (PCLK) is 16 MHz
TIMER24 : NS		6 capture compare channels implemented

## Register overview

Register	Offset	TZ	Description
TASKS_START	0x000		Start Timer
TASKS_STOP	0x004		Stop Timer
TASKS_COUNT	0x008		Increment Timer (Counter mode only)
TASKS_CLEAR	0x00C		Clear time
TASKS_CAPTURE[n]	0x040		Capture Timer value to CC[n] register
SUBSCRIBE_START	0x080		Subscribe configuration for task START
SUBSCRIBE_STOP	0x084		Subscribe configuration for task STOP
SUBSCRIBE_COUNT	0x088		Subscribe configuration for task COUNT
SUBSCRIBE_CLEAR	0x08C		Subscribe configuration for task CLEAR
SUBSCRIBE_CAPTURE[n]	0x0C0		Subscribe configuration for task CAPTURE[n]
EVENTS_COMPARE[n]	0x140		Compare event on CC[n] match
PUBLISH_COMPARE[n]	0x1C0		Publish configuration for event COMPARE[n]
SHORTS	0x200		Shortcuts between local events and tasks
INTEN	0x300		Enable or disable interrupt
INTENSET	0x304		Enable interrupt
INTENCLR	0x308		Disable interrupt
MODE	0x504		Timer mode selection
BITMODE	0x508		Configure the number of bits used by the TIMER
PRESCALER	0x510		Timer prescaler register
CC[n]	0x540		Capture/Compare register n
ONESHOTEN[n]	0x580		Enable one-shot operation for Capture/Compare channel n

### 8.22.5.1 TASKS\_START

Address offset: 0x000

Start Timer