

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
ID				A																																			
Reset 0x00000000				0 0																																			
ID	R/W	Field	Value ID	Value		Description																																	
A	RW	EVENTS_COLLISION				NFC auto collision resolution error reported.																																	
			NotGenerated	0		Event not generated																																	
			Generated	1		Event generated																																	

8.13.14.30 EVENTS_SELECTED

Address offset: 0x14C

NFC auto collision resolution successfully completed

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID																																				A		
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value		Description																																
A	RW	EVENTS_SELECTED				NFC auto collision resolution successfully completed																																
			NotGenerated	0	Event not generated																																	
			Generated	1	Event generated																																	

8.13.14.31 EVENTS_STARTED

Address offset: 0x150

EasyDMA is ready to receive or send frames.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	EVENTS_STARTED			EasyDMA is ready to receive or send frames.																														
			NotGenerated	0	Event not generated																														
			Generated	1	Event generated																														

8.13.14.32 PUBLISH_READY

Address offset: 0x180

Publish configuration for event **READY**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0												
ID				B																								A				A				A				A				A			
Reset 0x00000000				0 0																																											
ID	R/W	Field	Value ID	Value				Description																																							
A	RW	CHIDX		[0..255]				DPPI channel that event READY will publish to																																							
B	RW	EN																																													
			Disabled	0				Disable publishing																																							
			Enabled	1				Enable publishing																																							

8.13.14.33 PUBLISH_FIELDDETECTED

Address offset: 0x184

Publish configuration for event **FIELDDETECTED**