

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C C C C C																												A A A			
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
			SHIFT1	0x0	Shift OUT by 1 bit																														
			SHIFT2	0x1	Shift OUT by 2 bits																														
			SHIFT4	0x2	Shift OUT by 4 bits																														
			SHIFT8	0x3	Shift OUT by 8 bits																														
			SHIFT16	0x4	Shift OUT by 16 bits																														

8.26.3.91 NORDIC.OUTMODEB

Address offset: 0xBE4

Buffered OUTMODE register

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				B B B B B																												A A A			
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	MODE			Mode																														
			NoShifting	0x0	No shifting																														
			OutBBuf	0x2	Only OUTB used for buffering																														
			OutBBufToggleClk	0x4	Only OUTB used for buffering, auto-toggle clock line																														
B	RW	FRAMEWIDTH		0..16	Frame width in bits																														

8.26.3.92 NORDIC.INMODEB

Address offset: 0xBE5

Buffered INMODE register

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	MODE			Input Mode																														
			CONTINUOUS	0x0	Continuous sampling (if CPU is not sleeping)																														
			EVENT	0x1	Sampling on Counter1 event																														
			SHIFT	0x2	Sampling and shifting on Counter1 event																														

8.26.3.93 NORDIC.INB

Address offset: 0xBE6

Buffered GPIO input

GPIO Input. Real Time Peripherals VIO.