

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0					
ID														O	N	M					L				K	J	I					H	G	F	E	D	C	B	A
Reset 0x00000000			0 0																																				
ID	R/W	Field	Value ID	Value	Description																																		
F	RW	RXFRAMESTART W1S	Enabled	1	Read: Enabled																																		
					Write '1' to enable interrupt for event <a href="#">RXFRAMESTART</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
G	RW	RXFRAMEEND W1S			Write '1' to enable interrupt for event <a href="#">RXFRAMEEND</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
H	RW	ERROR W1S			Write '1' to enable interrupt for event <a href="#">ERROR</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
I	RW	RXERROR W1S			Write '1' to enable interrupt for event <a href="#">RXERROR</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
J	RW	ENDRX W1S			Write '1' to enable interrupt for event <a href="#">ENDRX</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
K	RW	ENDTX W1S			Write '1' to enable interrupt for event <a href="#">ENDTX</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
L	RW	AUTOCOLRESSTARTED W1S			Write '1' to enable interrupt for event <a href="#">AUTOCOLRESSTARTED</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
M	RW	COLLISION W1S			Write '1' to enable interrupt for event <a href="#">COLLISION</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
N	RW	SELECTED W1S			Write '1' to enable interrupt for event <a href="#">SELECTED</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		
			Enabled	1	Read: Enabled																																		
O	RW	STARTED W1S			Write '1' to enable interrupt for event <a href="#">STARTED</a>																																		
			Set	1	Enable																																		
			Disabled	0	Read: Disabled																																		