Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				C B A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**

ID	R/W	Field	Value ID	Value	Description
A	RW	CLOCKPOLARITY			Clock polarity This field is applicable only when OUTMODE.MODE = OutBBufToggleClk
			Low	0x0	Clock polarity is low
			High	0x1	Clock polarity is High
B	RW	STOPCOUNTERS			Stop counters CNT0 and CNT1 on OUTB under-run, or on INB Overflow if OUTMODE2 and INMODE2 This feature is used for clock stretching during OUTB under-run
			NoStop	0	Counters do not stop on OUTB under-run
			Stop	1	Counters stop on OUTB under-run Resume the counters when OUTB is written
C	RW	INSEL			Input pin selection
			SamePin	0x0	Sample on same OUT pin
			SeparatePin	0x1	Sample on separate pin

8.26.3.44 NORDIC.CNTMODE0

Address offset: 0x7D0

CNT0 Mode

Real Time Peripherals VTIM

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**

ID	R/W	Field	Value ID	Value	Description
A	RW	CNTMODE0			CNT0 Mode
			STOP	0x0	CNT0 stops at 0
			WRAP	0x1	When CNT0 reaches 0 it will continue counting from 0xFFFF
			RELOAD	0x2	When CNT0 reaches 0 it will continue counting from the value in CNTTOP
			TRIGCOMB	0x3	When CNT0 reaches 0 it is reloaded from CNTTOP and stops. Counting will restart when a VIO event happens

8.26.3.45 NORDIC.CNTMODE1

Address offset: 0x7D1

CNT1 Mode

Real Time Peripherals VTIM

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**

ID	R/W	Field	Value ID	Value	Description
A	RW	CNTMODE1			CNT1 Mode
			STOP	0x0	CNT1 stops at 0
			WRAP	0x1	When CNT1 reaches 0 it will continue counting from 0xFFFF
			RELOAD	0x2	When CNT1 reches 0 it will continue counting from the value in CNTTOP
			TRIGCOMB	0x3	In combine mode mode CNT1 acts as an extension of CNT0 (16 most significant bits of the 32-bit CNT)