

4.2.6.7.2 TASKS_CLRWRITEBUF

Address offset: 0x004

Clear internal write-buffer

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID		A	
Reset 0x00000000		0 0	
ID	R/W	Field	Description
A	W	TASKS_CLRWRITEBUF	Clear internal write-buffer
		Trigger	1
			Trigger task

4.2.6.7.3 TASKS_COMMITWRITEBUF

Address offset: 0x008

Commits the data stored in internal write-buffer to RRAM

READY status is updated during this operation

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID		A	
Reset 0x00000000		0 0	
ID	R/W	Field	Description
A	W	TASKS_COMMITWRITEBUF	Commits the data stored in internal write-buffer to RRAM
		Trigger	1
			READY status is updated during this operation
			Trigger task

4.2.6.7.4 SUBSCRIBE_WAKEUP

Address offset: 0x080

Subscribe configuration for task [WAKEUP](#)

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID		B	
Reset 0x00000000		0 0	
ID	R/W	Field	Description
A	RW	CHIDX	DPPI channel that task WAKEUP will subscribe to
B	RW	EN	
		Disabled	0
		Enabled	1
			Disable subscription
			Enable subscription

4.2.6.7.5 SUBSCRIBE_CLRWRITEBUF

Address offset: 0x084

Subscribe configuration for task [CLRWRITEBUF](#)