Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	A A A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A	RW	TXADDRESS			Transmit address select
					Logical address to be used when transmitting a packet

8.17.14.140 RXADDRESSES

Address offset: 0xE40

Receive address select

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	H G F E D C B A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A-H	RW	ADDR[i] (i=0..7)			Enable or disable reception on logical address i
			Disabled	0	Disable
			Enabled	1	Enable

8.17.14.141 CRCCNF

Address offset: 0xE44

CRC configuration

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	B B B A A
Reset 0x00000000	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

ID	R/W	Field	Value ID	Value	Description
A	RW	LEN			CRC length in number of bytes.
					Note: For MODE Ble_LR125Kbit and Ble_LR500Kbit, only LEN set to 3 is supported
			Disabled	0	CRC length is zero and CRC calculation is disabled
			One	1	CRC length is one byte and CRC calculation is enabled
			Two	2	CRC length is two bytes and CRC calculation is enabled
			Three	3	CRC length is three bytes and CRC calculation is enabled
B	RW	SKIPADDR			Control whether CRC calculation skips the address field. Other fields can also be skipped.
			Include	0	CRC calculation includes address field
			Skip	1	CRC calculation starting at first byte after address field.
			Ieee802154	2	CRC calculation starting at first byte after length field (as per 802.15.4 standard).
			SkipS0	3	CRC calculation starting at first byte after S0 field.
			SkipS1	4	CRC calculation starting at first byte after S1 field.

8.17.14.142 CRCPOLY

Address offset: 0xE48

CRC polynomial

NORDIC
SEMICONDUCTOR