

## 8.5.7.16 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			D	C	B	A																												
<b>Reset 0x00000000</b>			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value	Description																													
A	RW	READY			Enable or disable interrupt for event <a href="#">READY</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
B	RW	DOWN			Enable or disable interrupt for event <a href="#">DOWN</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
C	RW	UP			Enable or disable interrupt for event <a href="#">UP</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													
D	RW	CROSS			Enable or disable interrupt for event <a href="#">CROSS</a>																													
			Disabled	0	Disable																													
			Enabled	1	Enable																													

## 8.5.7.17 INTENSET

Address offset: 0x304

Enable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			D	C	B	A																												
<b>Reset 0x00000000</b>			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value	Description																													
A	RW	READY			Write '1' to enable interrupt for event <a href="#">READY</a>																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
B	RW	DOWN			Write '1' to enable interrupt for event <a href="#">DOWN</a>																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
C	RW	UP			Write '1' to enable interrupt for event <a href="#">UP</a>																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
D	RW	CROSS			Write '1' to enable interrupt for event <a href="#">CROSS</a>																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											