

11.1.2 CURRENT Electrical specification

11.1.2.1 Sleep

Symbol	Description	Min.	Typ.	Max.	Units
I _{OFF0}	System OFF, Wake on pin, 0 KB RAM retained		0.7		μA
I _{OFF1}	System OFF, Wake on pin + GRTC, LFXO, 0 KB RAM retained		0.9		μA
I _{ON_IDLE0}	System ON, Wake on pin, 0 KB RAM retained		0.8		μA
I _{ON_IDLE1}	System ON, Wake on pin, 64 KB RAM retained		1.3		μA
I _{ON_IDLE2}	System ON, Wake on pin, 96 KB RAM retained		1.5		μA
I _{ON_IDLE3}	System ON, Wake on pin, 128 KB RAM retained		1.8		μA
I _{ON_IDLE4}	System ON, Wake on pin, 192 KB RAM retained		2.3		μA
I _{ON_IDLE5}	System ON, Wake on pin, 256 KB RAM retained		2.7		μA
I _{ON_IDLE6}	System ON, Wake on pin + GRTC, LFXO, 64 KB RAM retained		1.5		μA
I _{ON_IDLE7}	System ON, Wake on pin + GRTC, LFXO, 128 KB RAM retained		1.9		μA
I _{ON_IDLE8}	System ON, Wake on pin + GRTC, LFXO, 256 KB RAM retained		2.9		μA
I _{ON_IDLE9}	System ON, Wake on pin + GRTC, LFXO, 256 KB RAM retained		3.5		μA
I _{ON_IDLE11}	System ON, Wake on pin, Constant Latency mode, 0 KB RAM retained		0.55		mA

11.1.2.2 COMP active

Symbol	Description	Min.	Typ.	Max.	Units
I _{COMP0}	COMP enabled in Low-power mode		44		μA
I _{COMP1}	COMP enabled in Normal mode		46		μA
I _{COMP2}	COMP enabled in High-speed mode		50		μA

11.1.2.3 CPU running

The CPU running parameters are obtained using the following compiler version:

Compiler: Arm version 6.16 (armclang)

Compiler flags:

```
--target=arm-arm-none-eabi -c -g -masm=auto -Wno-unused-value -mcpu=cortex-m33 -mfloat-abi=hard -mfpu=fpv5-sp-d16 -flto -Omax
```

Linker flags:

```
--lto --remove
```

Symbol	Description	Min.	Typ.	Max.	Units
I _{APPCPU0}	CPU running Coremark at 128 MHz from NVM, Cache enabled		2.6		mA
I _{APPCPU1}	CPU running Coremark at 128 MHz from RAM, Cache disabled		2.8		mA

11.1.2.4 QDEC active

Symbol	Description	Min.	Typ.	Max.	Units
I _{QDEC0}	QDEC enabled but not running		84		μA
I _{QDEC1}	QDEC running, 131 ms sample period		121		μA