



Figure 133: Simplified ADC sample network

TACQ	Maximum source resistance [kOhm]
≤1	<1
3	10
5	40
10	100
15	200
20	400
40	800

Table 57: Acquisition time

8.18.9 Limits event monitoring

A channel can be event monitored by configuring limit register `CH[n].LIMIT`.

If the conversion result is higher than the defined high limit, or lower than the defined low limit, the appropriate event will be generated.