8.11.10.32 PSEL.LRCK

Address offset: 0x568

Pin select for LRCK signal

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C	B B B A A A A A
Reset 0xFFFFFFFF					**1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1**	
ID	R/W	Field	Value ID	Value	Description	
A	RW	PIN		[0..31]	Pin number	
B	RW	PORT		[0..7]	Port number	
C	RW	CONNECT			Connection	
			Disconnected	1	Disconnect	
			Connected	0	Connect	

8.11.10.33 PSEL.SDIN

Address offset: 0x56C

Pin select for SDIN signal

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C	B B B A A A A A
Reset 0xFFFFFFFF					**1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1**	
ID	R/W	Field	Value ID	Value	Description	
A	RW	PIN		[0..31]	Pin number	
B	RW	PORT		[0..7]	Port number	
C	RW	CONNECT			Connection	
			Disconnected	1	Disconnect	
			Connected	0	Connect	

8.11.10.34 PSEL.SDOUT

Address offset: 0x570

Pin select for SDOUT signal

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C	B B B A A A A A
Reset 0xFFFFFFFF					**1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1**	
ID	R/W	Field	Value ID	Value	Description	
A	RW	PIN		[0..31]	Pin number	
B	RW	PORT		[0..7]	Port number	
C	RW	CONNECT			Connection	
			Disconnected	1	Disconnect	
			Connected	0	Connect	

8.11.10.35 CHANNEL[n].TERMINATEONBUSERROR (n=0..1)

Address offset: 0x580 + (n × 0x8)

Terminate the transaction if a BUSERROR event is detected.

NORDIC
SEMICONDUCTOR