

8.23.10.14.1.2 EVENTS DMA.RX.READY

Address offset: 0x150

Generated when EasyDMA has buffered the .PTR and .MAXCNT registers for the channel, allowing them to be written to prepare for the next sequence.

8.23.10.14.1.3 EVENTS DMA.RX.BUSERROR

Address offset: 0x154

An error occurred during the bus transfer.

When this event is generated, the address which caused the error can be read from the **BUSERRORADDRESS** register.

8.23.10.14.1.4 EVENTS DMA.RX.MATCH[n] (n=0..3)

Address offset: 0x158 + ($n \times 0x4$)

Pattern match is detected on the DMA data bus.