

8.18.11.21 PUBLISH_STOPPED

Address offset: 0x194

Publish configuration for event **STOPPED**

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	B	A A A A A A A A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	RW	CHIDX	[0..255]	DPPI channel that event STOPPED will publish to	
B	RW	EN			
			Disabled	0	Disable publishing
			Enabled	1	Enable publishing

8.18.11.22 PUBLISH_CH[n] (n=0..7)

Publish configuration for events

8.18.11.22.1 PUBLISH_CH[n].LIMITH (n=0..7)

Address offset: 0x198 + (n × 0x8)

Publish configuration for event **CH[n].LIMITH**

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	B	A A A A A A A A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	RW	CHIDX	[0..255]	DPPI channel that event CH[n].LIMITH will publish to	
B	RW	EN			
			Disabled	0	Disable publishing
			Enabled	1	Enable publishing

8.18.11.22.2 PUBLISH_CH[n].LIMITL (n=0..7)

Address offset: 0x19C + (n × 0x8)

Publish configuration for event **CH[n].LIMITL**

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID	B	A A A A A A A A			
Reset 0x00000000		0 0			
ID	R/W	Field	Value ID	Value	Description
A	RW	CHIDX	[0..255]	DPPI channel that event CH[n].LIMITL will publish to	
B	RW	EN			
			Disabled	0	Disable publishing
			Enabled	1	Enable publishing

8.18.11.23 SHORTS

Address offset: 0x200

Shortcuts between local events and tasks