

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0							
ID				B																								A					A		A		A		A		A	
Reset 0x00000000				0																																						
ID	R/W	Field	Value ID	Value		Description																																				
A	RW	CHIDX		[0..255]		DPPI channel that event STOPPED will publish to																																				
B	RW	EN																																								
			Disabled	0	Disable publishing																																					
			Enabled	1	Enable publishing																																					

8.11.10.11 PUBLISH_TXPTRUPD

Address offset: 0x194

Publish configuration for event **TXPTRUPD**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID				B																								A				A	A	A	A	A	A	A
Reset 0x00000000				0																																		
ID	R/W	Field	Value ID	Value		Description																																
A	RW	CHIDX		[0..255]		DPPI channel that event TXPTRUPD will publish to																																
B	RW	EN																																				
			Disabled	0	Disable publishing																																	
			Enabled	1	Enable publishing																																	

8.11.10.12 PUBLISH_FRAMESTART

Address offset: 0x19C

Publish configuration for event **FRAMESTART**

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
ID				B																								A					A	A	A	A	A	A	A
Reset 0x00000000				0																																			
ID	R/W	Field	Value ID	Value		Description																																	
A	RW	CHIDX		[0..255]		DPPI channel that event FRAMESTART will publish to																																	
B	RW	EN																																					
			Disabled	0	Disable publishing																																		
			Enabled	1	Enable publishing																																		

8.11.10.13 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				D C B A																															
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	RXPTRUPD			Enable or disable interrupt for event RXPTRUPD																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
B	RW	STOPPED			Enable or disable interrupt for event STOPPED																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														