

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A A																															
Reset 0x00000002				0 1 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	RATEOVERRIDE			Data rate override setting.																														
			125Kbit	0	125 Kbps																														
			500Kbit	2	500 Kbps																														
			1Mbit	3	1 Mbps																														
			2Mbit	4	2 Mbps																														
			4Mbit	5	4 Mbps																														

8.4.7.22 ADATAMASK

Address offset: 0x548

CCM adata mask.

Bitmask for the first adata byte. The masking is done before MAC generation/authentication.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A A A A A A A																															
Reset 0x000000E3				0 0																															

8.5 COMP — Comparator

The comparator peripheral (COMP) compares one input voltage (VIN+) against another input voltage (VIN-). VIN+ can be derived from an analog input pin (**AIN0** to **AIN7**). VIN- can be derived from multiple sources depending on the operation mode of the comparator.

The main features of COMP are the following:

- Input range from 0 V to VDD
- Single-ended mode
 - Fully flexible hysteresis using a 64-level reference ladder
- Differential mode
 - Configurable hysteresis
- Reference inputs (VREF)
 - External reference from **AIN0** to **AIN7** (between 0 V and VDD)
 - Internal VDD reference
 - 1.2 V internal reference
- Two speed/power consumption modes, low-power and high-speed
- Single-pin capacitive sensor support
- Event generation on output changes
 - UP event when VIN+ rises above VIN- (VIN+ > VIN-)
 - DOWN event when VIN+ falls below VIN- (VIN+ < VIN-)
 - CROSS event on VIN+ and VIN- crossing
 - READY event on core and internal reference (if used) ready