RRAM can also be accessed by FLPR. FLPR does not share the instruction cache with the Cortex-M33; it has its own built-in, dedicated cache that is only available to VPR. The regular CACHE — Instruction/data cache on page 29 is dedicated to the Cortex-M33 and is not available to FLPR.

3.5.3 Memory map
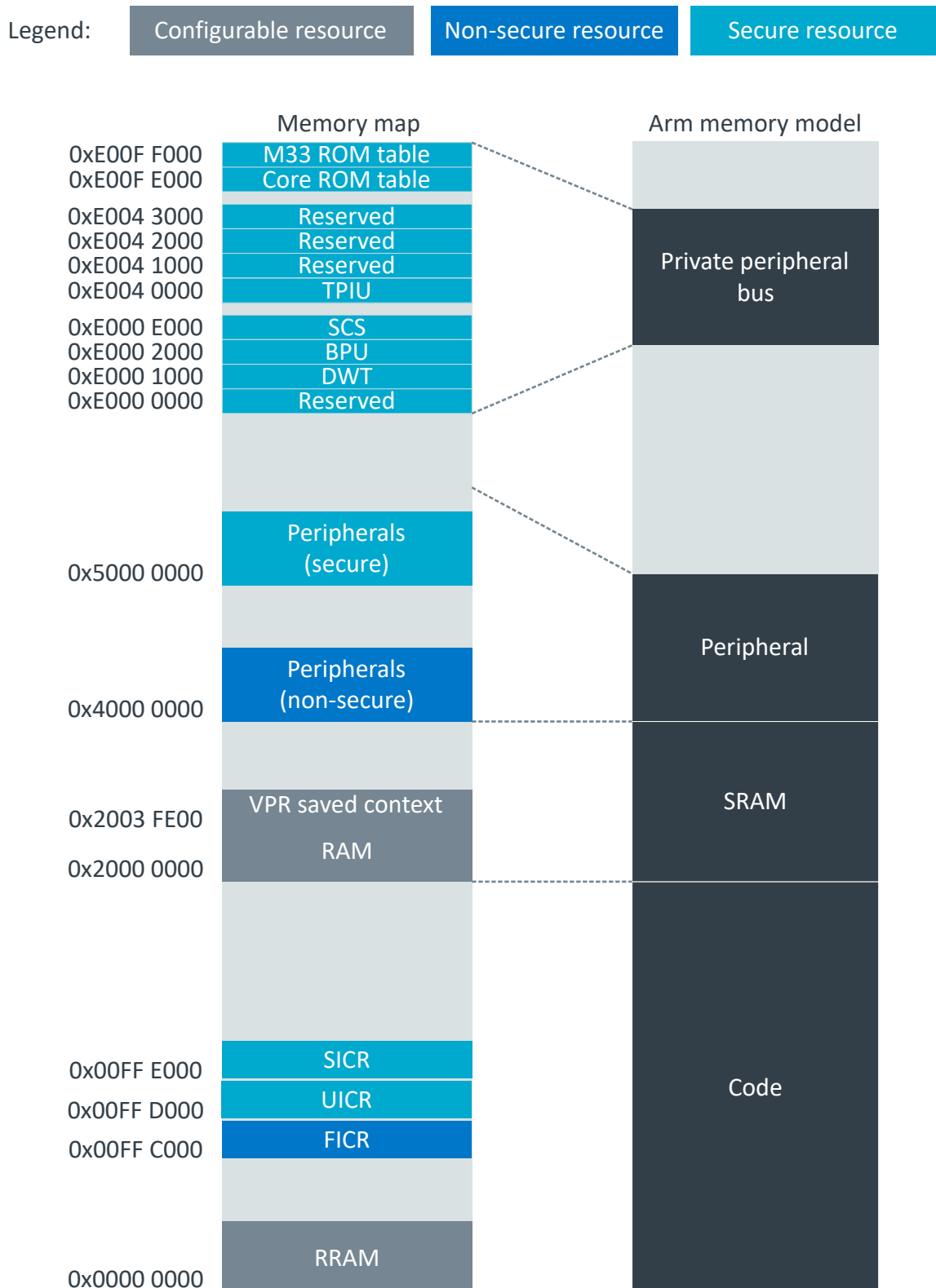
The complete memory map is shown in the following figure.

Figure 3: Memory map