

Pin	Clock pin	Name	Function	Description	Dedicated function
51		VDD	Power	Power supply	
52		VDD	Power	Power supply	
53		VSS	Power	Ground pad (die pad)	

Table 80: QFN52 pin assignments

For the device to function properly, the exposed die pad (pin 49) must be connected to ground (**VSS**, pins 32 and 44).

10.1.6 CSP47 (CAAA) package pin assignments

The CSP47 pin assignment figure and table describe the pinouts for this variant of the device.

Pins that can be used as clock signals are shown in red in the figure. For more information about clock pins, see [Clock pins](#) on page 860.

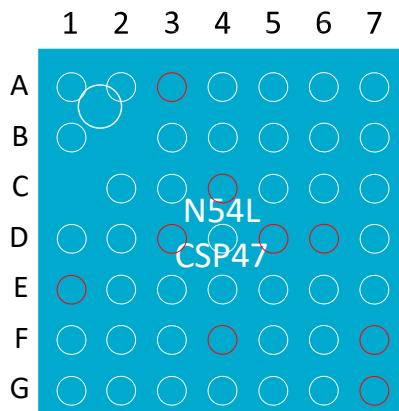


Figure 175: CSP47 pin assignments, top view