

8.22.5.13 SHORTS

Address offset: 0x200

Shortcuts between local events and tasks

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			P	O	N	M	L	K	J	I		H	G	F	E	D	C	B	A															
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID		Value																													
A-H	RW	COMPARE[i]_CLEAR (i=0..7)																																
		Disabled			0																													
		Enabled			1																													
I-P	RW	COMPARE[i]_STOP (i=0..7)																																
		Disabled			0																													
		Enabled			1																													

8.22.5.14 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			H	G	F	E	D	C	B	A																								
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID		Value																													
A-H	RW	COMPARE[i] (i=0..7)																																
		Disabled			0																													
		Enabled			1																													

8.22.5.15 INTENSET

Address offset: 0x304

Enable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			H	G	F	E	D	C	B	A																								
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID		Value																													
A-H	RW	COMPARE[i] (i=0..7)																																
		W1S			Set		1																											
					Disabled		0																											
					Enabled		1																											

8.22.5.16 INTENCLR

Address offset: 0x308

Disable interrupt