Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				f e d c b a Z Y X W V U T S R Q P O N M L K J I H G F E D C B A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A-P	W	OUT[i] (i=0..15)			
			UNCHANGED	0x0	Pin remains unchanged
			TOGGLE	0x1	Pin is toggled
Q-f	W	DIR[i] (i=0..15)			
			UNCHANGED	0x0	Pin remains unchanged
			TOGGLE	0x1	Pin is toggled

8.26.3.83 NORDIC.DIROUTBTGL

Address offset: 0xBD5

DIROUTB Toggle

Toggles bits in DIRB[15:0] and OUTB[15:0] in the same operation (sets dirty status). Real Time Peripherals VIO

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				f e d c b a Z Y X W V U T S R Q P O N M L K J I H G F E D C B A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A-P	W	OUTB[i] (i=0..15)			
			UNCHANGED	0x0	Pin remains unchanged
			TOGGLE	0x1	Pin is toggled
Q-f	W	DIRB[i] (i=0..15)			
			UNCHANGED	0x0	Pin remains unchanged
			TOGGLE	0x1	Pin is toggled

8.26.3.84 NORDIC.OUTBS

Address offset: 0xBD8

Buffered GPIO Output Dirty Status

Reads OUTB dirty status. Writes OUTB[15:0] value (sets dirty status). Real Time Peripherals VIO

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID				A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A A
Reset 0x00000000				**0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0**
ID	R/W	Field	Value ID	Value	Description
A	W	OUTB			Write to OUTB (if not dirty)
A	R	DIRTYBIT			Read Buffer Dirty status
			CLEAN	0x0	Buffer is clean
			DIRTY	0x1	Buffer is dirty

8.26.3.85 NORDIC.DIRBS

Address offset: 0xBD9

Buffered GPIO pin Direction Dirty Status

Reads DIRB dirty status. Writes DIRB value (sets dirty status). Real Time Peripherals VIO