Pin	Clock pin	Name	Function	Description	Dedicated function
C2		**VSS_PA**	Power	Ground (radio supply)	
C3		**P1.10** **ASI[2]** **RADIO[1]**	Digital I/O Digital I/O Digital I/O	General purpose I/O TAMPC active shield 2 input RADIO DFEGPIO	TAMPC RADIO
C4	Yes	**P1.11** **ASO[3]** **RADIO[2]** **AIN4**	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 output RADIO DFEGPIO Analog input	TAMPC RADIO
C5		**P1.00** **XL1**	Digital I/O Analog input	General purpose I/O Connection for 32.768 kHz crystal	
C6		**P1.01** **XL2**	Digital I/O Analog input	General purpose I/O Connection for 32.768 kHz crystal	
C7		**P1.02** **NFC1**	Digital I/O NFC input	General purpose I/O NFC antenna connection	
D1		**ANT**	RF	Single ended radio antenna connection	See Reference circuitry on page 889 for guidelines on how to ensure good RF performance
D2		**nRESET**	Reset	Pin reset with on-chip pull-up	
D3	Yes	**P0.04**	Digital I/O Digital I/O	General purpose I/O GRTC CLKOUT32K	GRTC
D4		**P2.08** **TRACEDATA[1]**	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.8 Trace data SPIM SDO SPIS SDO	FLPR Trace SPIM00/21 SPIS00/21

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