

8.22.5.13 SHORTS

Address offset: 0x200

Shortcuts between local events and tasks

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																																
ID				P O N M L K J I																H G F E D C B A																
Reset 0x00000000				0 0																																
ID	R/W	Field	Value ID	Value	Description																															
A-H	RW	COMPARE[i]_CLEAR (i=0..7)			Shortcut between event COMPARE[i] and task CLEAR																															
		Disabled	0	Disable shortcut																																
		Enabled	1	Enable shortcut																																
I-P	RW	COMPARE[i]_STOP (i=0..7)			Shortcut between event COMPARE[i] and task STOP																															
		Disabled	0	Disable shortcut																																
		Enabled	1	Enable shortcut																																

8.22.5.14 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A-H	RW	COMPARE[i] (i=0..7)			Enable or disable interrupt for event COMPARE[i]																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														

8.22.5.15 INTENSET

Address offset: 0x304

Enable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A-H	RW	COMPARE[i] (i=0..7)			Write '1' to enable interrupt for event COMPARE[i]																														
					W1S																														
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														

8.22.5.16 INTENCLR

Address offset: 0x308

Disable interrupt