

### 8.26.3.29 MVENDORID

Address offset: 0xF11

Machine Vendor ID

32-bit read-only register providing the JEDEC manufacturer ID of the provider of the core.

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		B A A A A A A			
<b>Reset 0x00000144</b>		<b>0 1 0 1 0 0 0 1 0 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	R	OFFSET			MVENDORID encodes the final byte in the Offset field, discarding the parity bit
B	R	BANK			MVENDORID encodes the number of one-byte continuation codes in the Bank field

### 8.26.3.30 MARCHID

Address offset: 0xF12

Machine Architecture ID

32-bit read-only register encoding the base microarchitecture of the hart

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		J I H H H G F F F E D C B A A			
<b>Reset 0x8000006E</b>		<b>1 0 1 1 0 1 1 1 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	R	MULDIV			Indicates the MULDIV parameter option
B	R	HIBERNATE			Indicates the POWEROFFSLEEP parameter option
C	R	DBG			Indicates the DBG parameter option
D	R	REMAP			Indicates the REMAP parameter option
E	R	BUSWIDTH			Indicates the BUS_WIDTH parameter option
F	R	BKPT			Indicates the BKPT parameter option
G	R	CACHE			Indicates that the CACHE is present
H	R	CACHEEXTRATAGBUF			Indicates the number of extra TAG buffers in CACHE
I	R	RETAINED			Indicates the RETAINED parameter option
J	R	IMPLEM			Indicates a non-open implementation

### 8.26.3.31 MIMPID

Address offset: 0xF13

Machine Implementation ID

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		C C C C C C C C B B B B B B B A A A A A A			
<b>Reset 0x00010300</b>		<b>0 1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	R	PATCHREV			Indicates the number of the patch revision
B	R	MINORREV			Indicates the number of the minor revision
C	R	MAJORREV			Indicates the number of the major revision