

## 8.20.7 Registers

### Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
SPIS00 : S	GLOBAL	0x5004A000	US	S	SA	No	SPI peripheral SPIS00
SPIS00 : NS		0x4004A000					
SPIS20 : S	GLOBAL	0x500C6000	US	S	SA	No	SPI peripheral SPIS20
SPIS20 : NS		0x400C6000					
SPIS21 : S	GLOBAL	0x500C7000	US	S	SA	No	SPI peripheral SPIS21
SPIS21 : NS		0x400C7000					
SPIS22 : S	GLOBAL	0x500C8000	US	S	SA	No	SPI peripheral SPIS22
SPIS22 : NS		0x400C8000					
SPIS30 : S	GLOBAL	0x50104000	US	S	SA	No	SPI peripheral SPIS30
SPIS30 : NS		0x40104000					

### Configuration

Instance	Domain	Configuration
SPIS00 : S	GLOBAL	Use dedicated pins on GPIO port P2
SPIS00 : NS		
SPIS20 : S	GLOBAL	Use GPIO port P1, or dedicated pins on P2
SPIS20 : NS		
SPIS21 : S	GLOBAL	Use GPIO port P1, or dedicated pins on P2
SPIS21 : NS		
SPIS22 : S	GLOBAL	Use GPIO port P1
SPIS22 : NS		
SPIS30 : S	GLOBAL	Use GPIO port P0
SPIS30 : NS		

### Register overview

Register	Offset	TZ	Description
TASKS_ACQUIRE	0x014		Acquire SPI semaphore
TASKS_RELEASE	0x018		Release SPI semaphore, enabling the SPI slave to acquire it
TASKS_DMA.RX.ENABLEMATCH[n]	0x030		Enables the MATCH[n] event by setting the ENABLE[n] bit in the CONFIG register.
TASKS_DMA.RX.DISABLEMATCH[n]	0x040		Disables the MATCH[n] event by clearing the ENABLE[n] bit in the CONFIG register.
SUBSCRIBE_ACQUIRE	0x094		Subscribe configuration for task ACQUIRE
SUBSCRIBE_RELEASE	0x098		Subscribe configuration for task RELEASE
SUBSCRIBE_DMA.RX.ENABLEMATCH[n]	0x0B0		Subscribe configuration for task ENABLEMATCH[n]
SUBSCRIBE_DMA.RX.DISABLEMATCH[n]	0x0C0		Subscribe configuration for task DISABLEMATCH[n]
EVENTS_END	0x104		Granted transaction completed
EVENTS_ACQUIRED	0x118		Semaphore acquired
EVENTS_DMA.RX.END	0x14C		Generated after all MAXCNT bytes have been transferred
EVENTS_DMA.RX.READY	0x150		Generated when EasyDMA has buffered the .PTR and .MAXCNT registers for the channel, allowing them to be written to prepare for the next sequence.
EVENTS_DMA.RX.BUSERROR	0x154		An error occurred during the bus transfer.
EVENTS_DMA.RX.MATCH[n]	0x158		Pattern match is detected on the DMA data bus.
EVENTS_DMA.TX.END	0x168		Generated after all MAXCNT bytes have been transferred