

Register	Offset	TZ	Description
PUBLISH_ADDRESS	0x30C		Publish configuration for event ADDRESS
PUBLISH_FRAMESTART	0x310		Publish configuration for event FRAMESTART
PUBLISH_PAYLOAD	0x314		Publish configuration for event PAYLOAD
PUBLISH_END	0x318		Publish configuration for event END
PUBLISH_PHYEND	0x31C		Publish configuration for event PHYEND
PUBLISH_DISABLED	0x320		Publish configuration for event DISABLED
PUBLISH_DEVMATCH	0x324		Publish configuration for event DEVMATCH
PUBLISH_DEVMISS	0x328		Publish configuration for event DEVMISS
PUBLISH_CRCOK	0x32C		Publish configuration for event CRCOK
PUBLISH_CRCERROR	0x330		Publish configuration for event CRCERROR
PUBLISH_BCMATCH	0x338		Publish configuration for event BCMATCH
PUBLISH_EDEND	0x33C		Publish configuration for event EDEND
PUBLISH_EDSTOPPED	0x340		Publish configuration for event EDSTOPPED
PUBLISH_CCAIDLE	0x344		Publish configuration for event CCAIDLE
PUBLISH_CCABUSY	0x348		Publish configuration for event CCABUSY
PUBLISH_CCASTOPPED	0x34C		Publish configuration for event CCASTOPPED
PUBLISH_RATEBOOST	0x350		Publish configuration for event RATEBOOST
PUBLISH_MHRMATCH	0x354		Publish configuration for event MHRMATCH
PUBLISH_SYNC	0x358		Publish configuration for event SYNC
PUBLISH_CTEPRESENT	0x35C		Publish configuration for event CTEPRESENT
PUBLISH_PLLREADY	0x3B0		Publish configuration for event PLLREADY
PUBLISH_RXADDRESS	0x3BC		Publish configuration for event RXADDRESS
PUBLISH_AUXDATADMAEND	0x3C0		Publish configuration for event AUXDATADMAEND
PUBLISH_CSTONESEND	0x3C8		Publish configuration for event CSTONESEND
SHORTS	0x400		Shortcuts between local events and tasks
INTENSET00	0x488		Enable interrupt
INTENSET01	0x48C		Enable interrupt
INTENCLR00	0x490		Disable interrupt
INTENCLR01	0x494		Disable interrupt
INTENSET10	0x4A8		Enable interrupt
INTENSET11	0x4AC		Enable interrupt
INTENCLR10	0x4B0		Disable interrupt
INTENCLR11	0x4B4		Disable interrupt
MODE	0x500		Data rate and modulation
PHYENDTXDELAY	0x518		Configurable delay of PHYEND event for TX
STATE	0x520		Current radio state
EDCTRL	0x530		IEEE 802.15.4 energy detect control
EDSAMPLE	0x534		IEEE 802.15.4 energy detect level
CCACTRL	0x538		IEEE 802.15.4 clear channel assessment control
DATAWHITE	0x540		Data whitening configuration
AUXDATA.CNF[n]	0x548		AUXDATA configuration
AUXDATADMA[n].ENABLE	0x550		Enable or disable data acquisition
AUXDATADMA[n].PTR	0x554		ACQ DMA pointer
AUXDATADMA[n].MAXCNT	0x558		Maximum number of 32-bit words to transfer
AUXDATADMA[n].AMOUNT	0x55C		Number of 32-bit words transferred in the last transaction
TIMING	0x704		Timing
FREQUENCY	0x708		Frequency
TXPOWER	0x710		Output power
TIFS	0x714		Interframe spacing in μ s
RSSISAMPLE	0x718		RSSI sample
RXGAIN.CONFIG	0x7D4		Override configuration of receiver gain control loop
FREQFINETUNE	0x804		Fine tuning of the RF frequency
FECONFIG	0x908		Config register