

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | |
|-------------------------|-----|--------------|----|----------|----|-------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|--|--|--|--|
| ID | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Reset 0x00000000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| ID | R/W | Field | | Value ID | | Value | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | COMPAREVALUE | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.10.7.48 CLKOUT

Address offset: 0x714

Configuration of clock output

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | | |
|-------------------------|-----|------------|----------|----------|----|-------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|--|--|--|--|--|
| ID | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Reset 0x00000000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | |
| ID | R/W | Field | | Value ID | | Value | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | CLKOUT32K | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Disabled | | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Enabled | | 1 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| B | RW | CLKOUTFAST | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Disabled | | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Enabled | | 1 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.10.7.49 CLKCFG

Address offset: 0x718

Clock Configuration

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | | |
|-------------------------|-----|------------|----|----------|-----------|-------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|--|--|--|--|--|
| ID | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Reset 0x00010001 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | | | | | |
| ID | R/W | Field | | Value ID | | Value | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | RW | CLKFASTDIV | | | 1..255 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| B | W | CLKSEL | | | LFXO | 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | | SystemLFC | 1 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | | LFLPRC | 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.10.7.50 SYSCOUNTER[n].SYSCOUNTERL (n=0..3)

Address offset: 0x720 + (n × 0x10)

The lower 32-bits of the SYSCOUNTER for index [n]

The SYSCOUNTERL must be read before SYSCOUNTERH.

| Bit number | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | | | | |
|-------------------------|-----|-------|----|----------|----|-------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|--|--|--|--|
| ID | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Reset 0x00000000 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| ID | R/W | Field | | Value ID | | Value | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A | R | VALUE | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |