

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------|-----|------------------|----------|-------|----|---|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| Bit number | | | | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| ID | | | | f | e | d | c | b | a | Z | Y | X | W | V | U | T | S | R | Q | P | O | N | M | L | K | J | I | H | G | F | E | D | C | B | A |
| Reset 0x00000000 | | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| ID | R/W | Field | Value ID | Value | | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A-f | RW | PIN[i] (i=0..31) | | | | Pin i | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | W1S | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Low | 0 | | Read: pin driver is low | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | High | 1 | | Read: pin driver is high | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Set | 1 | | Write: writing a '1' sets the pin high; writing a '0' has no effect | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.8.8.3 OUTCLR

Address offset: 0x008

Clear individual bits in GPIO port

Note: Read: reads value of OUT register.

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------|-----|------------------|----------|-------|----|--|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| Bit number | | | | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| ID | | | | f | e | d | c | b | a | Z | Y | X | W | V | U | T | S | R | Q | P | O | N | M | L | K | J | I | H | G | F | E | D | C | B | A |
| Reset 0x00000000 | | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| ID | R/W | Field | Value ID | Value | | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A-f | RW | PIN[i] (i=0..31) | | | | Pin i | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | W1C | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Low | 0 | | Read: pin driver is low | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | High | 1 | | Read: pin driver is high | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Clear | 1 | | Write: writing a '1' sets the pin low; writing a '0' has no effect | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.8.8.4 IN

Address offset: 0x00C

Read GPIO port

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------|-----|------------------|----------|-------|----|-------------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|
| Bit number | | | | 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| ID | | | | f | e | d | c | b | a | Z | Y | X | W | V | U | T | S | R | Q | P | O | N | M | L | K | J | I | H | G | F | E | D | C | B | A |
| Reset 0x00000000 | | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| ID | R/W | Field | Value ID | Value | | Description | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| A-f | R | PIN[i] (i=0..31) | | | | Pin i | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | Low | 0 | | Pin input is low | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | High | 1 | | Pin input is high | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

8.8.8.5 DIR (Retained)

Address offset: 0x010

Direction of GPIO pins

This register is retained.