

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	SRC			Select which LFCLK source is started by the LFCLKSTART task																														
			LFRC	0	32.768 kHz RC oscillator																														
			LFXO	1	32.768 kHz crystal oscillator																														
			LFSYNT	2	32.768 kHz synthesized from HFCLK																														

5.4.3.33 LFCLK.RUN

Address offset: 0x448

Indicates that LFCLKSTART task was triggered

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
ID																																							A
Reset 0x00000000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value																																			Description
A	R	STATUS																																					LFCLKSTART task triggered or not
			NotTriggered	0																																		Task not triggered	
			Triggered	1																																		Task triggered	

5.4.3.34 LFCLK.STAT

Address offset: 0x44C

Copy of LFCLK.SRCCOPY register, set when LFCLKSTARTED event is triggered.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C																B								A		A					
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	R	SRC			Value of LFCLK.SRCCOPY register when LFCLKSTARTED event was triggered																														
			LFRC	0	32.768 kHz RC oscillator																														
			LFXO	1	32.768 kHz crystal oscillator																														
			LFSYNT	2	32.768 kHz synthesized from HFCLK																														
B	R	ALWAYSRUNNING			ALWAYSRUN activated																														
			NotRunning	0	Automatic clock control enabled																														
			Running	1	Oscillator is always running																														
C	R	STATE			LFCLK state (Running between START task and STOPPED event)																														
			NotRunning	0	LFCLK not running																														
			Running	1	LFCLK running																														

5.4.3.35 LFCLK.SRCCOPY

Address offset: 0x450

Copy of LFCLK.SRC register, set when LFCLKSTART task is triggered