

### 8.3.5.11 PUBLISH\_NOTRESOLVED

Address offset: 0x188

Publish configuration for event NOTRESOLVED

### 8.3.5.12 PUBLISH\_ERROR

Address offset: 0x18C

## Publish configuration for event **ERROR**

### 8.3.5.13 INTENSET

Address offset: 0x304

## Enable interrupt

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																										D	C	B	A			
<b>Reset 0x00000000</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>			
ID	R/W	Field	Value ID	Value	Description																											
A	RW	END			Write '1' to enable interrupt for event END																											
		W1S																														
			Set	1	Enable																											
			Disabled	0	Read: Disabled																											
			Enabled	1	Read: Enabled																											