

Might ignore writes when the trace unit is enabled or not idle.

When the sequencer is used, all sequencer state transitions must be programmed with a valid event.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A-H	RW	F[i] (i=0..7)			Forward field.																														
			Disabled	0	The trace event does not affect the sequencer.																														
			Enabled	1	When the event occurs then the sequencer state moves from state n to state n+1.																														
I-P	RW	B[i] (i=0..7)			Backward field.																														
			Disabled	0	The trace event does not affect the sequencer.																														
			Enabled	1	When the event occurs then the sequencer state moves from state n+1 to state n.																														

9.8.1.22 TRCSEQRSTEV

Address offset: 0x118

Moves the sequencer to state 0 when a programmed event occurs.

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Bit number					31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ID																													A	A	A	A	A	A	A	A	
Reset 0x00000000					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value	Description																																
A	RW	EVENT		[0:255]	Select which event should reset the sequencer.																																

9.8.1.23 TRCSEQSTR

Address offset: 0x11C

Use this to set, or read, the sequencer state.

Might ignore writes when the trace unit is enabled or not idle.

Only returns stable data when TRCSTATR.PMSTABLE == 1.

When the sequencer is used, all sequencer state transitions must be programmed with a valid event.

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	STATE			Sets or returns the state of the sequencer.																														
			State0	0	The sequencer is in state 0.																														
			State1	1	The sequencer is in state 1.																														
			State2	2	The sequencer is in state 2.																														
			State3	3	The sequencer is in state 3.																														

9.8.1.24 TRCEXTINSEL

Address offset: 0x120