

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A																															
Reset 0x00000000																																
ID	R/W	Field	Value ID																													
A	RW	EVENTS_STARTED	Description																													
NotGenerated																																
Generated																																

8.19.8.12 EVENTS_STOPPED

Address offset: 0x104

SPI transaction has stopped

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																										
ID	A																																																									
Reset 0x00000000																																																										
ID	R/W	Field	Value ID																																																							
A	RW	EVENTS_STOPPED	Description																																																							
NotGenerated																																																										
Generated																																																										

8.19.8.13 EVENTS_END

Address offset: 0x108

End of RXD buffer and TXD buffer reached

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																										
ID	A																																																									
Reset 0x00000000																																																										
ID	R/W	Field	Value ID																																																							
A	RW	EVENTS_END	Description																																																							
NotGenerated																																																										
Generated																																																										

8.19.8.14 EVENTS_DMA

Peripheral events.

8.19.8.14.1 EVENTS_DMA.RX

Peripheral events.

8.19.8.14.1.1 EVENTS_DMA.RX.END

Address offset: 0x14C

Generated after all MAXCNT bytes have been transferred