

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				V U T S R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
V	RW	CH7LIMITL			Write '1' to enable interrupt for event <a href="#">CH7LIMITL</a>																														
		W1S																																	
			Set	1	Enable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														

### 8.18.11.26 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				V U T S R Q P O N M L K J I H G F E D C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	STARTED W1C			Write '1' to disable interrupt for event <span>STARTED</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
B	RW	END W1C			Write '1' to disable interrupt for event <span>END</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
C	RW	DONE W1C			Write '1' to disable interrupt for event <span>DONE</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
D	RW	RESULTDONE W1C			Write '1' to disable interrupt for event <span>RESULTDONE</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
E	RW	CALIBRATEDONE W1C			Write '1' to disable interrupt for event <span>CALIBRATEDONE</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
F	RW	STOPPED W1C			Write '1' to disable interrupt for event <span>STOPPED</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														
			Enabled	1	Read: Enabled																														
G	RW	CHOLIMITH W1C			Write '1' to disable interrupt for event <span>CHOLIMITH</span>																														
			Clear	1	Disable																														
			Disabled	0	Read: Disabled																														