Enable or disable interrupt

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C B A	
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
ID	R/W	Field	Value ID	Value	Description	
A	RW	POFWARN			Enable or disable interrupt for event POFWARN	
			Disabled	0	Disable	
			Enabled	1	Enable	
B	RW	SLEEPENTER			Enable or disable interrupt for event SLEEPENTER	
			Disabled	0	Disable	
			Enabled	1	Enable	
C	RW	SLEEPEXIT			Enable or disable interrupt for event SLEEPEXIT	
			Disabled	0	Disable	
			Enabled	1	Enable	

5.6.1.11 INTENSET

Address offset: 0x304

Enable interrupt

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C B A	
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
ID	R/W	Field	Value ID	Value	Description	
A	RW W1S	POFWARN			Write '1' to enable interrupt for event POFWARN	
			Set	1	Enable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
B	RW W1S	SLEEPENTER			Write '1' to enable interrupt for event SLEEPENTER	
			Set	1	Enable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	
C	RW W1S	SLEEPEXIT			Write '1' to enable interrupt for event SLEEPEXIT	
			Set	1	Enable	
			Disabled	0	Read: Disabled	
			Enabled	1	Read: Enabled	

5.6.1.12 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
ID					C B A	
Reset 0x00000000					0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
ID	R/W	Field	Value ID	Value	Description	
A	RW W1C	POFWARN			Write '1' to disable interrupt for event POFWARN	

NORDIC
SEMICONDUCTOR