

Figure 8-36. Standby Mode Pin Control

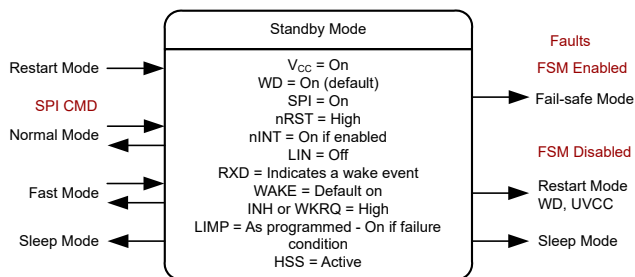


Figure 8-37. Standby Mode SPI Control

8.4.6 Restart Mode

Restart mode is a transitional mode. This mode can be entered from any of the other modes depending upon whether fail-safe mode is disabled. In this mode, the LDO is ramping when coming from sleep mode or fail-safe mode where the LDO was turned off, like a TSD event. Once $V_{CC} \geq UV_{CC}$ for t_{RSTN_act} (~2 ms), the device enters standby mode. While in restart mode, the nRST pin is latched low. Each time restart mode is entered the restart mode counter is incremented.

Note

The SWE timer starts when the device enters restart mode. If the SWE timer times out, the device enters fail-safe mode. If fail-safe mode is disabled, the device enters sleep mode.

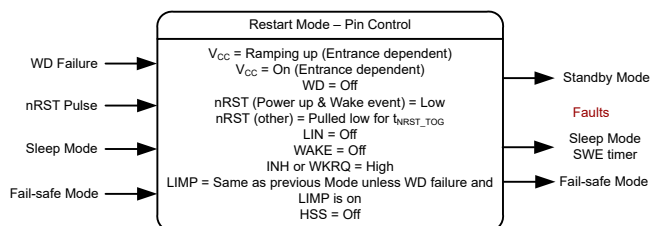


Figure 8-38. Restart Mode Pin Control

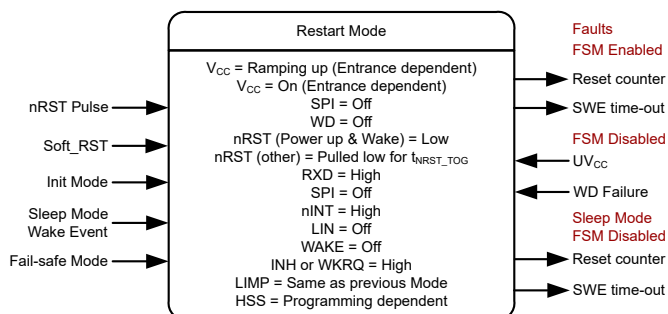


Figure 8-39. Restart Mode SPI Control