

9.8.1.34 TRCITATBIDR

Address offset: 0xEE4

Sets the state of output pins.

Bit number	31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
ID	G F E D C B A
Reset 0x00000000	0 0
ID R/W Field	Value ID Value Description
A-G RW ID[i] (i=0..6)	Drives the ATIDM1[i] output pin.

9.8.1.35 TRCITIATBINR

Address offset: 0xEF4

Reads the state of the input pins.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																														B	A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																											
A	RW	ATVALID			Returns the value of the ATVALIDMI input pin.																											
B	RW	AFREADY			Returns the value of the AFREADYMI input pin.																											

9.8.1.36 TRCITIATBOUTR

Address offset: 0xEFC

Sets the state of the output pins.

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	B	A																														
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value	Description																											
A	RW	ATVALID			Drives the ATVALIDDMI output pin.																											
B	RW	AFREADY			Drives the AFREADYDMI output pin.																											