

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	W	TASKS_TRIGGER						VPR task [n] register																											
				If RTPs are not enabled: writes to registers 15..0 are ignored, other read/write accesses operate normally																															
				If RTPs are enabled: reads or writes to these registers will be stalled if there is a simultaneous VPR CSR write access																															
				to any CSR register.																															
			Trigger	1				Trigger task																											

8.26.1.2 SUBSCRIBE_TRIGGER[n] (n=16..19)

Address offset: 0x080 + (n × 0x4)

Subscribe configuration for task TASKS_TRIGGER[n]

If RTPs are not enabled: writes to registers 15..0 are ignored, other read/write accesses operate normally

If RTPs are enabled: reads or writes to these registers will be stalled if there is a simultaneous VPR CSR write access

to any CSR register.

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value				Description																											
A	RW	EN						Subscription enable bit																											
								Its value depends on OR between bit 31 and bit 0 of previously written value																											
			Disabled	0x0				Disable subscription																											
			Enabled	0x1				Enable subscription																											

8.26.1.3 EVENTS_TRIGGERED[n] (n=16..22)

Address offset: 0x100 + (n × 0x4)

VPR event [n] register

If RTPs are not enabled: writes to registers 15..0 are ignored, other read/write accesses operate normally

If RTPs are enabled: reads or writes to these registers will be stalled if there is a simultaneous VPR CSR write access

to any CSR register.