

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C B																												A A A A			
Reset 0x00000002				0 1 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	SLEEPSTATE			Sleep State																														
			WAIT	0x0	Sleep is not turning off the clock																														
			RESET	0x2	Sleep state default reset value. Going to sleep with sleep state = RESET has the same effect as going to sleep with sleep state = WAIT																														
			SLEEP	0x5	Sleep is turning the clock off																														
			DEEPSLEEP	0x7	Sleep is turning the clock off and power is turned off																														
			HIBERNATE	0xF	sleep is turning the clock off and all the registers are saved automatically, restart by a reset																														
B	RW	RETURNSTOSLEEP			Return to Sleep																														
					Forces the CPU to return to sleep when it returns in a non handler program																														
			Disabled	0x0																															
			Enabled	0x1																															
C	RW	STACKONSLEEP			Stack on Sleep																														
					Force CPU to stack the context before going to sleep : this is used in order to have a fast wake up																														
			Disabled	0x0																															
			Enabled	0x1																															

### 8.26.3.35 NORDIC.VPRNORDICFEATURESDISABLE

Address offset: 0x7C2

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C C																															

### 8.26.3.36 NORDIC.VIOPINS

Address offset: 0x7C3

VPR pins used for Real Time Peripherals VIO

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x0000FFFF				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1																															
ID	R/W	Field	Value ID	Value				Description																											
A	R	VAL						VPR pins used for Real Time Peripherals VIO																											