Table 8-11. DEVICE_ID_y Register Field Descriptions

Bit	Field	Type	Reset	Description
7-0	DEVICE_ID	R	0b	The DEVICE_ID[1:8] registers determine the part number of the device. The reset values and value of each DEVICE_ID register are listed for the corresponding register address Address 00h = 54h = T Address 01h = 4Ch = L Address 02h = 49h = I Address 03h = 31h = 1 Address 04h = 34h = 4 Address 05h = 33h = 3 Address 06h = 31h = 1 Address 07h = 33h = 3 for 3.3 V LDO Address 07h = 35h = 5 for 5 V LDO

8.6.2 REV_ID_MAJOR Register (Address = 8h) [reset = 01h]

REV_ID_MAJOR is shown in Figure 8-56 and described in Table 8-12.

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Major Revision

Figure 8-56. REV_ID_MAJOR Register

7	6	5	4	3	2	1	0
Major_Revision							
R-01h							

Table 8-12. REV_ID_MAJOR Register Field Descriptions

Bit	Field	Type	Reset	Description
7-0	Major_Revision	R	01h	Major die revision

8.6.3 REV_ID_MINOR Register (Address = 9h) [reset = 0h]

REV_ID_MINOR is shown in Figure 8-57 and described in Table 8-13.

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Minor Revision

Figure 8-57. REV_ID_MINOR Register

7	6	5	4	3	2	1	0
Minor_Revision							
R-0b							

Table 8-13. REV_ID_MINOR Register Field Descriptions

Bit	Field	Type	Reset	Description
7-0	Minor_Revision	R	0b	Minor die revision

8.6.4 CRC_CNTL Register (Address = Ah) [reset = 0h]

CRC_CNTL is shown in Figure 8-58 and described in Table 8-14.

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SPI CRC register controls the CRC function. CRC_DIS bit can disable the CRC function.

Figure 8-58. CRC_CNTL Register

7	6	5	4	3	2	1	0
CRC_CNTL_RSVD							CRC_EN