8.17.14.79 PUBLISH_CCABUSY

Address offset: 0x348

Publish configuration for event CCABUSY

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event CCABUSY will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.80 PUBLISH_CCASTOPPED

Address offset: 0x34C

Publish configuration for event CCASTOPPED

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event CCASTOPPED will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.81 PUBLISH_RATEBOOST

Address offset: 0x350

Publish configuration for event RATEBOOST

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0		
ID				B		A A A A A A A A
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
ID	R/W	Field	Value ID	Value	Description	
A	RW	CHIDX		[0..255]	DPPI channel that event RATEBOOST will publish to	
B	RW	EN				
			Disabled	0	Disable publishing	
			Enabled	1	Enable publishing	

8.17.14.82 PUBLISH_MHRMATCH

Address offset: 0x354

Publish configuration for event MHRMATCH

NORDIC
SEMICONDUCTOR