

Pin	Clock pin	Name	Function	Description	Dedicated function
38		P1.10 ASI[2] RADIO[1]	Digital I/O Digital I/O Digital I/O	General purpose I/O TAMPC active shield 2 input RADIO DFEGPIO	TAMPC RADIO
39	Yes	P1.11 ASO[3] RADIO[2] AIN4	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 output RADIO DFEGPIO Analog input	TAMPC RADIO
40	Yes	P1.12 ASI[3] RADIO[3] AIN5	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 3 input RADIO DFEGPIO Analog input	TAMPC RADIO
41		P1.13 RADIO[4] AIN6	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
42		P1.14 RADIO[5] AIN7	Digital I/O Digital I/O Analog input	General purpose I/O RADIO DFEGPIO Analog input	RADIO
43		DECA	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
44		VSS	Power	Ground	
45		DECD	Power	0.9 V regulator supply decoupling	
46		DCC	Power	DC/DC regulator output	
47		VDD	Power	Power supply	
48		VDD	Power	Power supply	
49		VSS	Power	Ground pad (die pad)	

Table 79: QFN48 pin assignments