

8.5.7.27 HYST

Address offset: 0x538

Comparator hysteresis enable

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	HYST			Comparator hysteresis																														
			NoHyst	0	Comparator hysteresis disabled																														
			Hyst40mV	1	Comparator hysteresis enabled																														

8.5.7.28 ISOURCE

Address offset: 0x53C

Current source select on analog input

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	ISOURCE			Current source select on analog input																														
			Off	0	Current source disabled																														
			Ien2uA5	1	Current source enabled (+/- 2.5 uA)																														
			Ien5uA	2	Current source enabled (+/- 5 uA)																														
			Ien10uA	3	Current source enabled (+/- 10 uA)																														

8.6 ECB — AES electronic codebook mode encryption

The AES electronic codebook mode encryption (ECB) can be used for a range of cryptographic functions like hash generation, digital signatures, and keystream generation for data encryption/decryption. The ECB encryption block supports 128 bit AES encryption (encryption only, not decryption).

The main features of ECB are:

- 128-bit AES encryption
- Supports standard AES ECB block encryption
- Memory-to-memory operations using Scatter/Gather DMA

The inputs and outputs of the ECB are illustrated below.

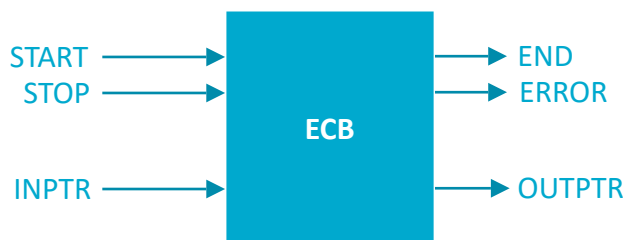


Figure 57: ECB block diagram