

## 6.3.5 Registers

### Instances

Instance	Domain	Base address	TrustZone			Split access	Description
			Map	Att	DMA		
PPIB00 : S	GLOBAL	0x50043000	US	S	NA	No	PPI bridge PPIB00
PPIB00 : NS		0x40043000					
PPIB01 : S	GLOBAL	0x50044000	US	S	NA	No	PPI bridge PPIB01
PPIB01 : NS		0x40044000					
PPIB10 : S	GLOBAL	0x50083000	US	S	NA	No	PPI bridge PPIB10
PPIB10 : NS		0x40083000					
PPIB11 : S	GLOBAL	0x50084000	US	S	NA	No	PPI bridge PPIB11
PPIB11 : NS		0x40084000					
PPIB20 : S	GLOBAL	0x500C3000	US	S	NA	No	PPI bridge PPIB20
PPIB20 : NS		0x400C3000					
PPIB21 : S	GLOBAL	0x500C4000	US	S	NA	No	PPI bridge PPIB21
PPIB21 : NS		0x400C4000					
PPIB22 : S	GLOBAL	0x500C5000	US	S	NA	No	PPI bridge PPIB22
PPIB22 : NS		0x400C5000					
PPIB30 : S	GLOBAL	0x50103000	US	S	NA	No	PPI bridge PPIB30
PPIB30 : NS		0x40103000					

### Configuration

Instance	Domain	Configuration
PPIB00 : S	GLOBAL	Bridges PPI channels 0-7 between PPIB_00 (MCU) and PPIB_10 (RADIO)
PPIB00 : NS		
PPIB01 : S	GLOBAL	Bridges PPI channels 0-7 between PPIB_01 (MCU) and PPIB_20 (PERI)
PPIB01 : NS		
PPIB10 : S	GLOBAL	Bridges PPI channels 0-7 between PPIB_10 (RADIO) and PPIB_00 (MCU)
PPIB10 : NS		
PPIB11 : S	GLOBAL	Bridges PPI channels 0-15 between PPIB_11 (RADIO) and PPIB_21 (PERI)
PPIB11 : NS		
PPIB20 : S	GLOBAL	Bridges PPI channels 0-7 between PPIB_20 (PERI) and PPIB_01 (MCU)
PPIB20 : NS		
PPIB21 : S	GLOBAL	Bridges PPI channels 0-15 between PPIB_21 (PERI) and PPIB_11 (RADIO)
PPIB21 : NS		
PPIB22 : S	GLOBAL	Bridges PPI channels 0-3 between PPIB_22 (PERI) and PPIB_30 (LP)
PPIB22 : NS		
PPIB30 : S	GLOBAL	Bridges PPI channels 0-3 between PPIB_30 (LP) and PPIB_22 (PERI)
PPIB30 : NS		