

Pin	Clock pin	Name	Function	Description	Dedicated function
38		P1.10	Digital I/O	General purpose I/O	TAMPC RADIO
		ASI [2]	Digital I/O	TAMPC active shield 2 input	
		RADIO [1]	Digital I/O	RADIO DFEGPIO	
39	Yes	P1.11	Digital I/O	General purpose I/O	TAMPC RADIO
		ASO [3]	Digital I/O	TAMPC active shield 3 output	
		RADIO [2]	Digital I/O	RADIO DFEGPIO	
		AIN4	Analog input	Analog input	
40	Yes	P1.12	Digital I/O	General purpose I/O	TAMPC RADIO
		ASI [3]	Digital I/O	TAMPC active shield 3 input	
		RADIO [3]	Digital I/O	RADIO DFEGPIO	
		AIN5	Analog input	Analog input	
41		P1.13	Digital I/O	General purpose I/O	RADIO
		RADIO [4]	Digital I/O	RADIO DFEGPIO	
		AIN6	Analog input	Analog input	
42		P1.14	Digital I/O	General purpose I/O	RADIO
		RADIO [5]	Digital I/O	RADIO DFEGPIO	
		AIN7	Analog input	Analog input	
43		DECA	Power	0.9 V regulator supply decoupling	Must be connected to DECRF
44		VSS	Power	Ground	
45		DECD	Power	0.9 V regulator supply decoupling	
46		DCC	Power	DC/DC regulator output	
47		VDD	Power	Power supply	
48		VDD	Power	Power supply	
49		VSS	Power	Ground pad (die pad)	

Table 79: QFN48 pin assignments