2.4GHz Loop Chip Antenna

AANI-CH-0070

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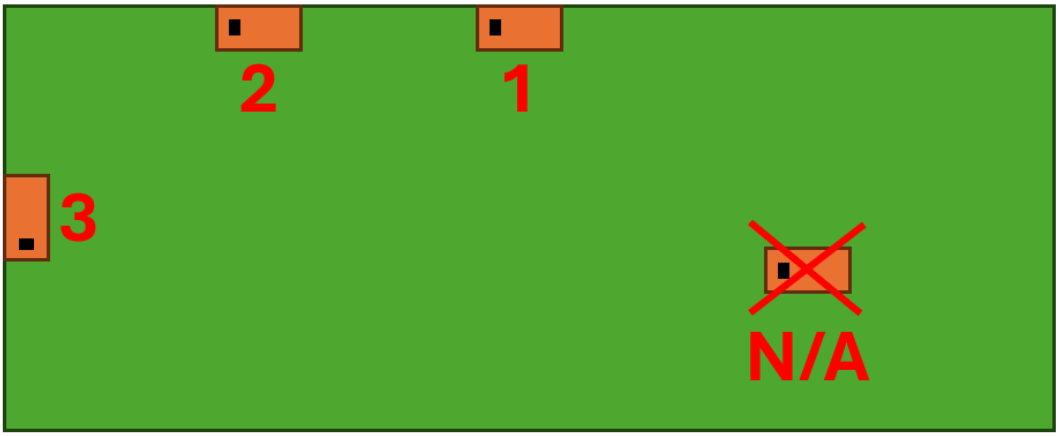
1.0 x 0.5 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

General Implementation Guidelines

The antenna can be positioned in different ways, although there are some positions which are more beneficial. The below illustration shows a typical PCB with examples on different antenna positions (the GND cutout is not visualized):

- The optimal position is usually option 1. Options 2 and 3 are also possible.
- The antenna must be placed along the PCB edge, i.e., it cannot be placed in the middle (see “N/A”).
- Option 2 or 3 may be the best option for PCB’s much larger than the evaluation board.



The rectangular copper cutout in the footprint must extend through all layers of the PCB stack-up, ensuring there is **no copper on any layer in this area**. Additionally, a robust via structure around the cutout and along the edge of the ground plane is highly recommended for optimal performance.

It is important to note that plastic and metal parts in close proximity to antennas may significantly affect antenna tuning and performance. For instance, a plastic housing above the antenna often causes the resonant frequency to shift downward. Since such effects are challenging to predict without detailed design information, it is recommended to measure the antenna performance in the final device after implementation. To compensate for potential frequency shifts, implementing a matching network on the antenna feed is advisable.

Another general consideration for surface-mounted antennas relates to PCB population. Electrical components placed near the antenna may impact its tuning and radiation performance. To mitigate this, components in the surrounding area should be positioned below a topographical slope. This slope should begin at the PCB level near the antenna's designated keep-out zone and gradually increase in height as distance from the antenna grows.

For technical assistance, please contact Abracon online support through our online support platform.