

For the device to function properly, the exposed die pad (pin 49) must be connected to ground (**VSS**, pins 32 and 44).

10.1.5 QFN52 (QGAA) package pin assignments

The QFN52 pin assignment figure and table describe the pinouts for this variant of the device.

Pins that can be used as clock signals are shown in red in the figure. For more information about clock pins, see [Clock pins on page 860](#).

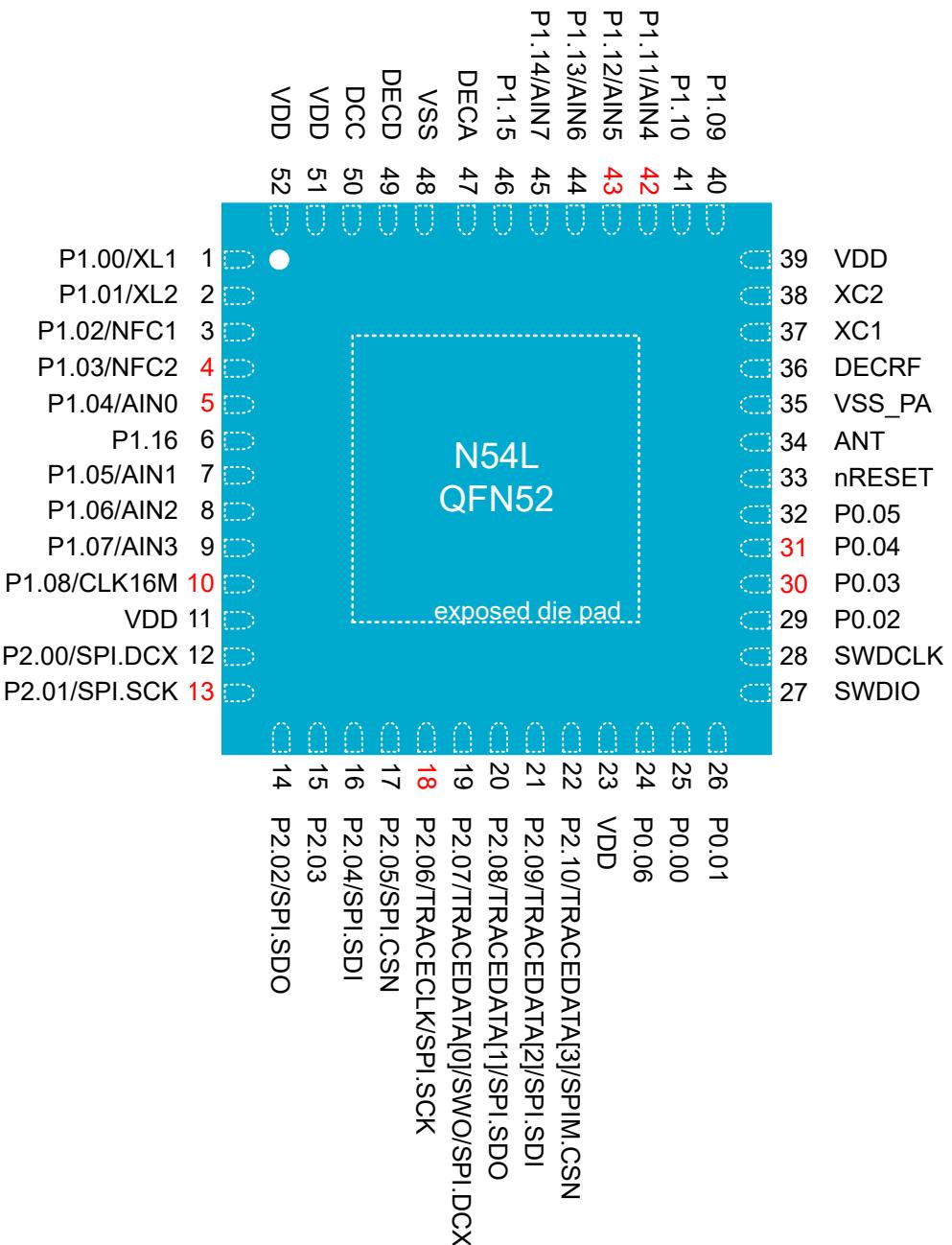


Figure 174: QFN52 pin assignments, top view