

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			V	U	T	S	R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C	B	A										
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value	Description																													
V	RW	CH7LIMITL			Write '1' to enable interrupt for event <a href="#">CH7LIMITL</a>																													
		W1S			Set	1	Enable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											

## 8.18.11.26 INTENCLR

Address offset: 0x308

Disable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			V	U	T	S	R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C	B	A										
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
ID	R/W	Field	Value ID	Value	Description																													
A	RW	STARTED			Write '1' to disable interrupt for event <a href="#">STARTED</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
B	RW	END			Write '1' to disable interrupt for event <a href="#">END</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
C	RW	DONE			Write '1' to disable interrupt for event <a href="#">DONE</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
D	RW	RESULTDONE			Write '1' to disable interrupt for event <a href="#">RESULTDONE</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
E	RW	CALIBRATEDONE			Write '1' to disable interrupt for event <a href="#">CALIBRATEDONE</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
F	RW	STOPPED			Write '1' to disable interrupt for event <a href="#">STOPPED</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											
					Enabled	1	Read: Enabled																											
G	RW	CHOLIMITH			Write '1' to disable interrupt for event <a href="#">CHOLIMITH</a>																													
		W1C			Clear	1	Disable																											
					Disabled	0	Read: Disabled																											