

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				C B A																															
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	FIELDDETECTED_ACTIVATE			Shortcut between event FIELDDETECTED and task ACTIVATE																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
B	RW	FIELDLOST_SENSE			Shortcut between event FIELDLOST and task SENSE																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
C	RW	TXFRAMEEND_ENABLERXDATA			Shortcut between event TXFRAMEEND and task ENABLERXDATA																														
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														

8.13.14.48 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				O N M																L				K J I				H G F E D C B A							
Reset 0x00000000				0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	READY			Enable or disable interrupt for event READY																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
B	RW	FIELDDETECTED			Enable or disable interrupt for event FIELDDETECTED																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
C	RW	FIELDLOST			Enable or disable interrupt for event FIELDLOST																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
D	RW	TXFRAMESTART			Enable or disable interrupt for event TXFRAMESTART																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
E	RW	TXFRAMEEND			Enable or disable interrupt for event TXFRAMEEND																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
F	RW	RXFRAMESTART			Enable or disable interrupt for event RXFRAMESTART																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
G	RW	RXFRAMEEND			Enable or disable interrupt for event RXFRAMEEND																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
H	RW	ERROR			Enable or disable interrupt for event ERROR																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
I	RW	RXERROR			Enable or disable interrupt for event RXERROR																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
J	RW	ENDRX			Enable or disable interrupt for event ENDRX																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														