

Pin	Clock pin	Name	Function	Description	Dedicated function
			Digital I/O	UARTE TXD	UARTE00/21
D5	Yes	P1.03 NFC2	Digital I/O NFC input	General purpose I/O NFC antenna connection	
D6	Yes	P1.04 ASO[0] AIN0	Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 0 output Analog input	TAMPC
D7		P1.06 ASO[1] AIN2	Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 1 output Analog input	TAMPC
E1	Yes	P0.03	Digital I/O Digital I/O	General purpose I/O GRTC PWM	GRTC
E2		P0.02	Digital I/O	General purpose I/O	
E3		SWDCLK	Debug	Serial wire clock. Input with on-chip pull-down.	
E4		P2.07 TRACEDATA[0] SWO	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.7 Trace data Serial wire output (SWO) SPIM DCX UARTE RXD	FLPR Trace Trace SPIM00/21 UARTE00/21
E5		P2.03	Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.3 QSPI D2	FLPR FLPR (QSPI)
E6		P1.05 ASI[0] RADIO[6] AIN1	Digital I/O Digital I/O Digital I/O Analog input	General purpose I/O TAMPC active shield 0 input RADIO DFEGPIO Analog input	TAMPC RADIO
E7		P1.07 ASI[1]	Digital I/O Digital I/O	General purpose I/O TAMPC active shield 1 input	TAMPC