

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															

8.25.13.30 CONFIG

Address offset: 0x56C

Configuration of parity, hardware flow control, framesize, and packet timeout.

Bit number				31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID																						G	F	E	E	E	E	D										
Reset 0x00001000				0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0		
ID	R/W	Field	Value ID	Value	Description																																	
A	RW	HWFC			Hardware flow control																																	
			Disabled	0	Disabled																																	
			Enabled	1	Enabled																																	
B	RW	PARITY			Parity																																	
			Excluded	0x0	Exclude parity bit																																	
			Included	0x7	Include parity bit																																	
C	RW	STOP			Stop bits																																	
			One	0	One stop bit																																	
			Two	1	Two stop bits																																	
D	RW	PARITYTYPE			Even or odd parity type																																	
			Even	0	Even parity																																	
			Odd	1	Odd parity																																	
E	RW	FRAMESIZE			Set the data frame size																																	
			9bit	9	9 bit data frame size. 9th bit is treated as address bit.																																	