

8.24.10.27 ERRORSRC

Address offset: 0x4D0

Error source

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ID																																			
Reset 0x00000000																																			
ID	R/W	Field	Value ID	Value	Description																														
A	RW	OVERFLOW			RX buffer overflow detected, and prevented																														
		W1C																																	
			NotDetected	0	Error did not occur																														
			Detected	1	Error occurred																														
B	RW	DNACK			NACK sent after receiving a data byte																														
		W1C																																	
			NotReceived	0	Error did not occur																														
			Received	1	Error occurred																														
C	RW	OVERREAD			TX buffer over-read detected, and prevented																														
		W1C																																	
			NotDetected	0	Error did not occur																														
			Detected	1	Error occurred																														

8.24.10.28 MATCH

Address offset: 0x4D4

Status register indicating which address had a match

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ID																																			
Reset 0x00000000																																			
ID	R/W	Field	Value ID	Value	Description																														
A	R	MATCH	[0..1]		Indication of which address in ADDRESS that matched the incoming address																														

8.24.10.29 ENABLE

Address offset: 0x500

Enable TWIS

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
ID																																			
Reset 0x00000000																																			
ID	R/W	Field	Value ID	Value	Description																														
A	RW	ENABLE			Enable or disable TWIS																														
			Disabled	0	Disable TWIS																														
			Enabled	9	Enable TWIS																														

8.24.10.30 ADDRESS[n] (n=0..1)

Address offset: 0x588 + (n × 0x4)

TWI slave address n