

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				M L K J I H G F																E D C B A															
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
C	RW	LASTTX_STOP	Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
			Shortcut between event <a href="#">LASTTX</a> and task <a href="#">STOP</a>																																
D	RW	LASTRX_DMA_TX_START	Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
			Shortcut between event <a href="#">LASTRX</a> and task <a href="#">DMA.TX.START</a>																																
E	RW	LASTRX_STOP	Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														
			Shortcut between event <a href="#">LASTRX</a> and task <a href="#">STOP</a>																																
F-I	RW	DMA_RX_MATCH[i]_DMA_RX_ENABLEMA +1)%4] (i=0..3)			Shortcut between event <a href="#">DMA.RX.MATCH[n]</a> and task <a href="#">DMA.RX.ENABLEMATCH[(i+1)%4]</a>																														
					Allows daisy-chaining match events.																														
			Disabled	0	Disable shortcut																														
J-M	RW	DMA_RX_MATCH[i]_DMA_RX_DISABLEMATCH[i] (i=0..3)	Enabled	1	Enable shortcut																														
			Shortcut between event <a href="#">DMA.RX.MATCH[n]</a> and task <a href="#">DMA.RX.DISABLEMATCH[n]</a>																																
			Disabled	0	Disable shortcut																														
			Enabled	1	Enable shortcut																														

## 8.23.10.22 INTEN

Address offset: 0x300

Enable or disable interrupt

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				O N M L K J I H G F																E D				C				B				A			
Reset 0x00000000				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0																															
ID	R/W	Field	Value ID	Value	Description																														
A	RW	STOPPED			Enable or disable interrupt for event <a href="#">STOPPED</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
B	RW	ERROR			Enable or disable interrupt for event <a href="#">ERROR</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
C	RW	SUSPENDED			Enable or disable interrupt for event <a href="#">SUSPENDED</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
D	RW	LASTRX			Enable or disable interrupt for event <a href="#">LASTRX</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
E	RW	LASTTX			Enable or disable interrupt for event <a href="#">LASTTX</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
F	RW	DMARXEND			Enable or disable interrupt for event <a href="#">DMARXEND</a>																														
			Disabled	0	Disable																														
			Enabled	1	Enable																														
G	RW	DMARXREADY			Enable or disable interrupt for event <a href="#">DMARXREADY</a>																														