

8.26.3.53 NORDIC.WAIT0

Address offset: 0x7DA

Wait 0

Writing to this register will stall the CPU until CNT0 reaches 0

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
ID																		B	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
Reset 0x00000000																		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ID	R/W	Field	Value ID	Value	Description																														
A	W	DATA			Value to write to CNT0																														
B	W	WRITEDATA																																	
		WAIT	0x0	Wait until CNT0 reaches 0																															
		WRITE	0x1	Write DATA to CNT0 and then wait until CNT0 reaches 0																															

8.26.3.54 NORDIC.WAIT1

Address offset: 0x7DB

Wait 1

Writing to this register will stall the CPU until CNT1 reaches 0

8.26.3.55 NORDIC.WAIT

Address offset: 0x7DC

Wait

Writing any value to register will stall the CPU until CNT reaches 0 in TRIGCOMB mode

8.26.3.56 NORDIC.TASKS

Address offset: 0x7E0

DPPI Tasks

CSR view of TASKS_TRIGGER[31:0] APB registers. Real Time Peripherals VEVIF

Shares a physical register with IN (lower 16 bits).