Figure 8-81. TIMER1_CONFIG Register (continued)

R/W-0h	R-0b	R/W-000b

Table 8-37. TIMER1_CONFIG Register Field Descriptions

Bit	Field	Type	Reset	Description
7-4	TIMER1_ON_WIDTH	R/W	0b	Sets the high side switch on time (ms) for timer 1 0000b = Off (HSS is high impedance) 0001b = 0.1 0010b = 0.3 0011b = 0.5 0100b = 1 0101b = 10 0110b = 20 0111b = 30 1000b = 40 1001b = 50 1010b = 60 1011b = 80 1100b = 100 1101b = 150 1110b = 200 1111b = On (HSS is on 100%) **Note** NOTE: t_{WK_CYC} which is set by $t_{WK\_CYC\_SET}$ works with these times to determine if a state change has taken place on the WAKE pin. When t_{WK_CYC} is set at 65 µs the 100 µs on width time cannot be used.
3	TIMER1_RSVD	R	0b	Reserved
2-0	TIMER1_PERIOD	R/W	0b	Sets the timer period (ms) for timer 1 000b = 10 001b = 20 010b = 50 011b = 100 100b = 200 101b = 500 110b = 1000 111b = 2000

8.6.28 TIMER2_CONFIG (Address = 26h) [reset = 00h]

TIMER2_CONFIG is shown in Figure 8-82 and described in Table 8-38

Return to Summary Table.

Sets timer 2 period and on time. Careful selection is important as selecting a 200ms on width and a 10ms period is not possible.

Figure 8-82. TIMER2_CONFIG Register

7	6	5	4	3	2	1	0
TIMER2_ON_WIDTH				TIMER2_RSVD	TIMER2_PERIOD		
R/W-0h				R-0b	R/W-000b		