

## 8.21.2.8 INTENCLR

Address offset: 0x308

Disable interrupt

### 8.21.2.9 TEMP

Address offset: 0x508

Temperature in °C (0.25° steps)

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A	
<b>Reset 0x00000000</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>		
ID	R/W	Field	Value ID	Value	Description																											
A	R	TEMP		Temperature in °C (0.25° steps)																												
				Result of temperature measurement. Die temperature in °C, 2's complement format, 0.25 °C steps																												
				11 bits sign extended to 32 bits, with 2 LSBs as fractional bits.																												
				Decision point: DATARDY																												

8 21.2.10 A0

Address offset: 0x520

Slope of 1st piece wise linear function

8.21.2.11 A1

Address offset: 0x524

Slope of 2nd piece wise linear function