

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0xFFFFFFFF				1 1																															
ID	R/W	Field	Value ID	Value								Description																							
A	R	HWID		Identification number for the HW																															

4.2.4.1.1.2 INFO.DEVICEID[n] (n=0..1)

Address offset: 0x304 + (n × 0x4)

Device identifier

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0xFFFFFFFF				1 1																															
ID	R/W	Field	Value ID	Value				Description																											
A	R	DEVICEID						64 bit unique device identifier																											
				DEVICEID[0] contains the least significant bits of the device identifier.																															
				DEVICEID[1] contains the most significant bits of the device identifier.																															

4.2.4.1.1.3 INFO.UUID[n] (n=0..3)

Address offset: 0x30C + (n × 0x4)

128-bit Universally Unique Identifier (UUID).

Bit number					31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID					A A																															
Reset 0xFFFFFFFF					1 1																															
ID	R/W	Field	Value ID	Value	Description																															
A	R	UUID			Device UUID [n].																															
					The DEVICE.UUID[0] contains the least significant bits of the device identifier.																															

4.2.4.1.1.4 INFO.PART

Address offset: 0x31C

Part code

Bit number				31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0																															
ID				A A																															
Reset 0xFFFFFFFF				1 1																															
ID	R/W	Field	Value ID	Value																Description															
A	R	PART																		Part code															
			N54L15	0x00054B15																nRF54L15															
			N54L10	0x00054B10																nRF54L10															
			N54L05	0x00054B05																nRF54L05															
			Unspecified	0xFFFFFFFF																Unspecified															

4.2.4.1.1.5 INFO.VARIANT

Address offset: 0x320

Part Variant, Hardware version and Production configuration