

### 8.4.7.13 MACSTATUS

Address offset: 0x400

MAC check result

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A			
<b>Reset 0x00000000</b>		<b>0 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	R	MACSTATUS			The result of the MAC check performed during the previous decryption operation
			CheckFailed	0	MAC check failed
			CheckPassed	1	MAC check passed

### 8.4.7.14 ERRORSTATUS

Address offset: 0x404

Error status

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A A A			
<b>Reset 0x00000000</b>		<b>0 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	R	ERRORSTATUS			Error status when the ERROR event is generated
			NoError	0	No errors have occurred
			PrematureInptrEnd	1	End of INPTR job list before CCM data structure was read.
			PrematureOutptrEnd	2	End of OUTPTR job list before CCM data structure was read.
			EncryptionTooSlow	3	Encryption of the unencrypted CCM data structure did not complete in time.
			DmaError	4	Bus error during DMA access.

### 8.4.7.15 ENABLE

Address offset: 0x500

Enable

Bit number		31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0			
ID		A A			
<b>Reset 0x00000000</b>		<b>0 0</b>			
ID	R/W	Field	Value ID	Value	Description
A	RW	ENABLE			Enable or disable CCM
			Disabled	0	Disable
			Enabled	2	Enable

### 8.4.7.16 MODE

Address offset: 0x504

Operation mode