

Register	Offset	TZ	Description
PROFILING.CLEAR	0x418		Clear the profiling counters.
PROFILING.HIT	0x41C		The cache hit counter for cache region.
PROFILING.MISS	0x420		The cache miss counter for cache region.
PROFILING.LMISS	0x424		The cache line miss counter for cache region.
PROFILING.READS	0x428		Number of reads for cache region.
PROFILING.WRITES	0x42C		Number of writes for cache region.
DEBUGLOCK	0x430		Lock debug mode.
WRITELOCK	0x434		Lock cache updates.

#### 4.2.3.4.1 TASKS\_INVALIDATECACHE

Address offset: 0x008

Invalidate the cache.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.

#### 4.2.3.4.2 TASKS INVALIDATELINE

Address offset: 0x014

Invalidate the line.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.

#### **4.2.3.4.3 TASKS ERASE**

Address offset: 0x020

Erase the cache.

The **STATUS** is updated for this task. This task can be triggered only when the **STATUS** is ready.