

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																													B	A		
<b>Reset 0x00000000</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>			
ID	R/W	Field	Value ID	Value	Description																											
A	R	RAMCORRECTION		This bit indicates that a 1-bit error has been detected and corrected on RAM interface																												
B	R	RAMFAILURE		This bit indicates that an uncorrectable error has been detected on the data RAM interface																												

### 7.8.1.7.52 IKG.START

Address offset: 0x3000

Start register.

### 7.8.1.7.53 IKG.STATUS

Address offset: 0x3004

## Status register.

### 7.8.1.7.54 IKG.INITDATA

Address offset: 0x3008

InitData register.