Pin	Clock pin	Name	Function	Description	Dedicated function
		TRACECLK	Digital I/O	Trace clock	Trace
17		**P2.07** **TRACEDATA[0]** **SWO**	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.7 Trace data Serial wire output (SWO) SPIM DCX UARTE RXD	 FLPR Trace Trace SPIM00/21 UARTE00/21
18		**P2.08** **TRACEDATA[1]**	Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O Digital I/O	General purpose I/O FLPR.8 Trace data SPIM SDO SPIS SDO UARTE TXD	 FLPR Trace SPIM00/21 SPIS00/21 UARTE00/21
19		**VDD**	Power	Power supply	
20		**P0.00**	Digital I/O	General purpose I/O	
21		**P0.01**	Digital I/O	General purpose I/O	
22		**SWDIO**	Debug	Serial wire data. Bidirectional with standard-drive and on-chip pull-up.	
23		**SWDCLK**	Debug	Serial wire clock. Input with on-chip pull-down.	
24	Yes	**P0.04**	Digital I/O Digital I/O	General purpose I/O GRTC CLKOUT32K	 GRTC
25		**nRESET**	Reset	Pin reset with on-chip pull-up	
26		**ANT**	RF	Single ended radio antenna connection	See Reference circuitry on page 889 for guidelines on how to ensure good RF performance
27		**VSS_PA**	Power	Ground (radio supply)	