



Figure 170: Erase all states

Erase all protection

It is possible to prevent the debugger from performing an erase all operation by using either the TAMPC `TAMPC.PROTECT.ERASEPROTECT` register, or the `UICR.ERASEPROTECT` register followed by a device reset.

Once this has been configured, the CTRL-AP `ERASEALL` operation is disabled.

The register `ERASEPROTECT.STATUS` on page 828 holds the status for erase protection.