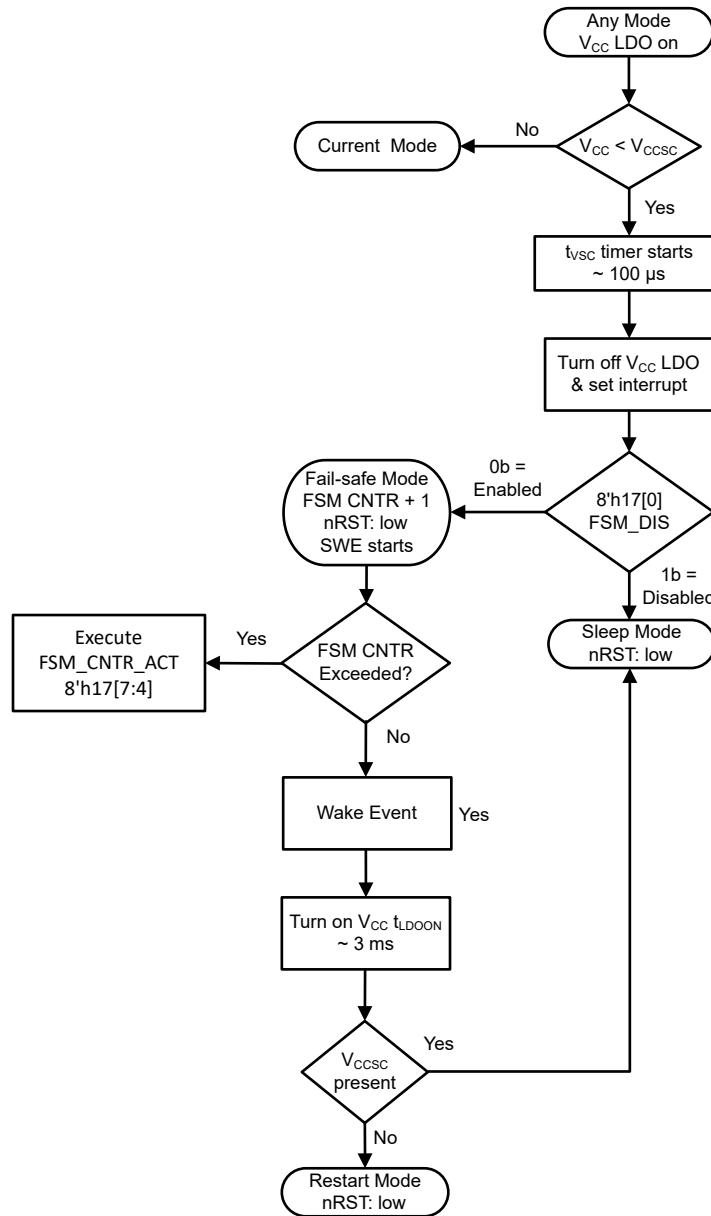


Note

If a short circuit event is detected while the device is in Init mode the device will transition to sleep mode. The device will not have determined the state of the pins and default to Pin control and will need to be powered cycled if the default state is different than what is expected.



If the exit events do not take place before the SWE timer timeouts the device will enter sleep mode

Figure 8-15. V_{CCSC}, short to ground

8.3.22.9.2 Output Capacitance Selection

For stable operation over the full temperature range and with load currents up to 125 mA on V_{CC}, a certain capacitance is expected, and depends upon the minimum load current. To support no load to full load, a value of 10 μ F and ESR smaller than 2 Ω is needed. For 50 μ A to full load, a smaller capacitance can be used but is system dependent and should be selected that will meet LIN to V_{CC} cross talk compliance during DPI testing. The low ESR recommendation is to improve the load transient performance.