

7.8.6.6.3 INTEN

Address offset: 0x300

Enable or disable interrupt

7.8.6.6.4 INTENSET

Address offset: 0x304

Enable interrupt

Bit number	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID																														B	A	
Reset 0x00000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
ID	R/W	Field	Value ID	Value		Description																										
A	RW	TAMPER				Write '1' to enable interrupt for event TAMPER																										
		W1S		Set	1	Enable																										
				Disabled	0	Read: Disabled																										
				Enabled	1	Read: Enabled																										
B	RW	WRITEERROR				Write '1' to enable interrupt for event WRITEERROR																										
		W1S		Set	1	Enable																										
				Disabled	0	Read: Disabled																										
				Enabled	1	Read: Enabled																										

7.8.6.6.5 INTENCLR

Address offset: 0x308

Disable interrupt