

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C	B	A															
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID	Value	Description																													
		Enabled	1	Enable shortcut																														
P	RW	PHYEND_DISABLE			Shortcut between event PHYEND and task DISABLE																													
			Disabled	0	Disable shortcut																													
			Enabled	1	Enable shortcut																													
Q	RW	PHYEND_START			Shortcut between event PHYEND and task START																													
			Disabled	0	Disable shortcut																													
			Enabled	1	Enable shortcut																													

8.17.14.90 INTENSET00

Address offset: 0x488

Enable interrupt

Bit number			31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ID			W	V	U	T	S	R	Q	P	O	M	L	K	J	I	H	G	F	E	D	C	B	A										
Reset 0x00000000			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
ID	R/W	Field	Value ID	Value	Description																													
A	RW	READY			Write '1' to enable interrupt for event READY																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
B	RW	TXREADY			Write '1' to enable interrupt for event TXREADY																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
C	RW	RXREADY			Write '1' to enable interrupt for event RXREADY																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
D	RW	ADDRESS			Write '1' to enable interrupt for event ADDRESS																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
E	RW	FRAMESTART			Write '1' to enable interrupt for event FRAMESTART																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											
F	RW	PAYLOAD			Write '1' to enable interrupt for event PAYLOAD																													
			W1S		Set	1	Enable																											
					Disabled	0	Read: Disabled																											