



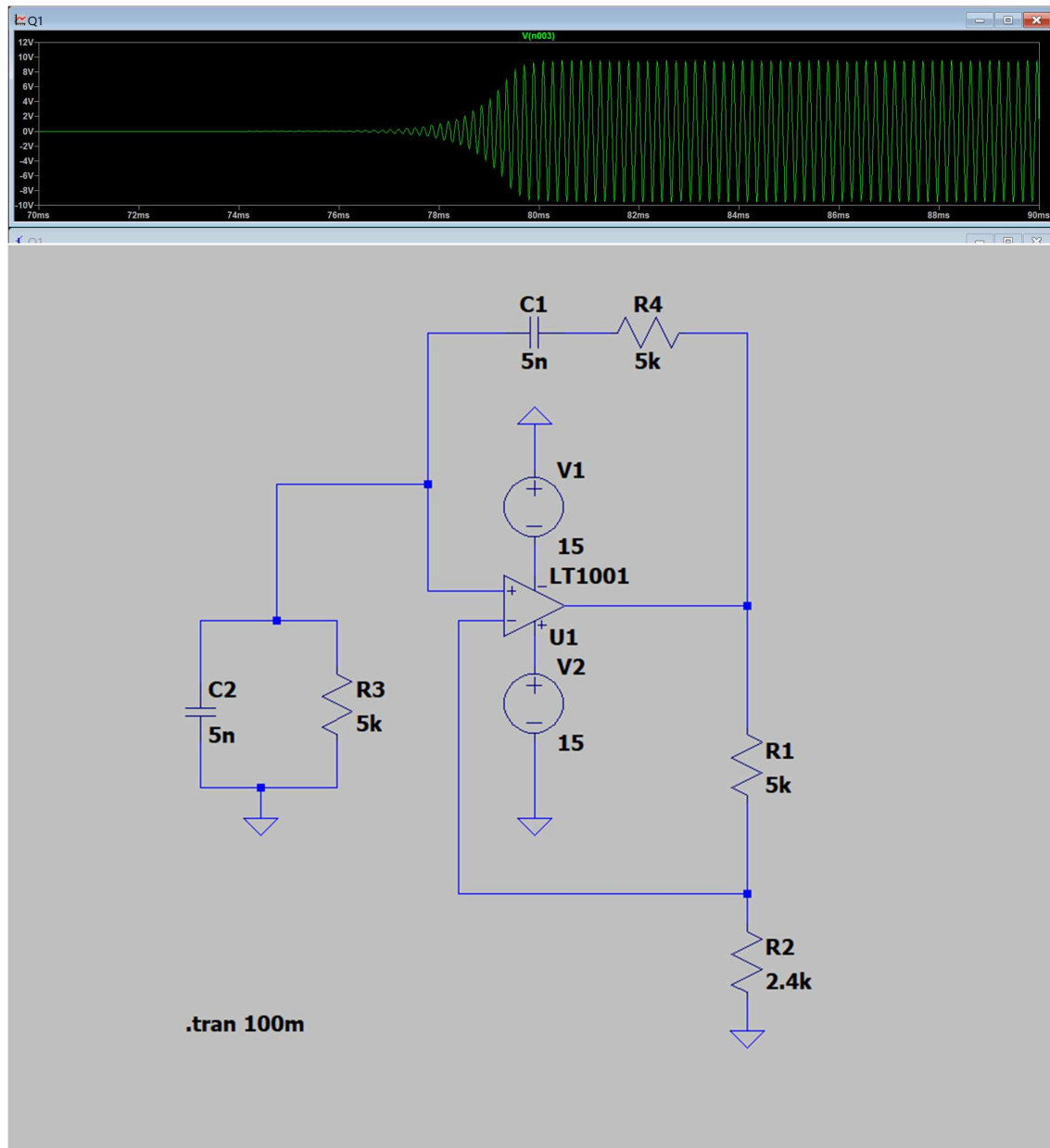
ELECTRONICS CIRCUIT ASSIGNMENT 2A

LTSpice Simulation

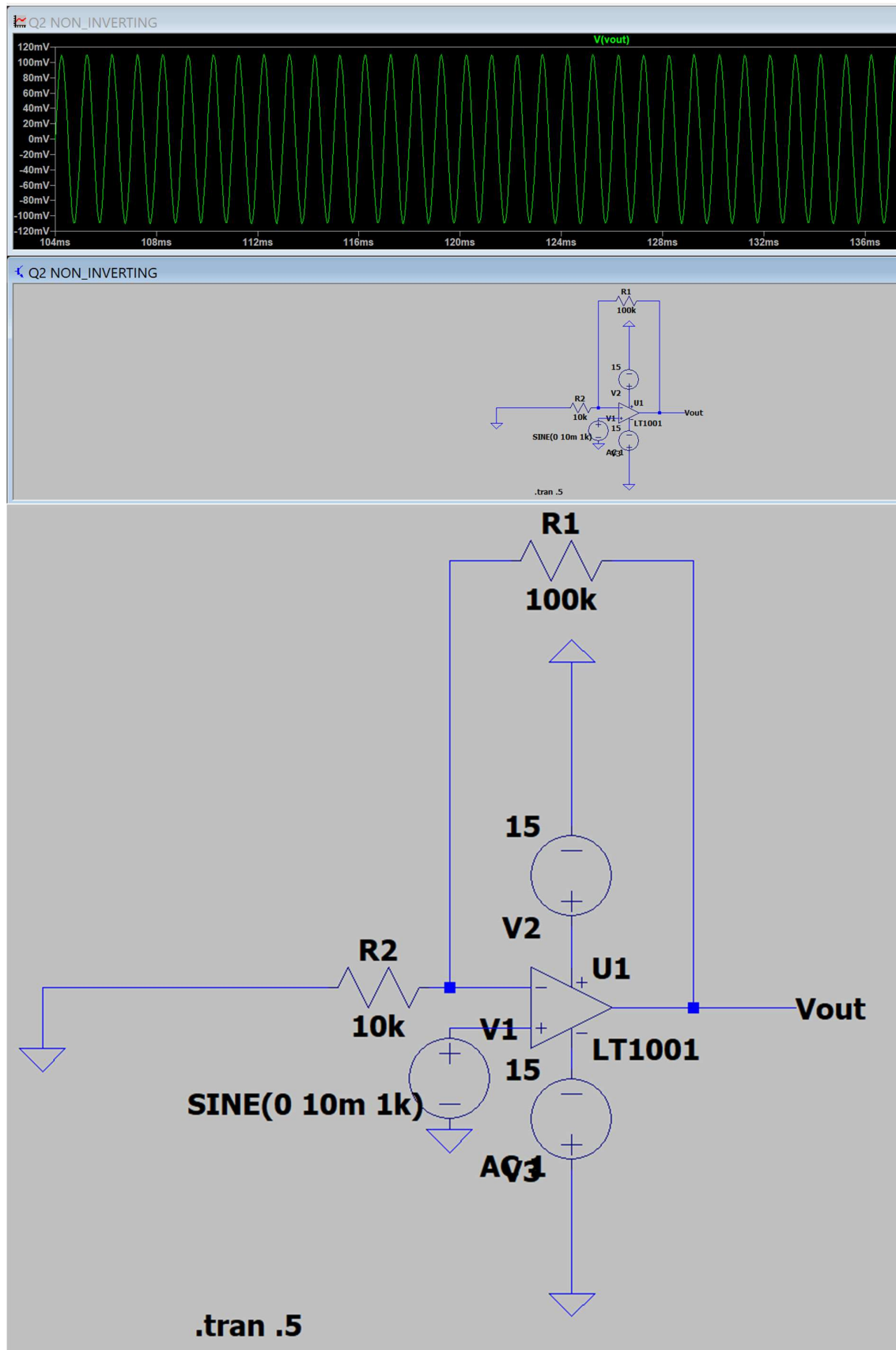
Name : Aman Kumar Raula

Roll : 123EE0321

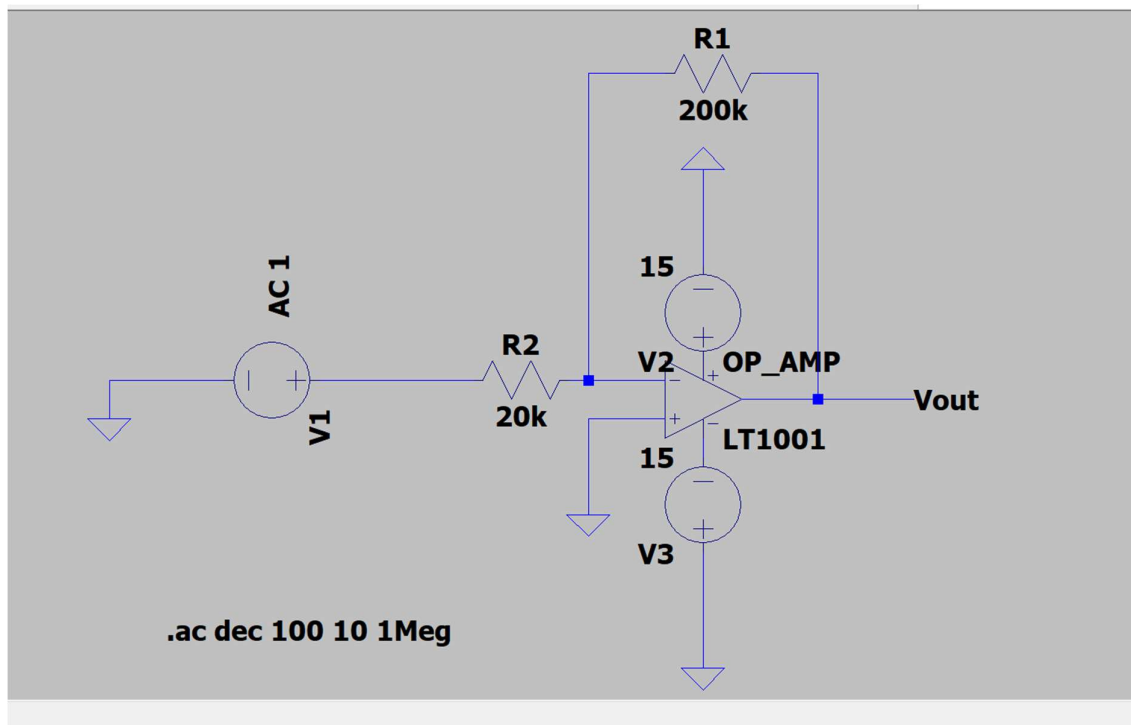
Q1. Oscillator

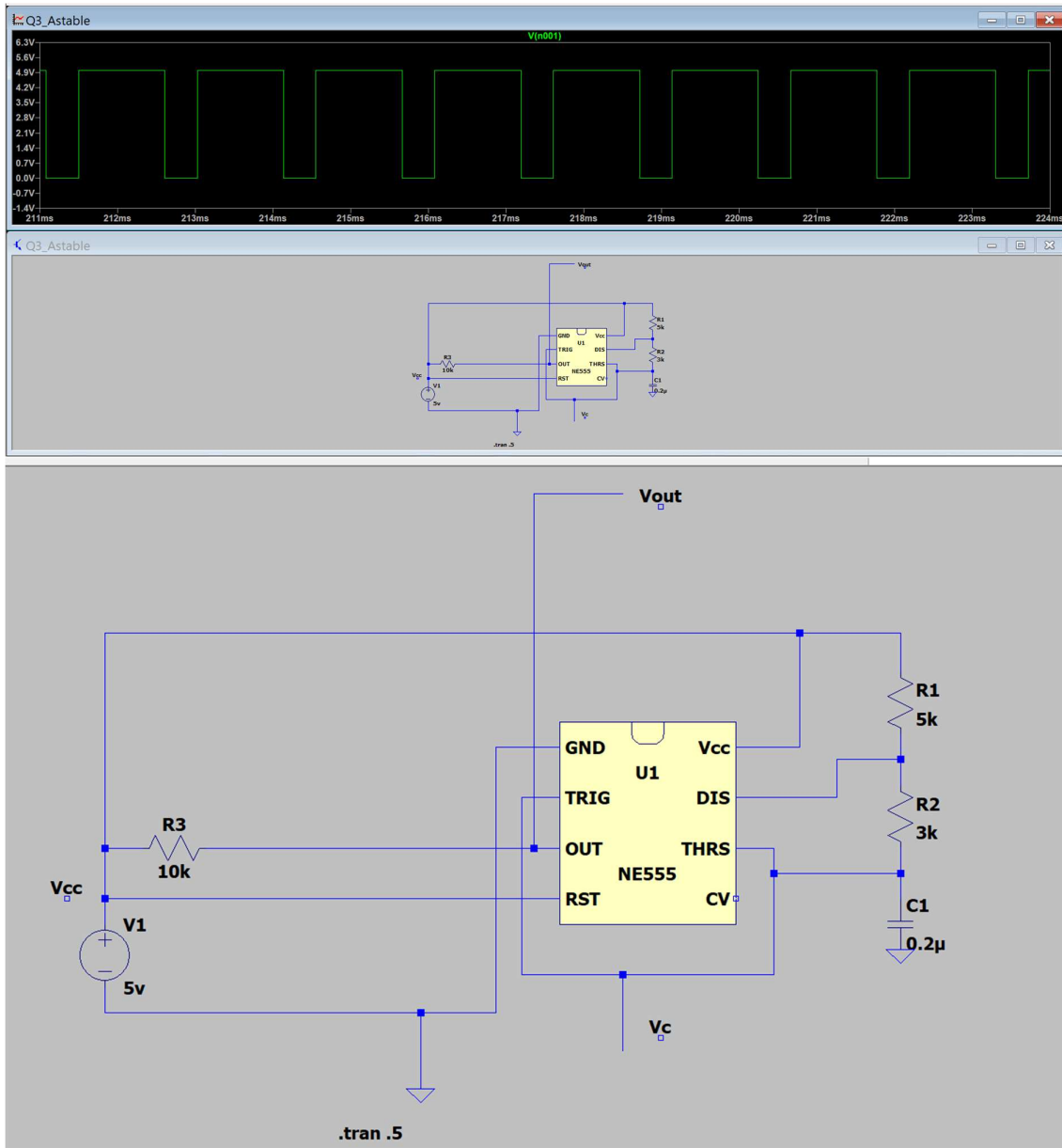


Q2 . NON INVERTING AMPLIFIER



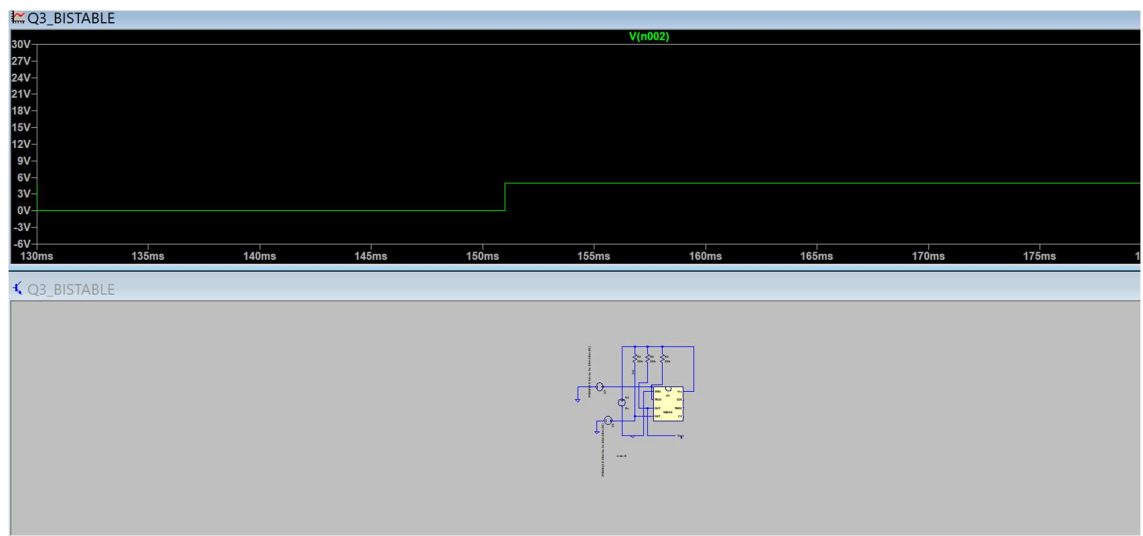
Q2.
INVERTING
AMPLIFIER



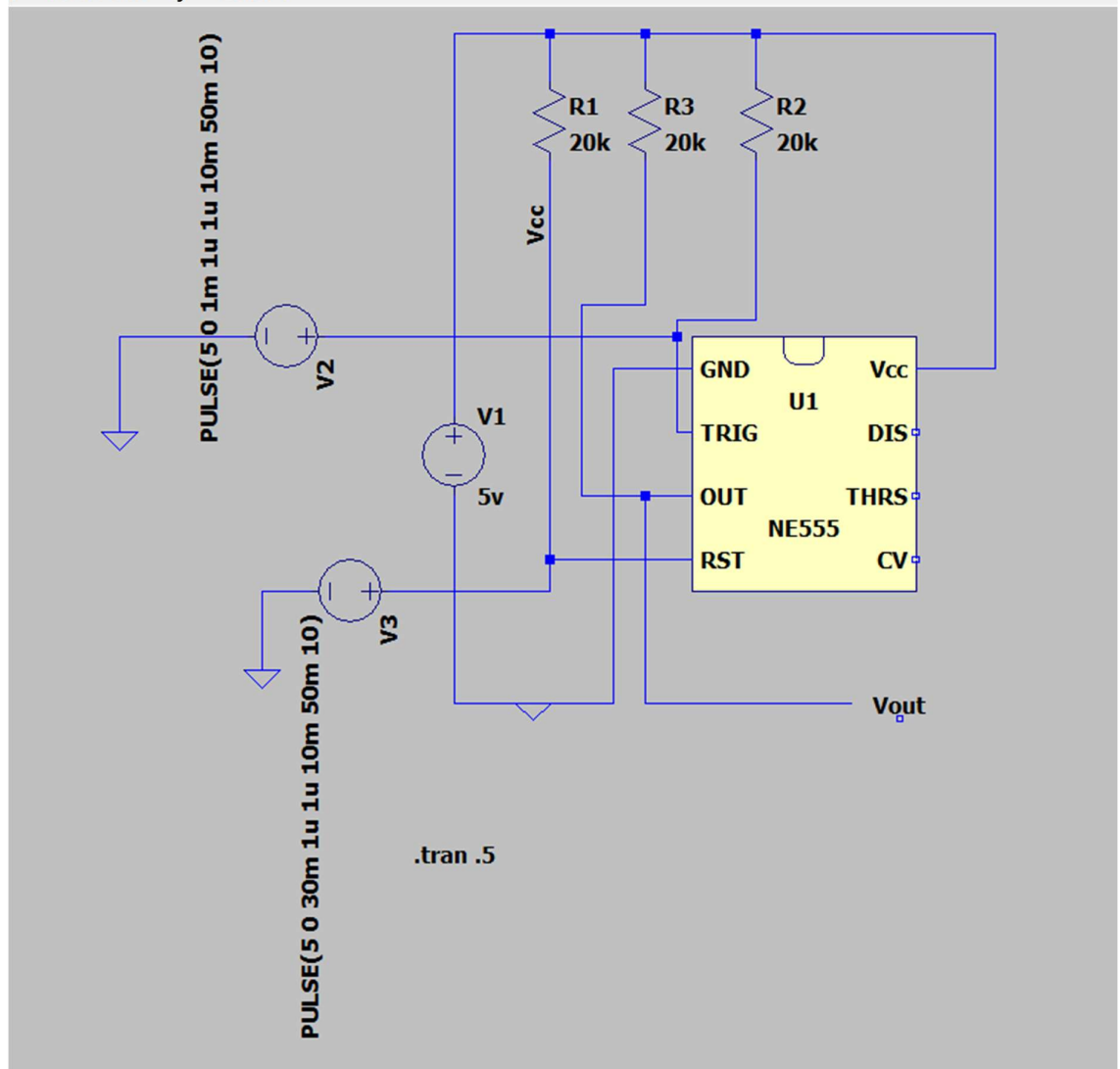


Q3. BI STABLE MODE 555 TIMER

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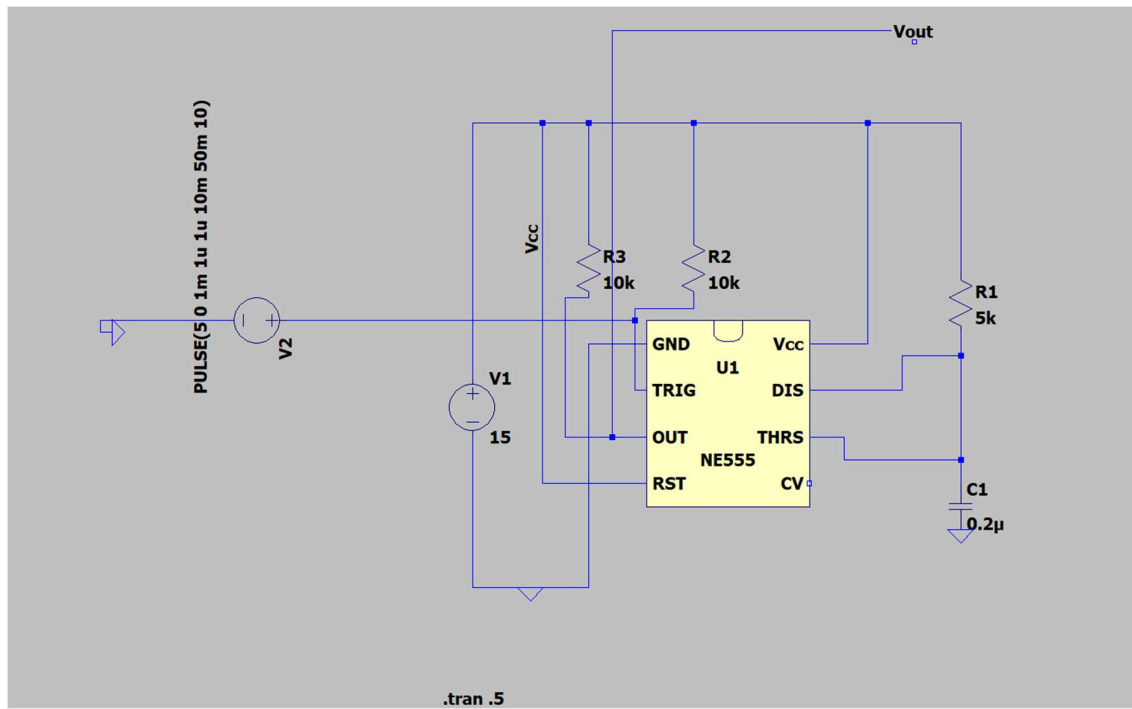


x = 131.11ms y = 28.67V

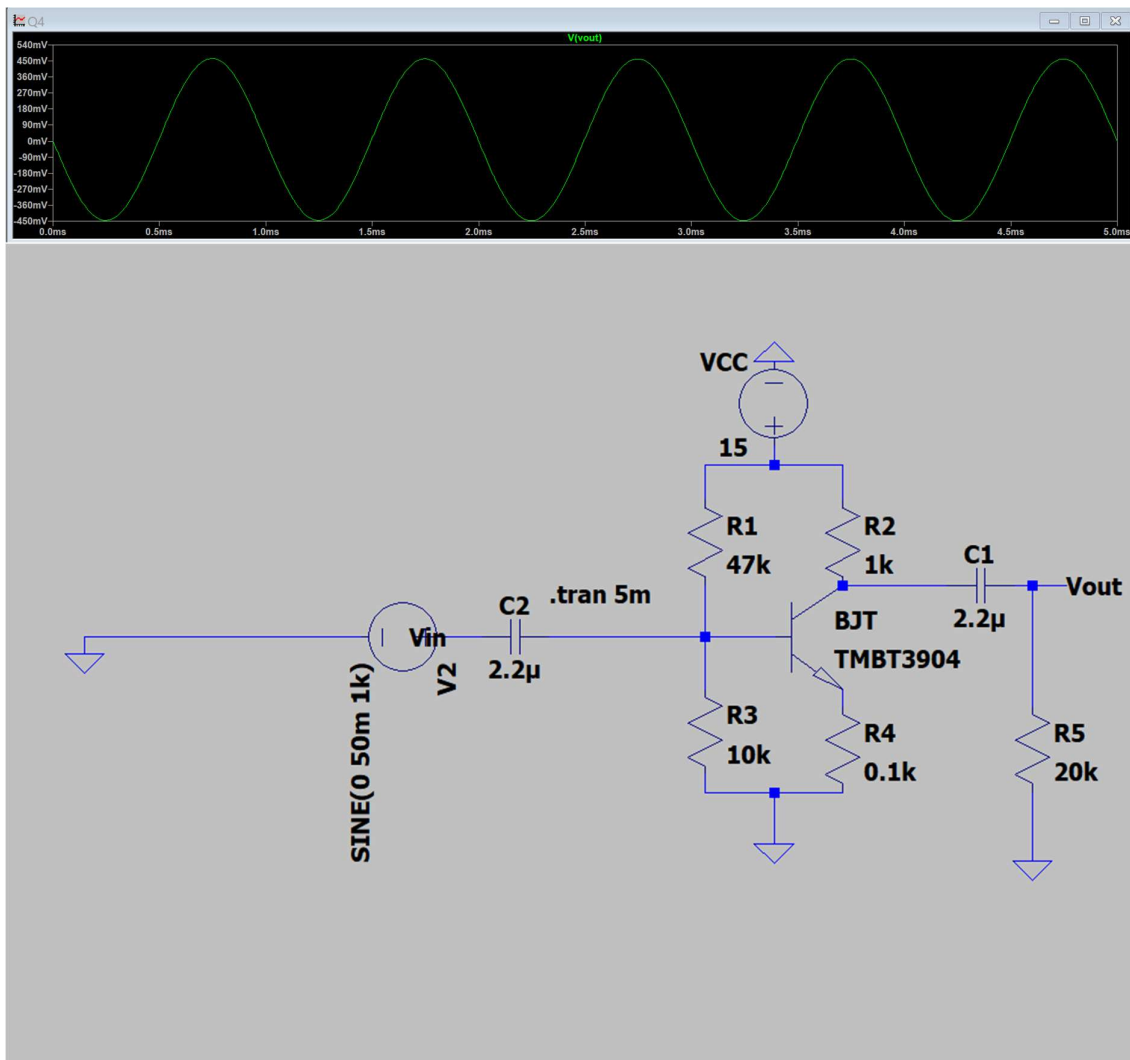


Q3. MONO STABLE MODE 555 TIMER

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Q4 . BJT AMPLIFIER



Q5 . LOOK AHEAD FULL ADDER USING LOGIC GATES

