

Strict alteration: A turn variable:  
S/W mechanism implemented at  
user mode. Busy waiting solution.  
2 process solution  $\rightarrow$  only 2 processes  
 $(T=0)$

For process  $P_0$  :  $T_{un} = 0$

Non CS  $\text{turn} = 1$   
while ( $\text{turn} \neq 0$ )

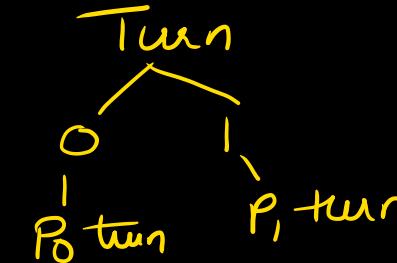
CS ✓  
turn = 1; ✓ give chance to P  
non CS → Out ✓

mutual exclusion ✓

Progress X  
Bounded waiting ✓

Architectural neutrality ✓

$$(P_0, P_1) \quad T_u$$



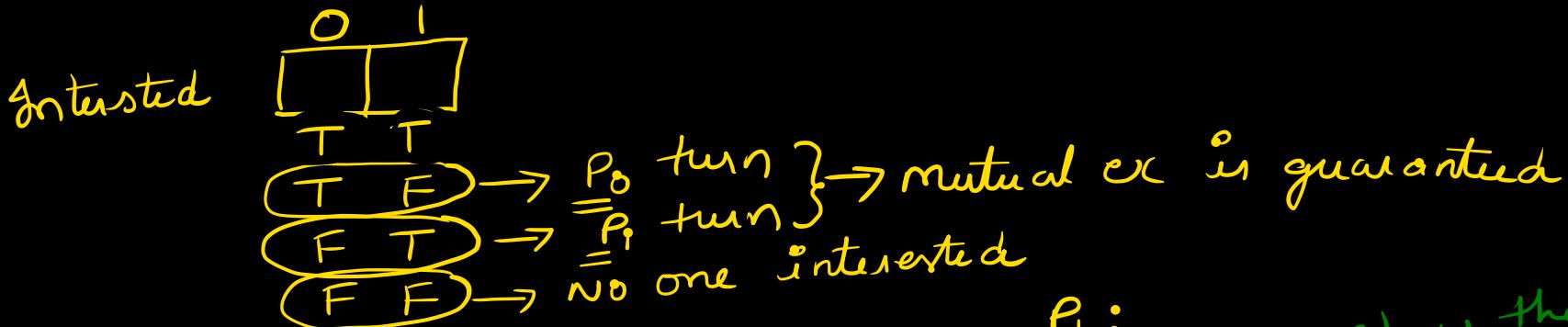
For process API: → CS

try ← while (turn != 1);

Diagram illustrating variable states:

- A red box labeled "CS" contains the assignment statement "turn = 0;".
- An arrow labeled "exit" points to the "CS" box from the left.
- The text "non CS" is written below the "CS" box.

Interested variable: S/w mech, 2 process sol, user mode, Busy waiting.



$P_0$ :

non CS

show the interest

if  $P_1$  is also interested then wait

```
Interested [0] = T;
while (Interested [1] == T);
```

$P_1$ :

show the interest

entry

non CS

if  $P_0$  is also interested then wait

```
Interested [1] = T;
while (Interested [0] == T);
```

exit

CS

```
Interested [1] = False
```

CS

```
Interested [0] = F
```

exit









```

#define N 2
#define TRUE 1
#define FALSE 0
int interested[N] = FALSE
int turn;

```

✓ void entry-section (int process)

```

{
    int other;
    other = 1 - process;
    interested[process] = TRUE;
    turn = process;
    while (interested[other] == true && turn == process);
}

```

*Before entering CS show interest*

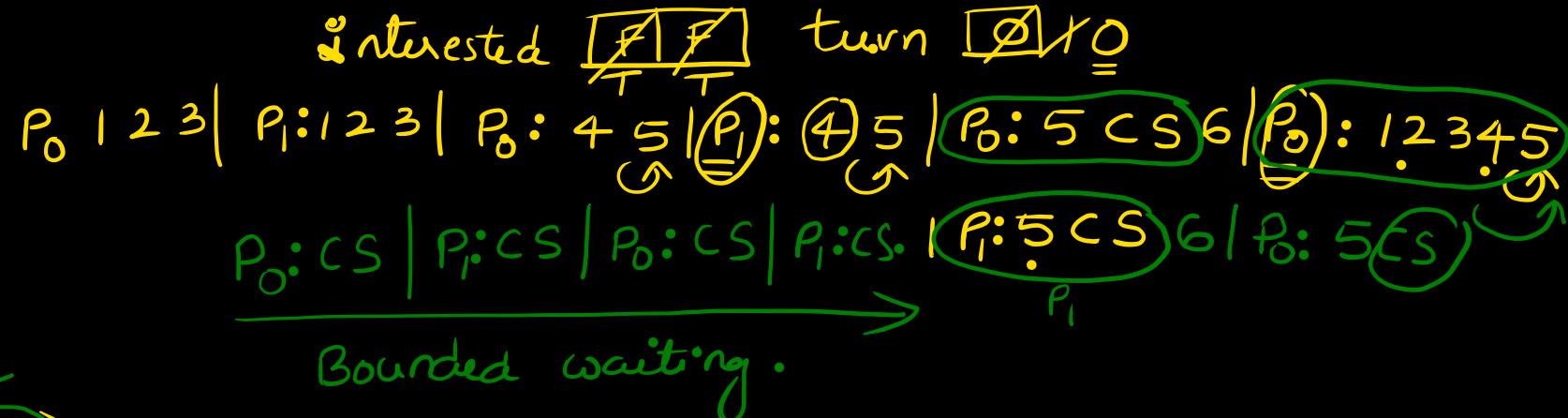
*declare that it's your turn*

✓ void exit-section (int process)

```

{
    interested[process] = FALSE;
}

```



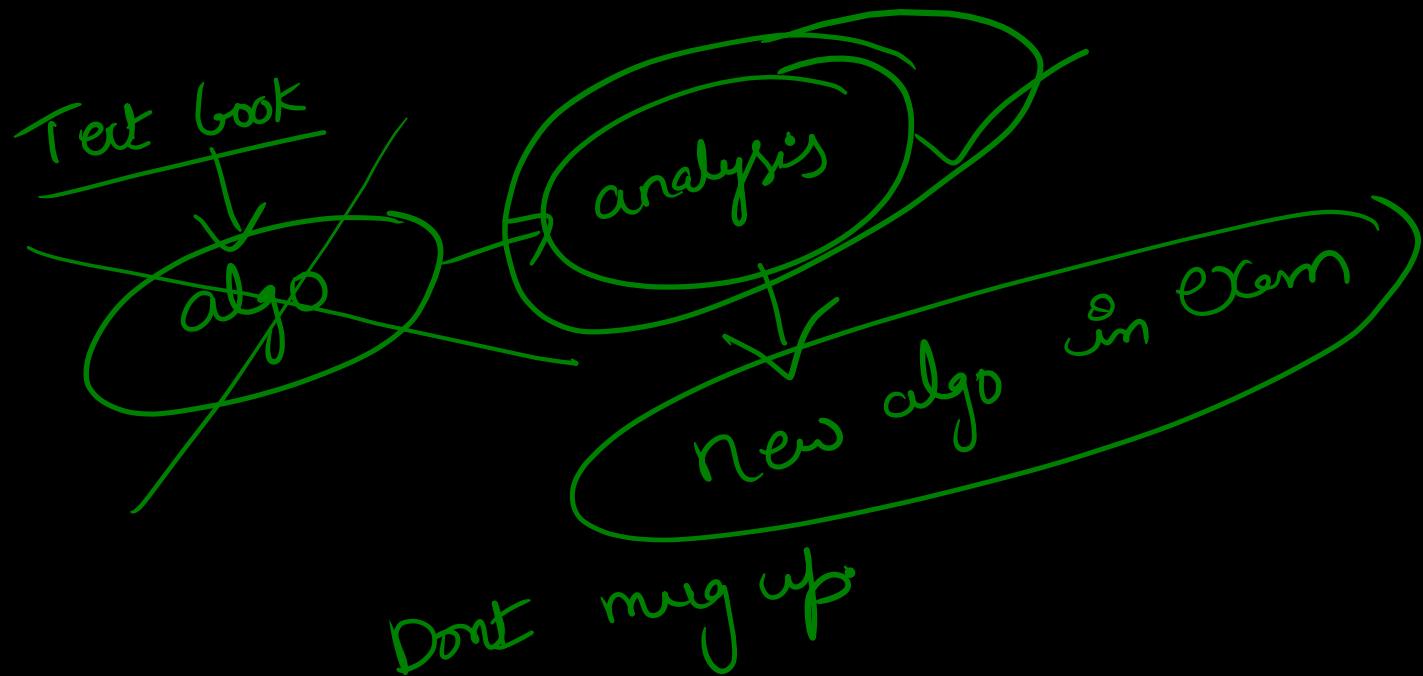
not sure. Benefit of doubt.

If other is interested and its your turn, then wait.

$P_0 \mid 1 \ 2 \ 3 \ 4 \mid P_1 \mid 1 \ 2 \ 3 \ 4 \ 5 \mid$

who will enter CS first?

$P_0 : 1 \ 2 \ 3 \mid P_1 : 1 \ 2 \ 3 \mid P_0 : 4 \ 5 \mid P_1 : 4 \ 5 \mid P_0 : 5 \text{ CS } 6 \mid P_1 : 5 \text{ CS } 6 \mid$



~~stopping~~ Process

unintended  
process, should  
not stop intended  
processes.

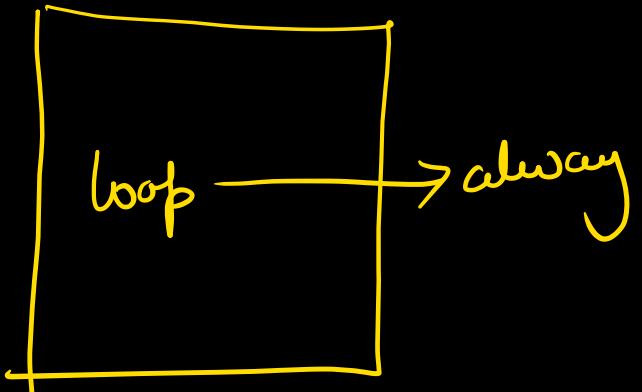
B W  $\rightarrow$  waiting .

$\rightarrow$  A process should not  
wait forever.

# Synchronization mechanisms without Busy waiting

Busy waiting:

entry



non Busy waiting:

Entry

unintended process

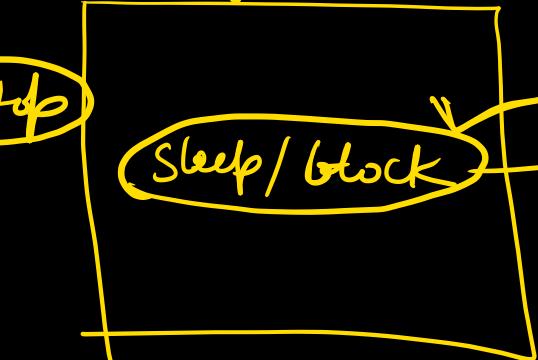
↳ never stop

✓ Progress

Queue

P<sub>1</sub>, P<sub>2</sub>, P<sub>3</sub>

Bounded waiting



ME

depends on implementation

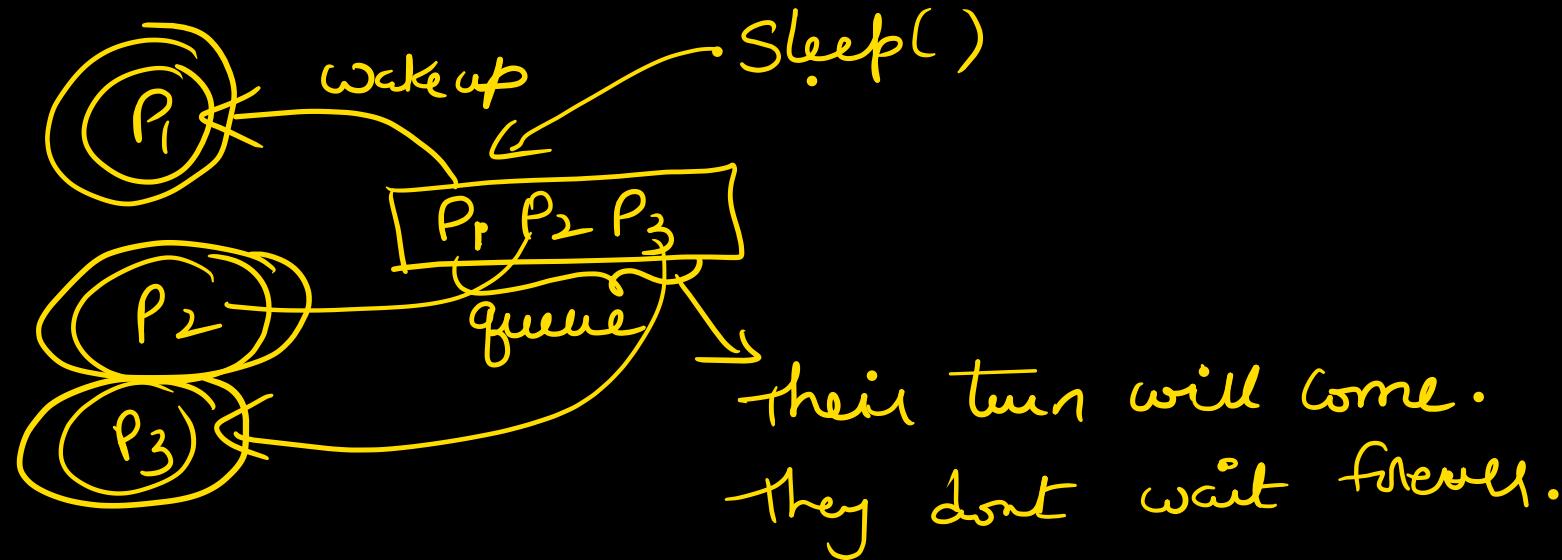
no waiting

Act Architecture

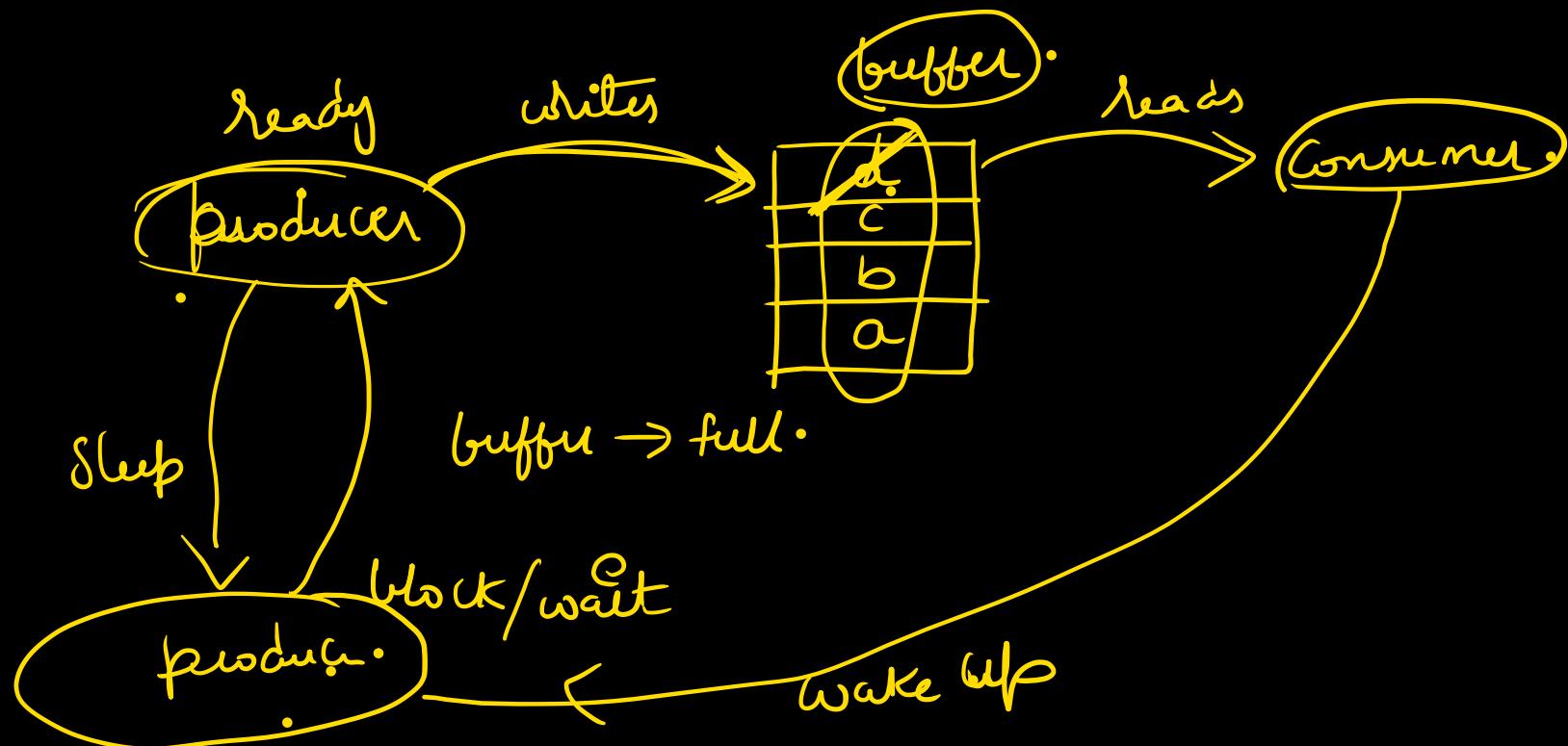
support from OS  
to implement  
sleep() → system call

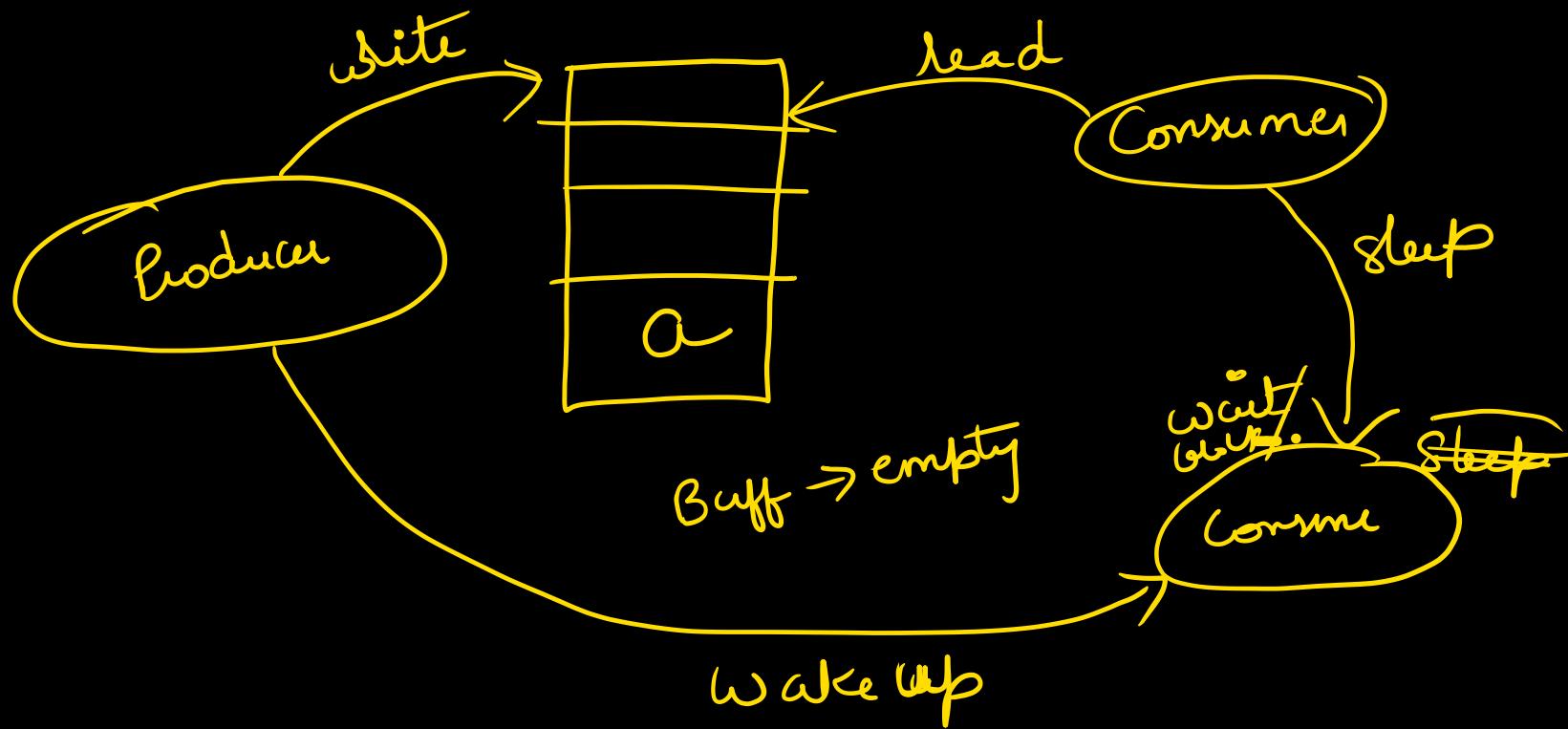


(No Busy waiting) > ~~Busy~~ Busy waiting

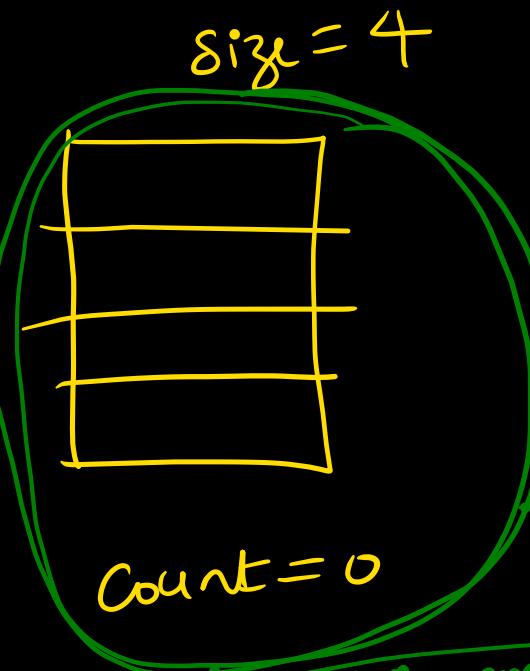


## Sleep and wake up:





producer:  
 Sleep  $\rightarrow$  Count = 4  
 Wake up consumer  $\rightarrow$  Count = 1  
 When will consumer sleep?  
 Wake up call is wasted, no problem



Buff is empty.  
 Consumer has not consumed, so it is not sleeping  
 Producer will add 1 item and send wake up signal.

Consumer:  
 sleep  $\rightarrow$  Count = 0 ✓  
 Because Buff is empty.  
 Wake producer  
 $\rightarrow$  Count = 3

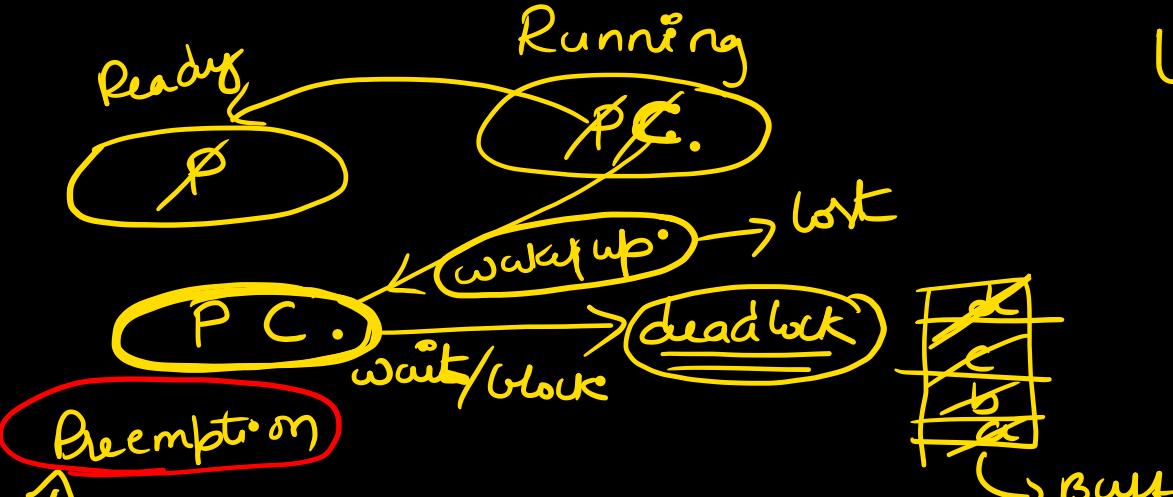




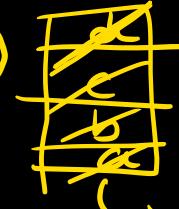
```

#define N 100
#define count 0
void producer(void)
{
    int item;
    while (TRUE)
    {
        item = produce();
        if (count == N) sleep();
        insert-item(item);
        count++;
        if (count == 1) wake up(consumer);
    }
}

```



Ureak = 5 min



Buff is full.

Buff is full

maybe sleeping  
so wakeup

consumer

item = produce();  
 if (count == N) sleep();  
 insert-item(item);  
 count++;  
 if (count == 1) wake up(consumer);

void consumer(void)

{ int item;

while (TRUE)

{

    if (count == 0) sleep();

    item = remove\_item()

    count --;

    if (count == N-1)

        wake up (producer);

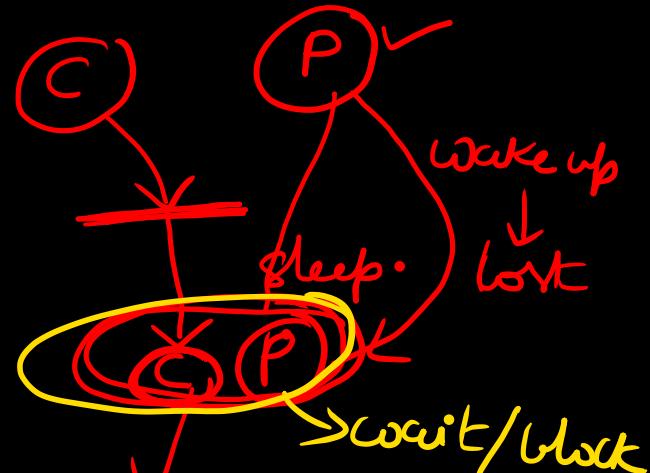
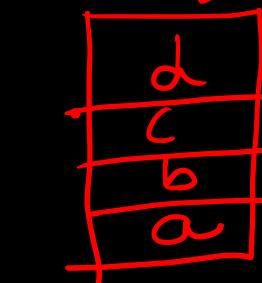
    consume\_item(item);

}

}

preemption

Buff empty.

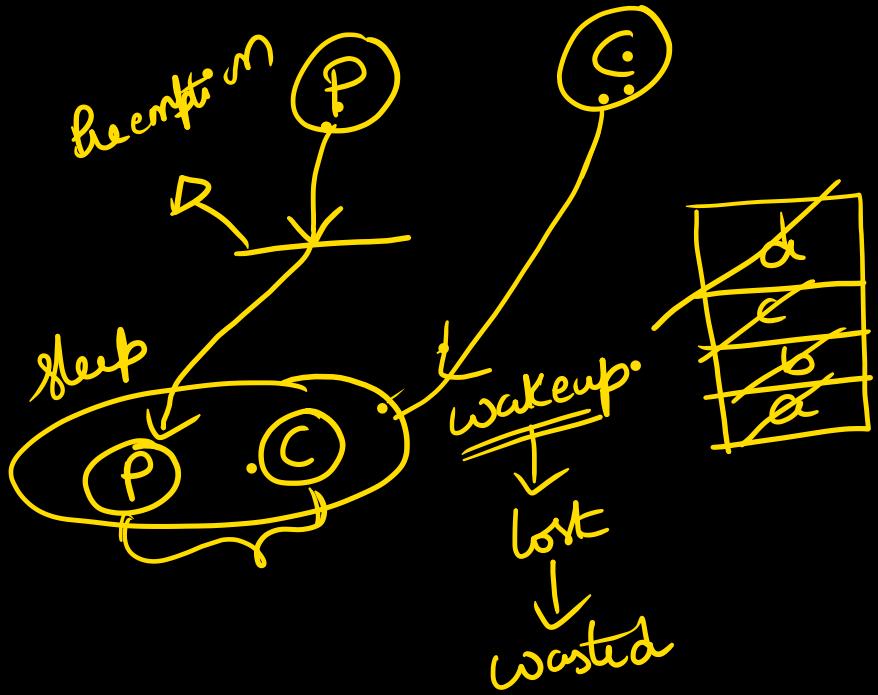


may be sleeping

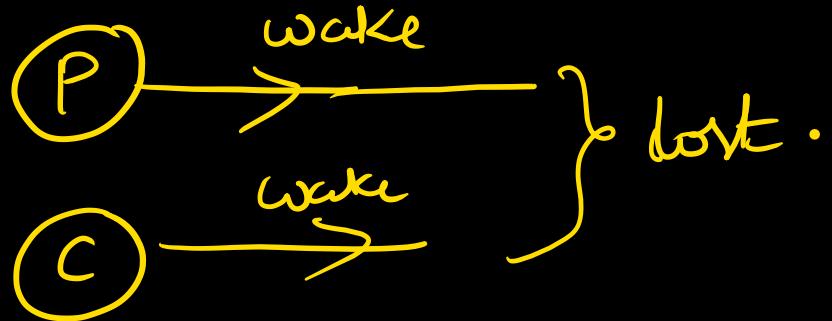
Dead lock

block

Chapter .

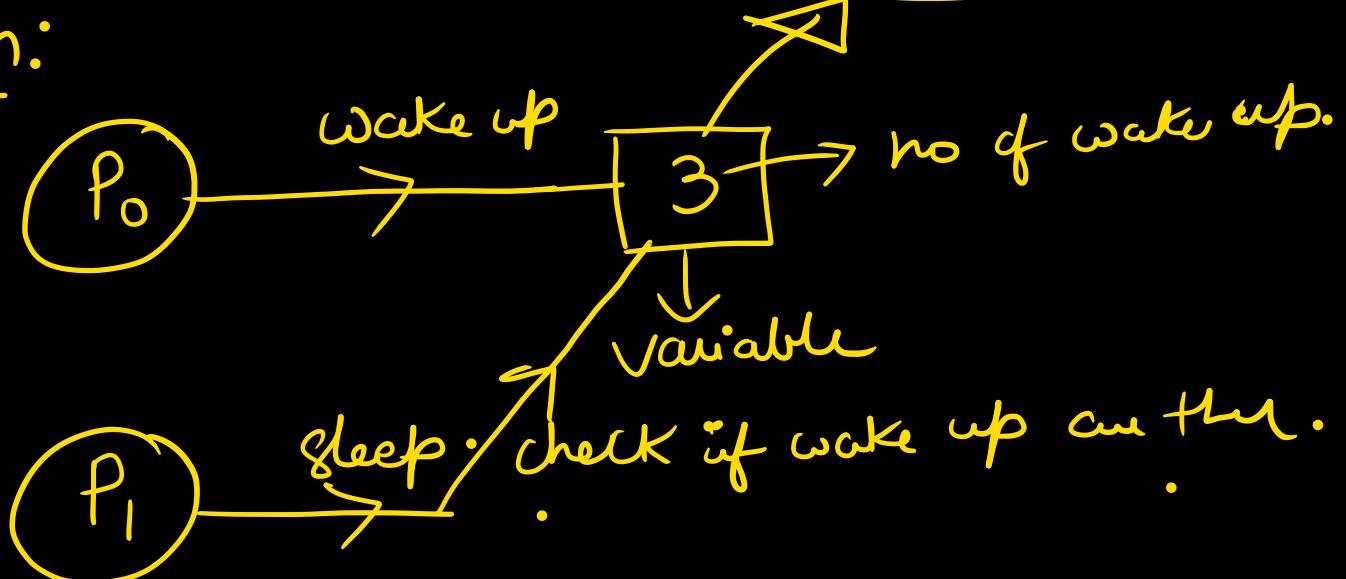


## problem with sleep/wake up.



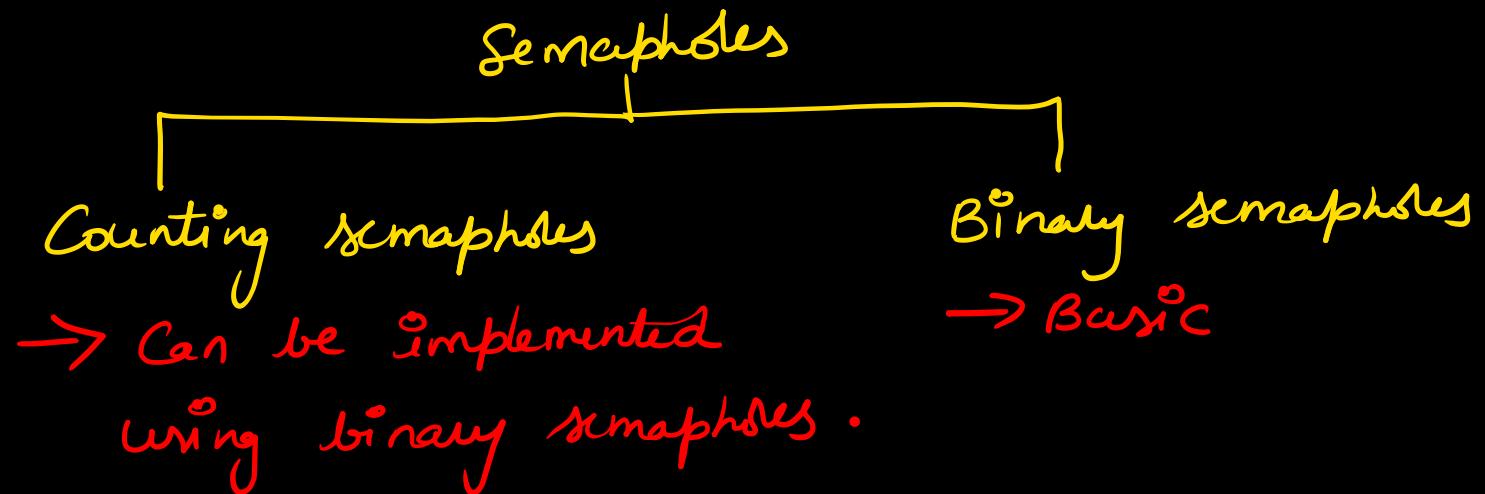
✓  
Concept behind smartphone.

## Solution:



## Semaphores:

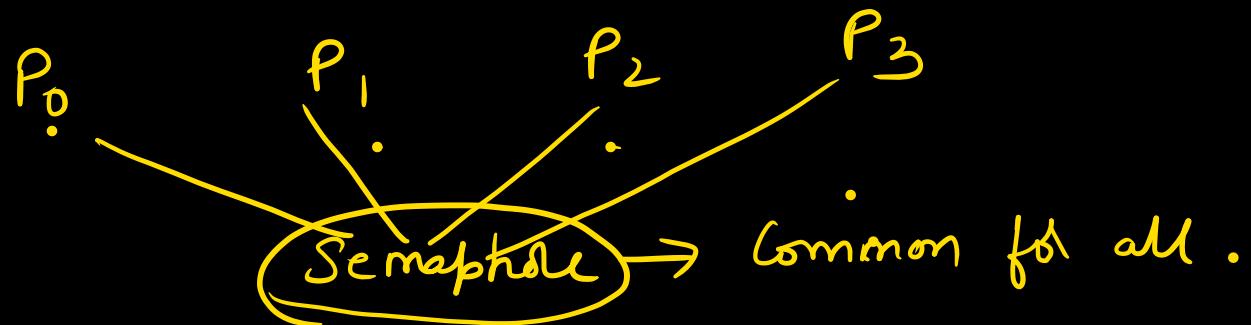
Variables on which Read and update happens atomically  
in Kernel mode.  
Support from OS is required



## Counting semaphores:

These are used when the resources (critical section) can be used by more than one process simultaneously.

Ex: If 4 processes can enter CS simultaneously,  
then ~~can~~ counting semaphore  $a = 4$ .



```

struct semaphore
{
    int value;
    Queue type L;
}

Down(semaphore s)
{
    s.value = s.value - 1;
    if (s.value < 0) → -ve number
    {
        Put Process (PCB) in L;
        sleep();
    }
    else
        return n;
}

```

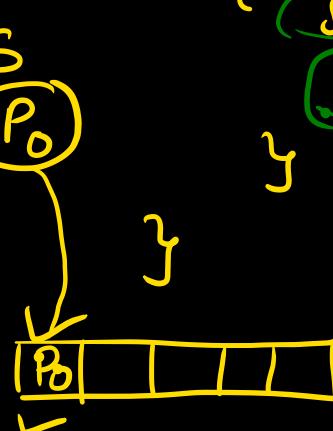
+ve → how many processes can enter CS

-ve → how many processes are blocked

```

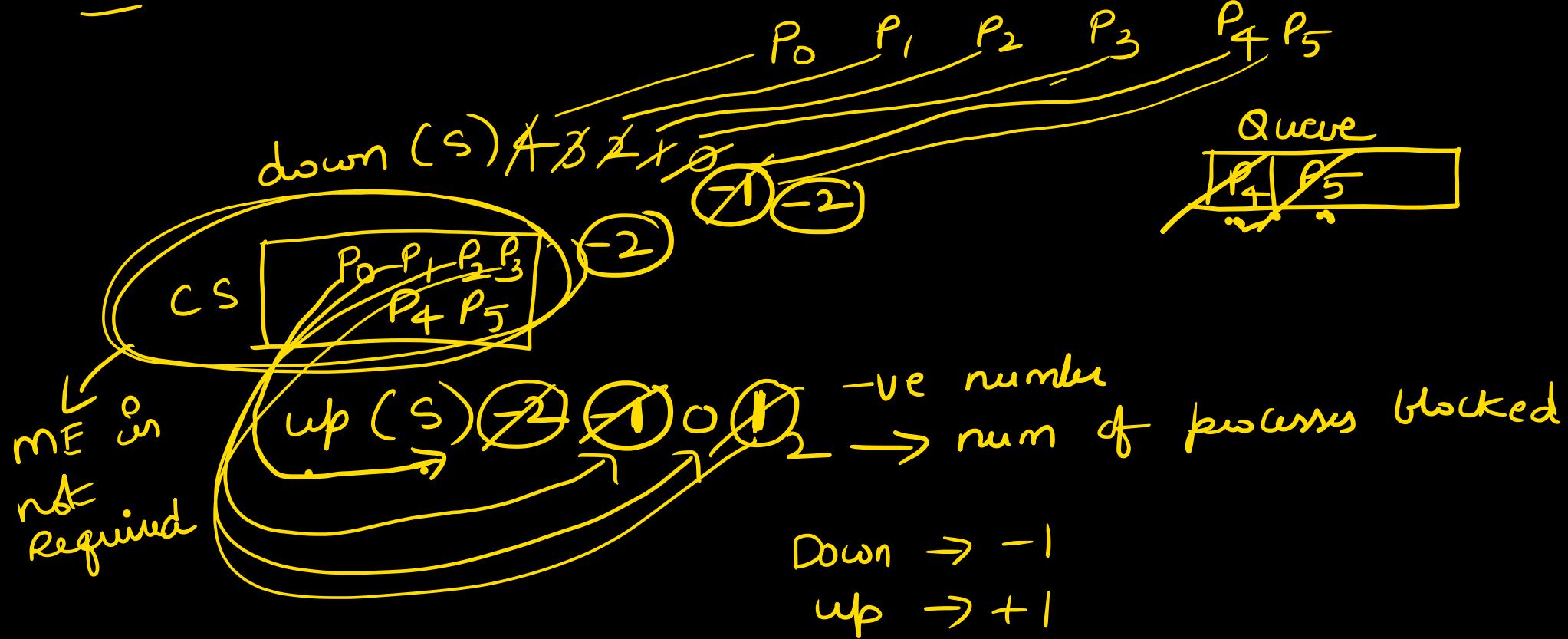
up(semaphore s)
{
    s.value = s.value + 1;
    if (s.value ≤ 0) →
    {
        select a process from L;
        wake up();
    }
}

```



Before implementing -ve, → Some processes are sleeping.

Ex: Semaphore  $S = 4$  .



ME  $\rightarrow$  depends on whether we want it.

If  $S=4 \rightarrow$  4 processes can enter CS

if  $S=1 \rightarrow$  only one process  $\rightarrow$  ME

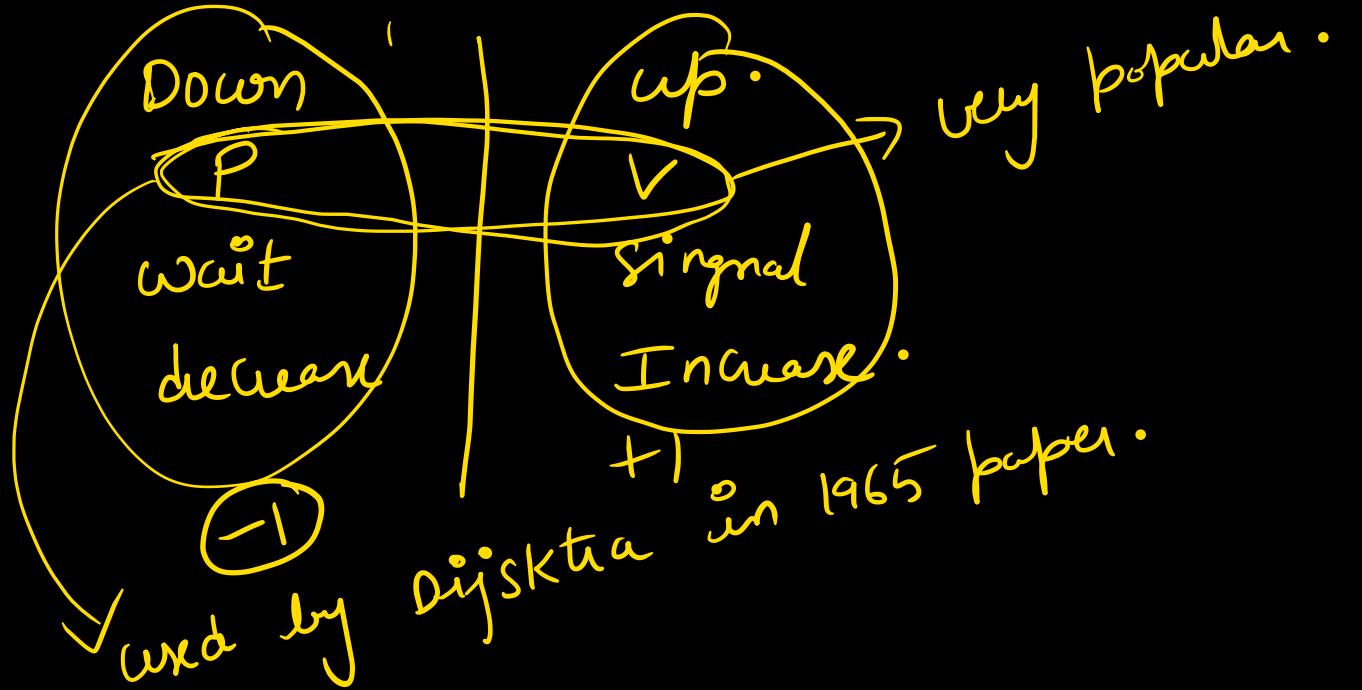
Progress  $\rightarrow$  No process can stop other processes.

~~Bounded wait~~ Bounded wait  $\rightarrow$  we are using a queue (FIFO)

every process will  
get a chance in FIFO  
order.

Architectural neutral  $\rightarrow$  X

$\hookrightarrow$  dependent on OS.



q8) A counting semaphore was initialized to 10. Then **(GP)**  
and **(4V)** operations were performed. What is result?

$$10 - 6 + 4 = 8$$

q2) S=7, 20P, 15V S=?

$$7 - 20 + 15 = 2.$$