

# Amanpreet S. Walia

amanpreetwalia278@gmail.com • +1 (905) 781-9261 • Brampton, ON, Canada

SUMMARY	<ul style="list-style-type: none"><li>Computer Vision Research Engineer specializing in on-device image enhancement, model optimization (SNPE/DLC, AIMET), and local LLM deployment (Ollama, llama.cpp).</li><li>Strong research background with CVPR publications and a US patent in computational photography.</li></ul>				
EDUCATION	<b>M.Sc. (Thesis), Computer Science</b> McGill University Thesis: <a href="#">Uncertainty in depth estimation using RGB-gated images</a>	2018 – 2021	GPA: <b>3.90</b> /4.00		
	<b>B.Eng., Computer Engineering</b> York University	2013 – 2018	GPA: <b>7.9</b> /9.0		
TECHNICAL SKILLS	<b>Languages:</b> Python, C++, C, Java, MATLAB, SQL <b>Frameworks/Tools:</b> PyTorch, Qualcomm SNPE/DLC, AIMET, LLM Deployment (Ollama, llama.cpp, GGUF), OpenCV, TensorFlow, Keras, L <sup>A</sup> T <sub>E</sub> X <b>Hardware:</b> Qualcomm Snapdragon, Nvidia Jetson TX1, Huawei Atlas 200, Raspberry Pi				
EXPERIENCE	<b>Computer Vision Research Engineer, Samsung Research America</b> Dec 2022 – Present <b>Theme:</b> <a href="#">Efficient Models for Image Enhancement</a> <ul style="list-style-type: none"><li>Deployed image enhancement models to Qualcomm devices by converting pipelines to SNPE/DLC and resolving operator/runtime constraints for production inference.</li><li>Built and optimized super-resolution and HDR components with a focus on on-device quality stability (artifact control, consistency across scenes) and runtime efficiency.</li><li>Improved latency and memory footprint through deployment-oriented architecture changes and quantization workflows using AIMET.</li></ul> <b>Computer Vision Researcher, Algolux</b> Aug 2021 – Dec 2022 <b>Theme:</b> <a href="#">Depth Estimation from RGB &amp; Gated Images</a> <ul style="list-style-type: none"><li>Developed a self-supervised depth estimation approach for gated imaging that improved generalization and closed the gap with prior supervised baselines under real capture conditions.</li></ul> <b>Machine Learning Engineer (Full-time Contract), Huawei Canada</b> Mar 2021 – Aug 2021 <b>Theme:</b> <a href="#">Model Compression for NLP on NPU</a> <ul style="list-style-type: none"><li>Ported low-rank decomposed GPT-2/CPM-style models to Huawei NPU execution constraints; validated accuracy/performance trade-offs and integration readiness.</li></ul>				
PUBLICATIONS	<ul style="list-style-type: none"><li>Amirhossein Kazerouni, Maitreya Suin, Tristan Aumentado-Armstrong, Sina Honari, <b>Amanpreet S. Walia</b>, Iqbal Mohamed, Kosta Derpanis, Babak Taati. <i>Face2Scene: Using Facial Degradation as an Oracle for Diffusion-Based Scene Restoration.</i> <b>CVPR 2026 (Accepted).</b></li><li>Stefanie Walz, Mario Bijelic, Andrea Ramazzina, <b>Amanpreet S. Walia</b>, Fahim Mannan, Felix Heide. <i>Gated Stereo: Joint Depth Estimation from Gated and Wide-Baseline Active Stereo Cues.</i> <b>CVPR 2023.</b> arXiv:2305.12955. <a href="https://arxiv.org/abs/2305.12955">https://arxiv.org/abs/2305.12955</a></li><li><b>Amanpreet S. Walia</b>, Stefanie Walz, Mario Bijelic, Fahim Mannan, Fernando Julca-Aguilar, Michael Langer, Felix Heide. <i>Gated2Gated: Self-Supervised Depth Estimation from Gated Images.</i> <b>CVPR 2022.</b> arXiv:2112.02416. <a href="http://arxiv.org/abs/2112.02416">http://arxiv.org/abs/2112.02416</a></li></ul>				
PATENTS	<b>Dual-camera Joint Denoising-Deblurring using Burst of Short and Long Exposure Images.</b> 2024 Inventors: Shayan Shekarforoush, <b>Amanpreet Singh Walia</b> , Aleksai Levinshtein, Konstantinos G. Derpanis, Marcus A. Brubaker Patent Application: <a href="#">US20240311968A1</a>				