

# Aman Singh

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## Education

Arizona State University, MS in Computer Engineering (Thesis)	January 2024 – December 2025
CGPA: 3.62/4.00	
National Institute of Technology Karnataka, B.Tech Electrical and Electronics Engineering	July 2018 – April 2022
CGPA: 6.44/10.00	

## Skills

**Programming and Development:** C, C++, Python, Java, TensorFlow, PyTorch, Version Control, Git, Web Development, Android, Django, MATLAB

**Data Science and Machine Learning:** Computer Vision, NLP, Algorithms, Data Structures, Data Visualization, Statistics, Statistical Machine Learning, Probability, Artificial Intelligence, Deep Learning, Forecasting, Reinforcement Learning

**Robotics and Miscellaneous** PDDL, OOD Detection, Error Detection, Mathematics, ROS, R

## Experience

<b>Researcher</b> , MPS Lab, Arizona State University, AZ	March 2024 – Current
<ul style="list-style-type: none"><li>Conducting research under <b>Prof Aviral Shrivastava</b> focused on improving the <b>Reliability</b> of machine learning algorithms by using Neural Activations to detect anomalies.</li><li>Contributing to the <b>peer-review process</b> for conferences such as ESWEEK, IEEE-ESL, ACM-TECS, and RTSS.</li><li>Technologies: <b>Pytorch, Fault-injection, OOD Detection, Soft error, SDC, Adversarial attacks.</b></li></ul>	
<b>Software Development Engineer</b> , Amazon, Bangalore	June 2022 – March 2023
<ul style="list-style-type: none"><li>Led peak planning initiatives, including scaling factor analysis, <b>load testing</b> for <b>APIs</b> and <b>Pollers</b>, and optimizing AWS resources <b>Lambda, Kinesis, DynamoDB</b>. Achieved a <b>30%</b> year-over-year <b>cost reduction</b> with <b>1.4x</b> traffic. Coordinated with dependent and client teams to optimize API latency and manage higher traffic.</li><li>Revamped the migration from gzip to parquet, improving <b>Athena query latency</b> by 40% for portal. Designed Adaptive Rate Limiter on the API calls to reduce sudden spikes in latency of apis.</li><li>Technologies: <b>Java, AWS S3, Lambda, Kinesis, DynamoDB, Glue, Athena.</b></li></ul>	
<b>Software Design Engineer Intern</b> , Lava International, Bangalore	May 2021 – July 2021
<ul style="list-style-type: none"><li>Developed an <b>automation framework</b> to automate various test cases at different layers of <b>Android</b>.</li><li>Designed a program to check the Bluetooth of android devices by creating an <b>APK</b> that automates file transfer between two android devices.</li><li>Technologies: <b>Python, Java, Android.</b></li></ul>	

## Projects

<b>Fault-Tolerant Neural Networks</b> (Github Link)(Paper Link)	August 2024 – October 2024
<ul style="list-style-type: none"><li>Implemented Dr Dna paper(DNNs) to detect Silent Data Corruptions (SDCs), achieving a 100% detection rate</li><li>Employed <b>PyTorchFI</b> with a <b>custom bit-flip function</b> to simulate single-bit flip faults. Tested across multiple neural network architectures, including <b>AlexNet, ResNet-18, and LeNet-5</b></li></ul>	
<b>Detection of Adversarial and OOD samples</b> (GitHub Link) (Paper Link)	Nov 2024 – Dec 2024
<ul style="list-style-type: none"><li>Detected Adversarial and OOD samples in a neural network using <b>Mahalanobis distance</b> if the samples from the input classes. Improved the original method by using <b>voting</b> based method for detection instead of training another linear regression model for <b>feature ensembling</b>.</li><li>Achieved a detection accuracy 92 % for both OOD and Adversarial Attack which is very similar to SOTA while significantly reducing runtime overhead.</li></ul>	

## Achievements

GRE – Total: 317/340, Quantitative: 170/170	2023
Google Kickstart – Rank 1516 in Google Kickstart Round B	2021
International Coding Marathon – Global rank 260 among 1200 participants	2021
Codechef Starters – Global rank 72 among 4900+ participants	2021
JEE Main – Secured AIR 5398 among 1.5 million candidates in India	2018

## Volunteering

<b>Grader</b> , Arizona State University	August 2024 – Current
<ul style="list-style-type: none"><li>Assisted in grading assignments, quizzes, exams, papers, and discussion boards for the Electrical Circuits course.</li><li>Ensured timely and accurate feedback to students, contributing to understanding and success in the course.</li></ul>	