

# AMAN ZAVERI

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Dear Hiring Manager,

I am writing to express my enthusiastic interest in the ASIC Formal Verification Intern position at Groq Inc, as advertised on your careers page. Groq's innovative approach to hardware design and its commitment to pushing the boundaries of computing deeply resonate with my passion for creating efficient and reliable systems. I am confident that my skills and experience align perfectly with the requirements of this role.

My experience in verifying complex digital designs and automating verification workflows makes me a strong candidate for this internship. Specifically, my work engineering a tire-pressure system in C++ via HAL + AIDL with sub-second alerts and 99% reliability demonstrates my ability to build robust and dependable systems. Additionally, my development of Wi-Fi validation pipelines in Python/C++ integrated with Jenkins CI/CD, which cut test time by 40%, highlights my proficiency in automation and tool integration, essential skills for this position. My familiarity with languages like Python, C++, and potentially SystemVerilog through other hardware-related projects equips me with the necessary programming foundation.

Beyond technical skills, I embody the attributes of a Groqstar. I approach challenges with humility, eager to learn from experienced engineers and contribute collaboratively to the team. I possess a growth mindset, constantly seeking to expand my knowledge and improve my skills. My curiosity and passion for innovation drive me to find creative solutions to complex problems.

You can find examples of my project work on my GitHub account: [insert GitHub link here]. One project that may be of interest is the development of Wi-Fi validation pipelines which streamlined testing, and involved both Python and C++ implementations.

I am eager to contribute my skills and enthusiasm to Groq Inc and am confident that I can make a meaningful contribution to your team. Thank you for your time and consideration.

Aman Zaveri