AMAN ZAVERI

647-676-8981 | a2zaveri@uwaterloo.ca | LinkedIn | GitHub

Dear Hiring Manager,

I am writing to express my enthusiastic interest in the Digital Design Verification Engineering position at Low Power Futures. The opportunity to contribute to the verification of low-power hardware acceleration engines for IoT applications deeply resonates with my passion for developing efficient and reliable systems.

My background in embedded systems and verification aligns strongly with the requirements of this role. In particular, my experience developing Wi-Fi validation pipelines in Python/C++ integrated with Jenkins CI/CD, which resulted in a 40% reduction in test time, demonstrates my ability to write and update test sequences effectively. I am confident in my ability to leverage my proficiency in Python, C, and potentially adapt my knowledge to System Verilog to ensure the hardware meets all specifications.

Furthermore, I have a track record of contributing to all stages of the development process, from design to deployment. My work engineering a tire-pressure system in C++ using HAL + AIDL achieved sub-second alerts and 99% reliability, highlighting my commitment to building robust and dependable solutions. I am eager to apply this dedication to ensuring the quality and performance of Low Power Futures' hardware.

I am excited about the prospect of joining Low Power Futures and contributing my skills to your innovative team. I am confident that my experience and enthusiasm make me an excellent fit for this role.

Aman Zaveri