

Lecture 2: Registers

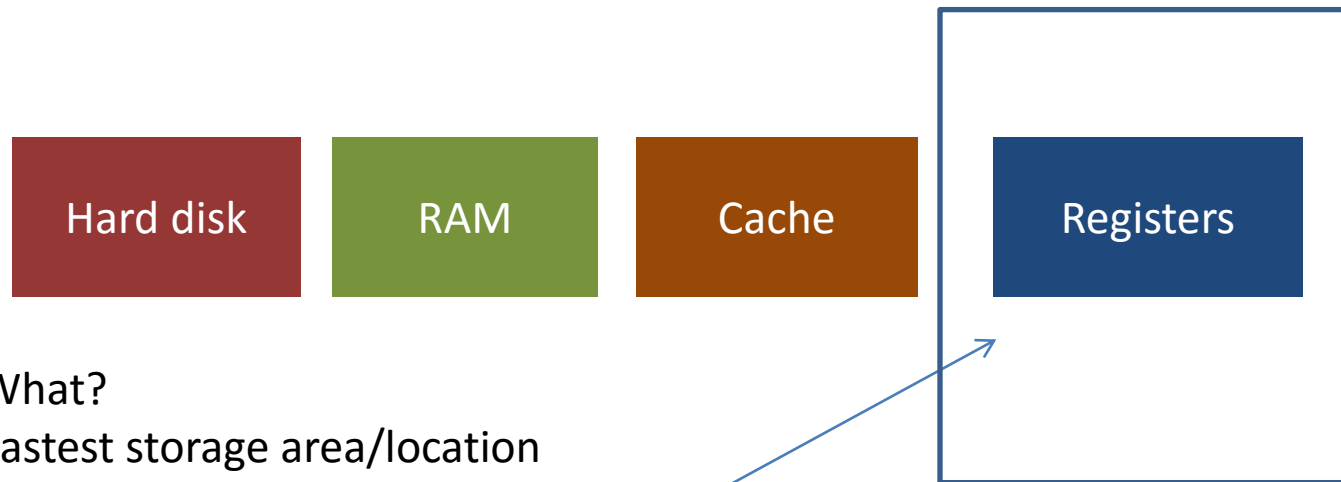
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Why?

Optimization of Processing time

Understanding of hardware and software interaction

CPU



What?

Fastest storage area/location

“Quickly accessible by CPU as they are built into CPU.”

What’s the origin of Registers?

Intel 4004 in 1971, Federico Faggin

Record or collection of information