OSMANIA UNIVERSITY

CONSOLIDATED MEMORANDUM OF MARKS

EG125673

EXAMINATION : B.E. (ELECTRONICS & COMM.)[2006-07 TO 2009-10] JUNE 2010

REF NO. : 168
ROLL NO. : 02074083   NAME : G. AMARNATH   FATHER'S NAME : G. BHASKAR RAO

SL. NO.	SUBJECTS	U. EXAM MAX MARKS	U. EXAM MARKS SECURED	SESS. MAX MARKS	SESS. MARKS SECURED
	I YEAR				
1	ENGLISH	75	67	25	20
2	MATHEMATICS - I	75	70	25	25
3	MATHEMATICS - II	75	75	25	25
4	ENGINEERING PHYSICS	75	75	25	22
5	PHYSICS LAB	50	41	25	22
6	ENGINEERING CHEMISTRY	75	70	25	23
7	CHEMISTRY LAB	50	43	25	25
	II YEAR I SEMESTER				
1	MATHEMATICS - III	75	73	25	25
2	BASIC CIRCUIT ANALYSIS	75	65	25	21
3	ELECTROMAGNETIC THEORY	75	55	25	24
4	ELECTRONIC DEVICES	75	60	25	25
5	ELEMENTS OF MECH. ENGINEERING	75	63	25	22
6	ELECTRICAL TECHNOLOGY	75	68	25	17
7	ELECTRONICS DEVICES - I LAB	50	45	25	19
8	ELECTRONICS WORKSHOP&B.C LAB	50	38	25	21
	III YEAR I SEMESTER				
1	INTEGRATED CIRCUITS & APPLS.	75	69	25	13
2	COMPUTER ORG. & ARCHITECTURE	75	63	25	22
3	ANALOG COMMUNICATIONS	75	61	25	14
4	AUTOMATIC CONTROL SYSTEMS	75	67	25	18
5	MICROPROCESSORS & INTERFACING	75	58	25	17
6	INTEGRATED CIRCUITS LAB	50	44	25	24
7	MICROPROCESSORS& INTERF. LAB	50	43	25	24
8	COMMUNICATION ENGINEERING LAB	50	38	25	23
	IV YEAR I SEMESTER				
1	MICROWAVE ENGINEERING	75	65	25	22
2	VHDL	75	67	25	20
3	ELECTRONIC INSTRUMENTATION	75	51	25	22
4	MOBILE CELLUAR COMM.	75	66	25	25
5	DIGITAL IMAGE PROCESSING	75	56	25	25
6	INDUSTRIAL ADMN. & FIN. MANG.	75	69	25	23
7	MICROWAVE LAB	50	47	25	24
8	VHDL LAB	50	42	25	24
9	PROJECT SEMINAR	==	==	25	22

SL. NO.	SUBJECTS	U. EXAM MAX MARKS	U. EXAM MARKS SECURED	SESS. MAX MARKS	SESS. MARKS SECURED
	I YEAR				
8	ENGINEERING GRAPHICS	100	70	50	47
9	WORKSHOP PRACTICE	50	47	25	23
10	ENGINEERING MECHANICS	75	62	25	25
11	PROGRAMMING IN C & C++	75	73	25	25
12	PROGRAMMING LAB	50	45	25	24
13	ENGLISH LANGUAGE LAB	==	==	25	22
	II YEAR II SEMESTER				
1	MATHEMATICS IV	75	65	25	25
2	ANALOG ELECTRONICS CIRCUITS	75	67	25	23
3	NETWORKS & TRANSMISSION LINES	75	66	25	22
4	SIGNAL ANA. & TRANSFORM TECH.	75	73	25	25
5	PULSE, DIGITAL AND S. CIRCUITS	75	71	25	24
6	ENVIRONMENTAL STUDIES	75	54	25	25
7	ANALOG ELECTRONICS CIR. LAB	50	34	25	22
8	ELECTRICAL TECHNOLOGY LAB	50	44	25	22
	III YEAR II SEMESTER				
1	DIGITAL COMMUNICATIONS	75	59	25	21
2	DIGITAL SIGNAL PROCESSING	75	70	25	23
3	ANTENNAS AND PROPAGATION	75	58	25	24
4	MICROCONTROLLERS AND APPLNS.	75	70	25	25
5	MANAGERIAL ECONOMICS & ACCY.	75	59	25	18
6	DIGITAL COMMUNICATIONS LAB	50	44	25	21
7	DIGITAL SIGNAL PROCESSING LAB	50	47	25	23
8	MICROCONTROLLER LAB	50	47	25	24
9	INDUSTRIAL VISIT / STUDY	==	==	GRD	VGD
	IV YEAR II SEMESTER				
1	RADAR AND SATELLITE C S	75	66	25	25
2	ENTERPRENEURSHIP	75	62	25	23
3	SPEECH PROCESSING	75	72	25	25
4	PROJECT	GRD	VGD	50	44
5	SEMINAR	==	==	25	23

Note: Bridge course I&II Sems/Environmental studies subjects marks are not included in the Aggregate

AGGREGATE : 4531   AGGREGATE IN WORDS : ** FOUR FIVE THREE ONE **   **DIVISION** FIRST WITH DISTINCTION

G. Total : 5175

SECTION - INCHARGE   CONTROLLER OF EXAMINATIONS