



Radxa C200 schematic

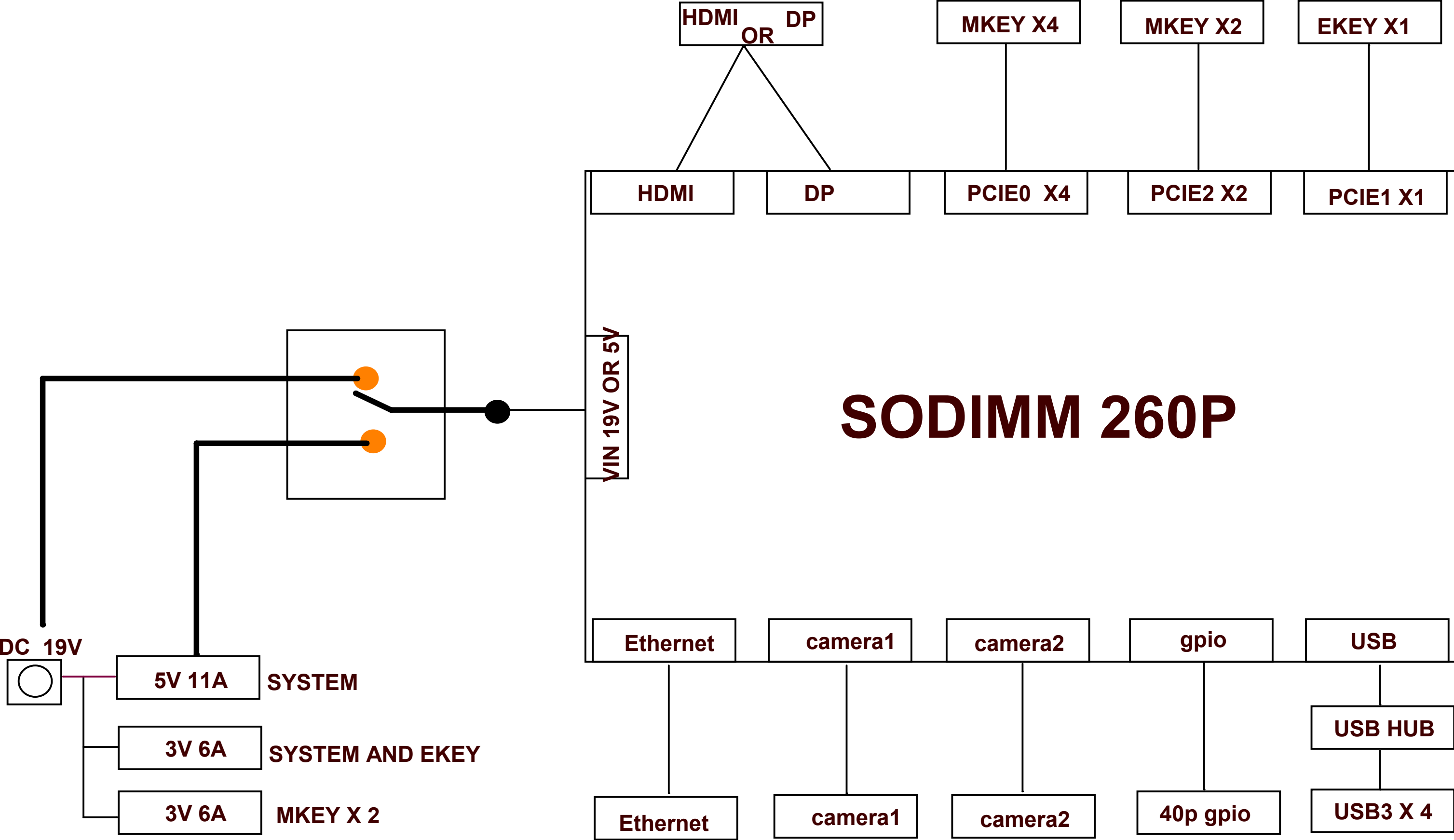
V1.11



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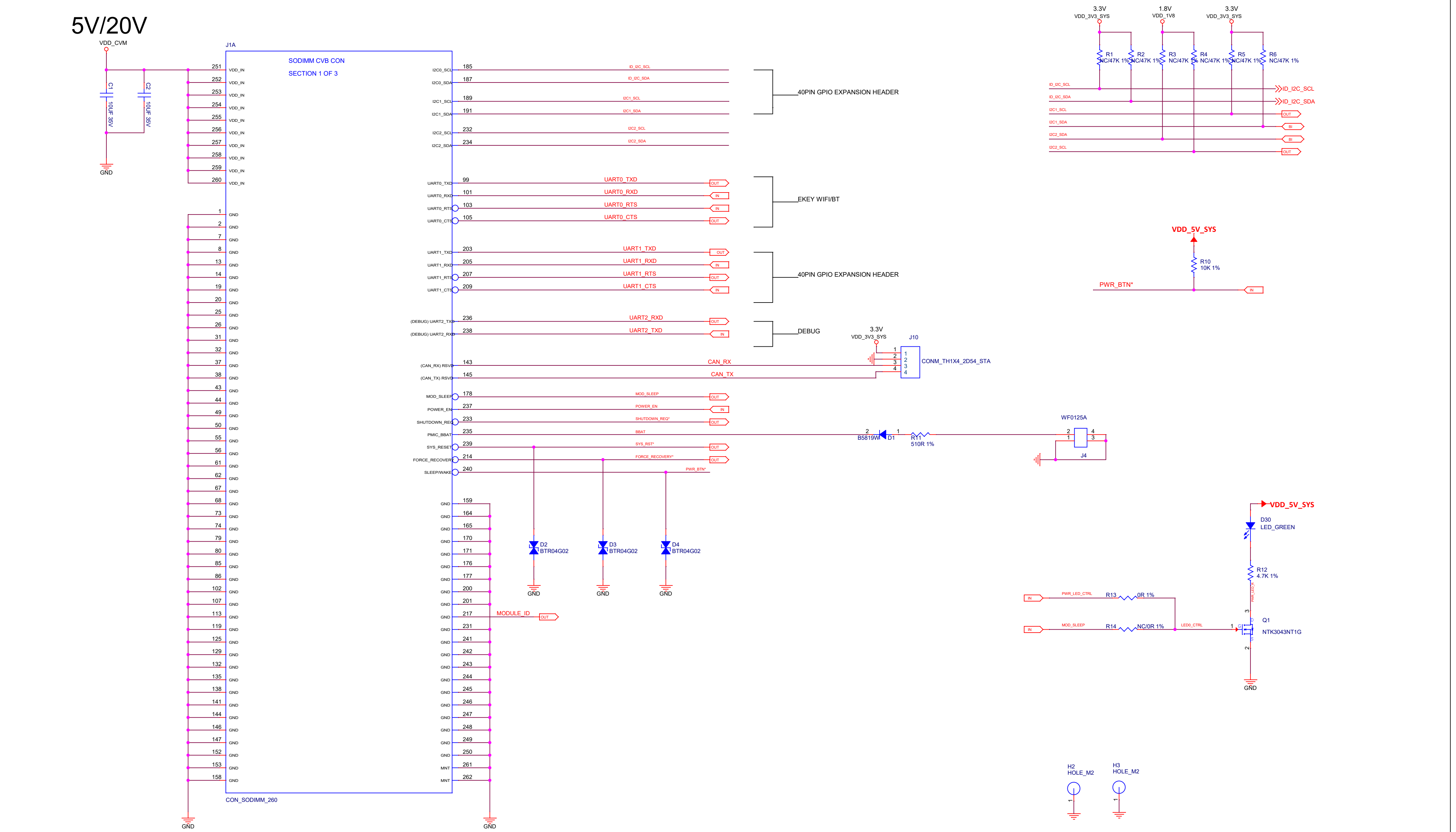
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Block diagram

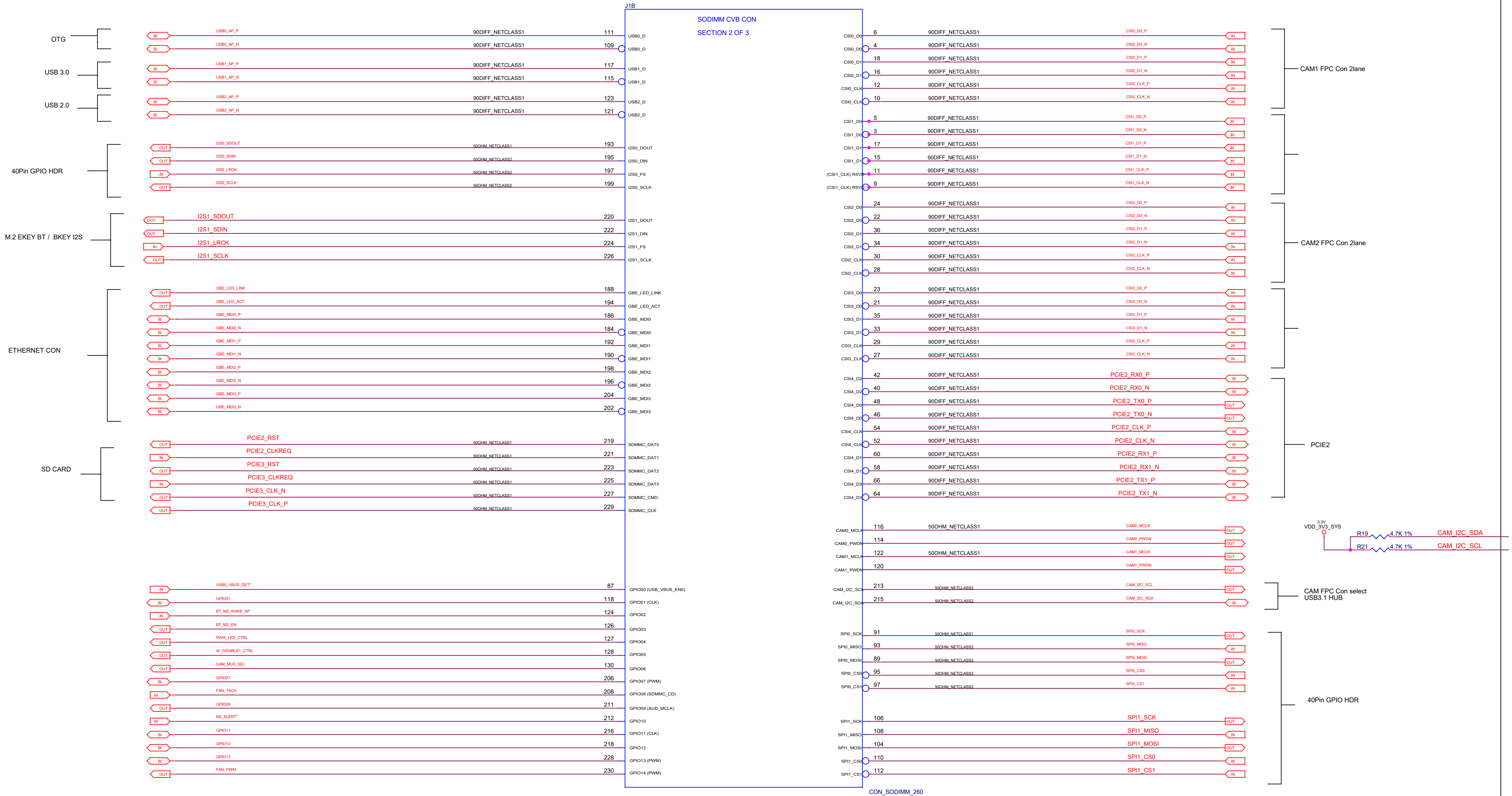


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A4	Page Name: 03_Block diagram	V1.11
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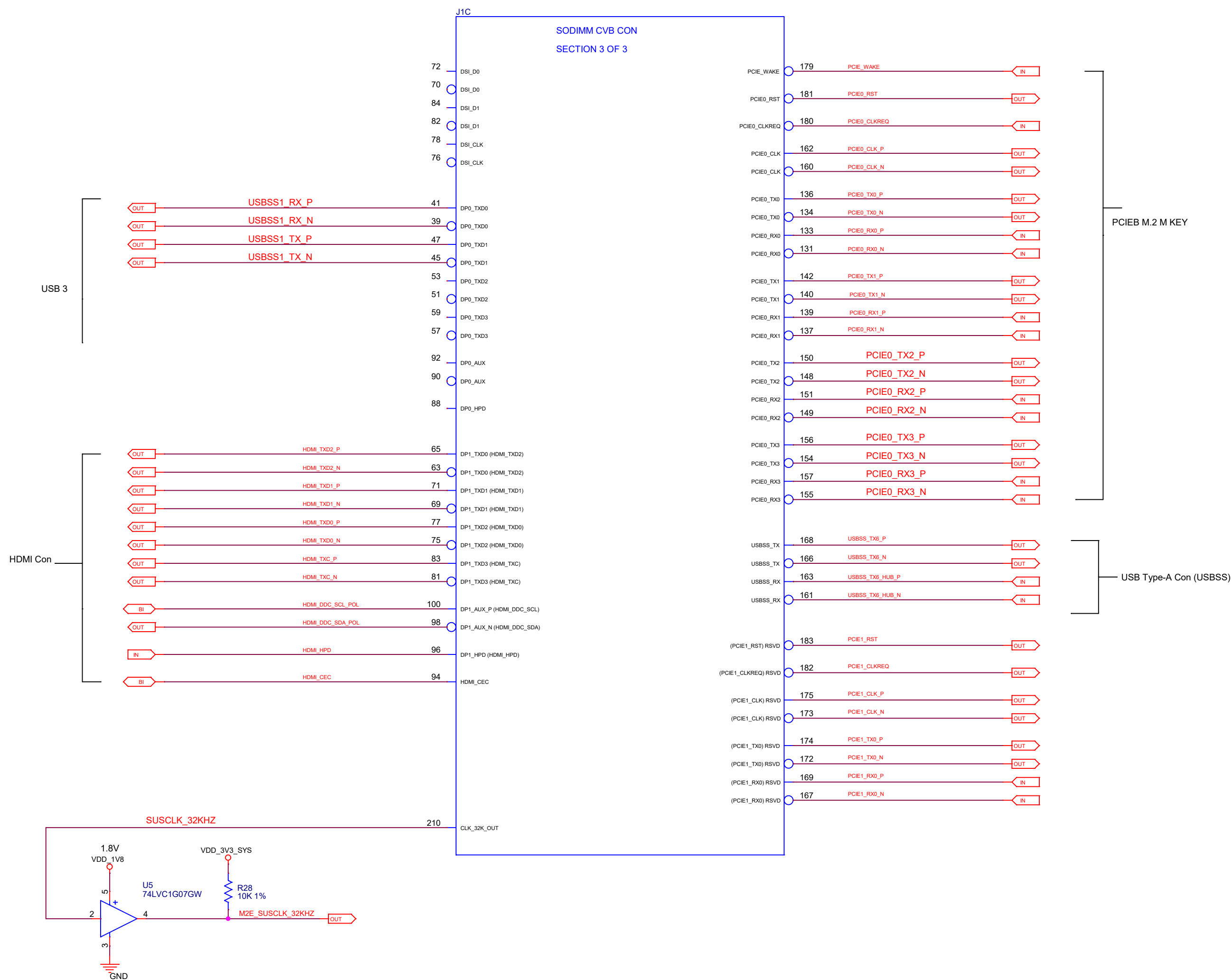
UART, I2C, CAN and GPIOs



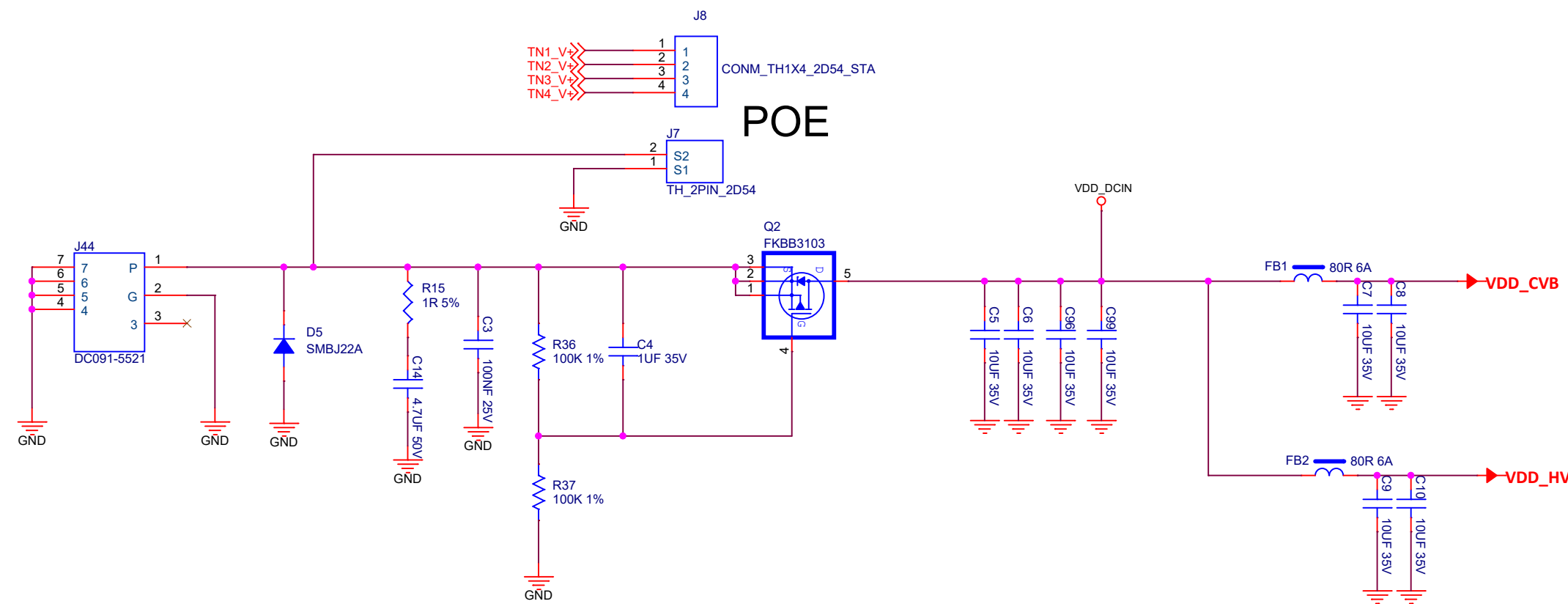
GPIOs, USB2.0, I2S, SPI, GBe and CSI



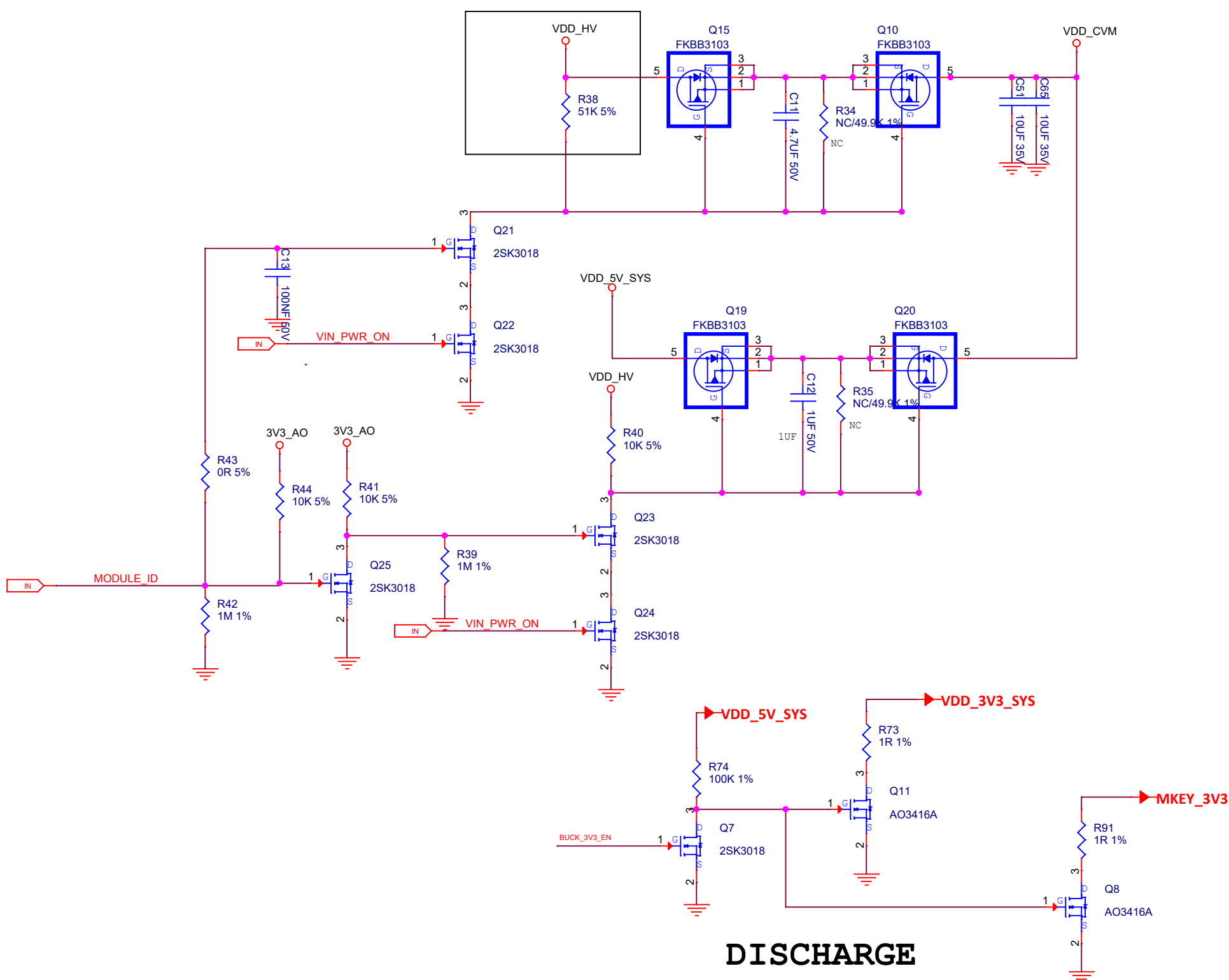
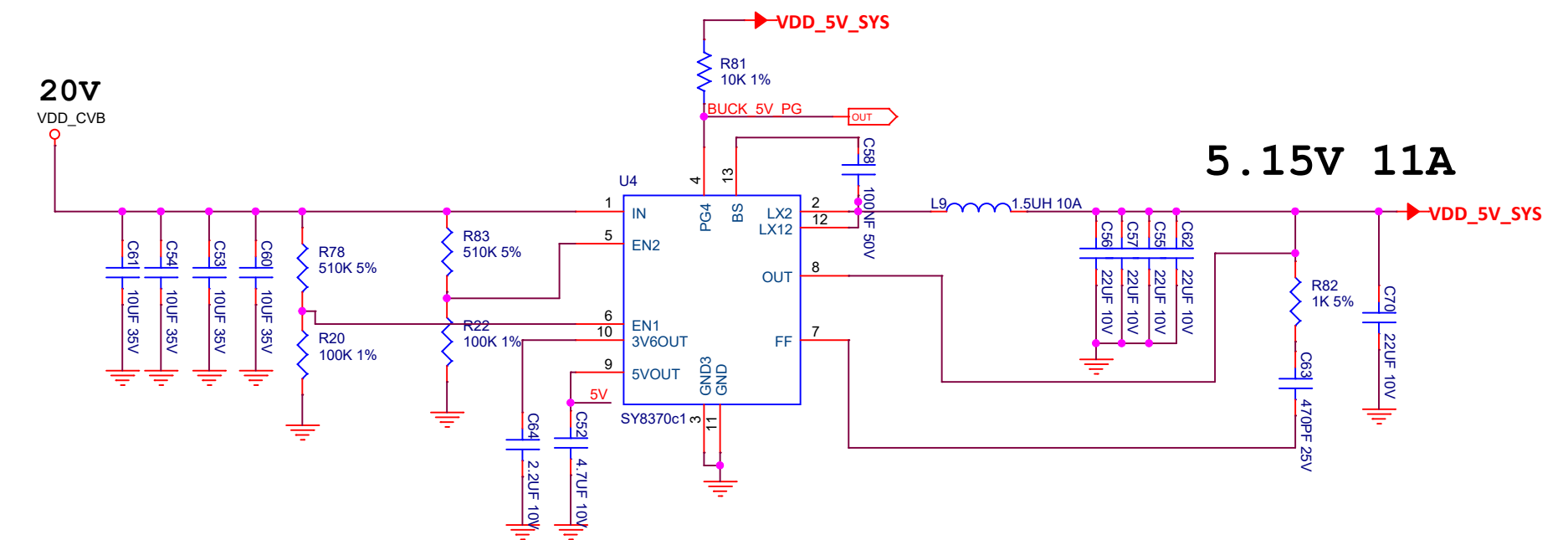
HDMI, DP, DSI, USBSS and PCIe



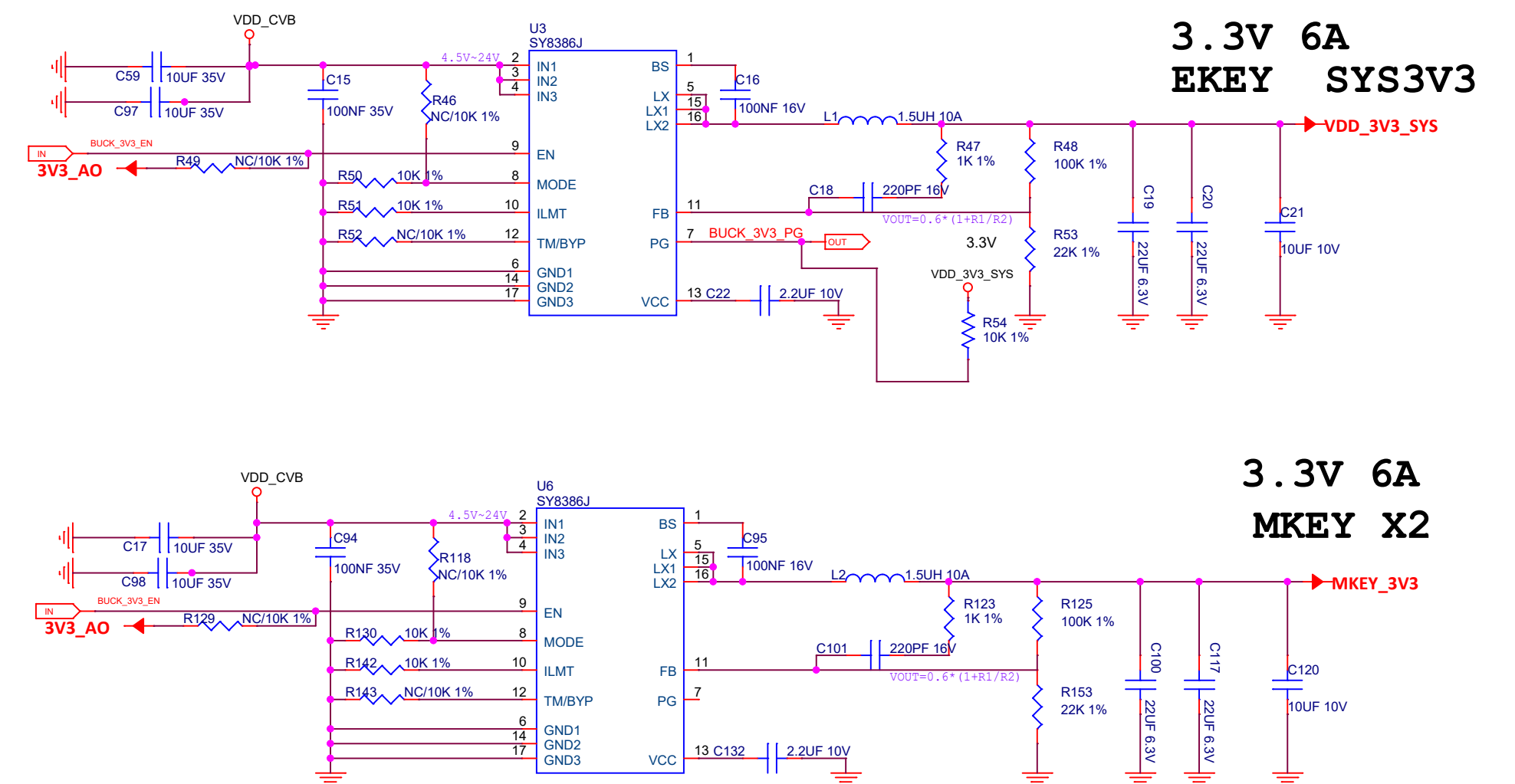
DC JACK POWER



PRE-REG 5V DCDC

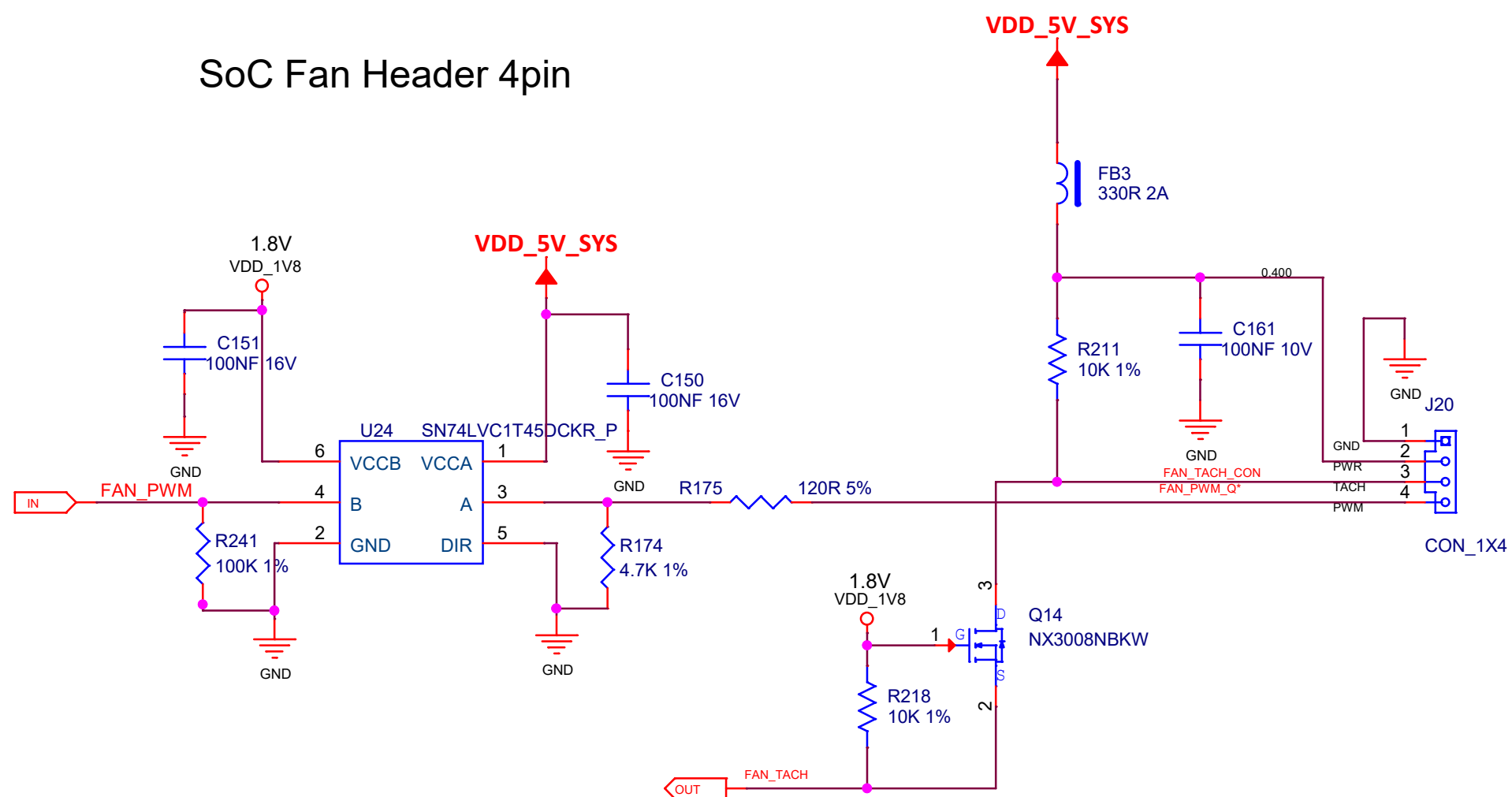


DISCHARGE

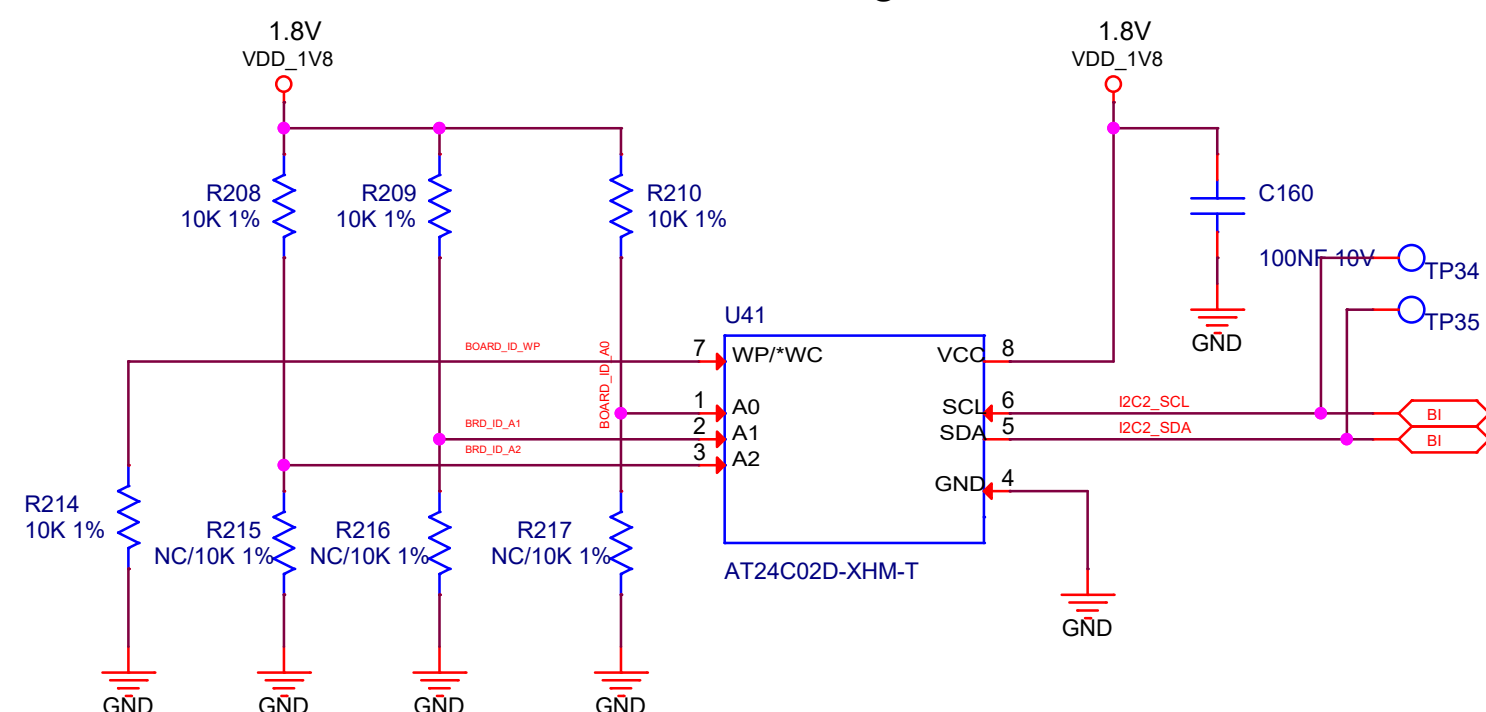


Size	Title: C200	REV
A2	Page Name: 07_DC Jack and DCDC	V1.1
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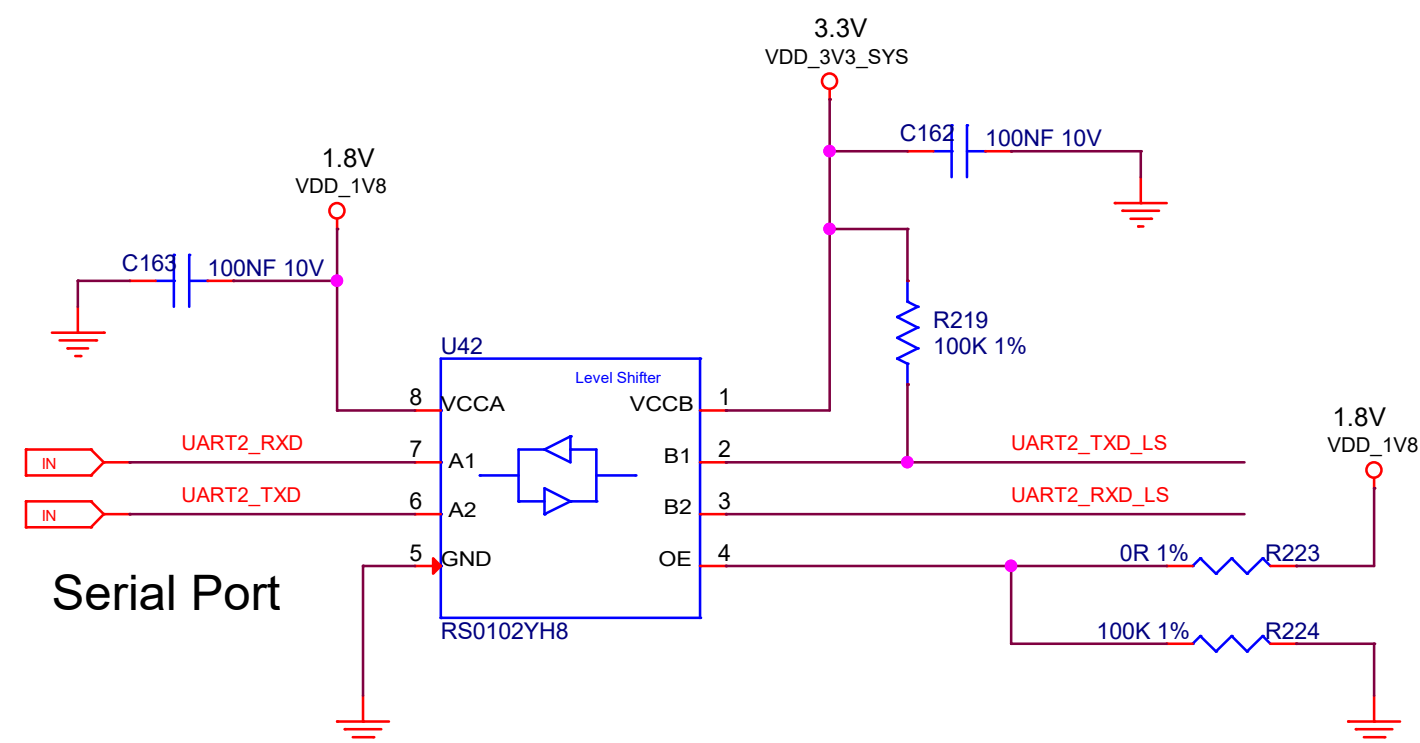
SoC Fan Header 4pin



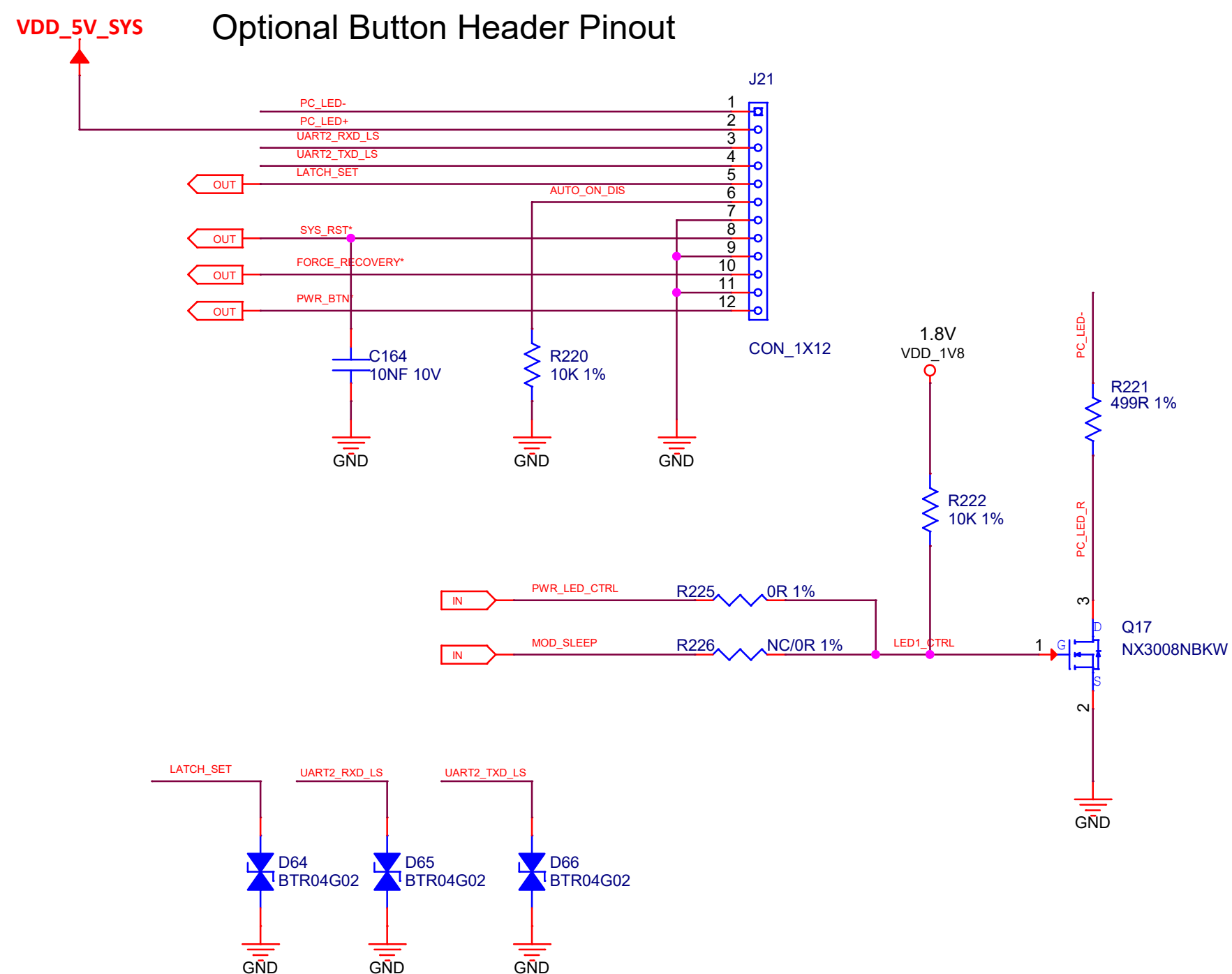
Carrier board config



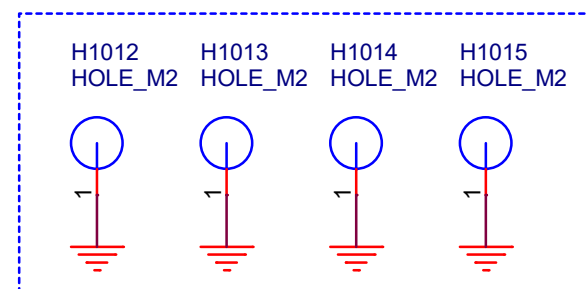
Serial Port



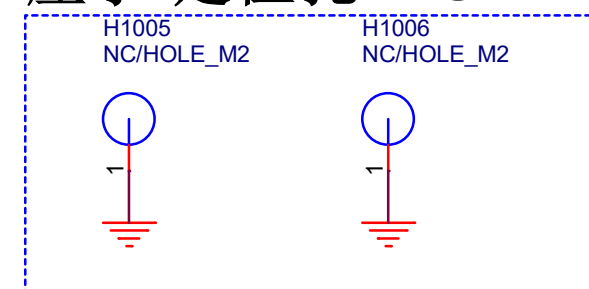
Optional Button Header Pinout



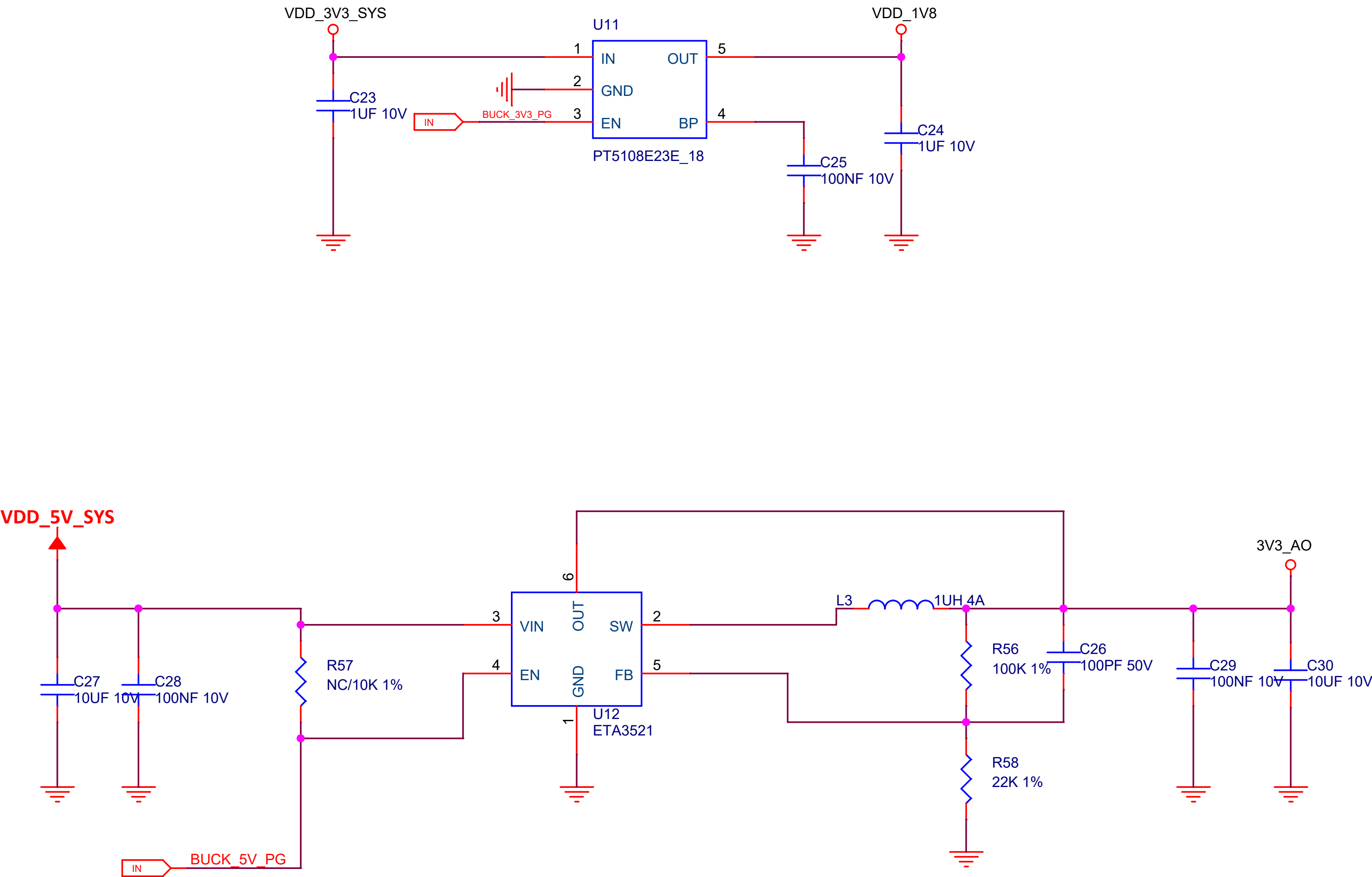
PCB 定位孔 HOLE



座子 定位孔 **HOLE**

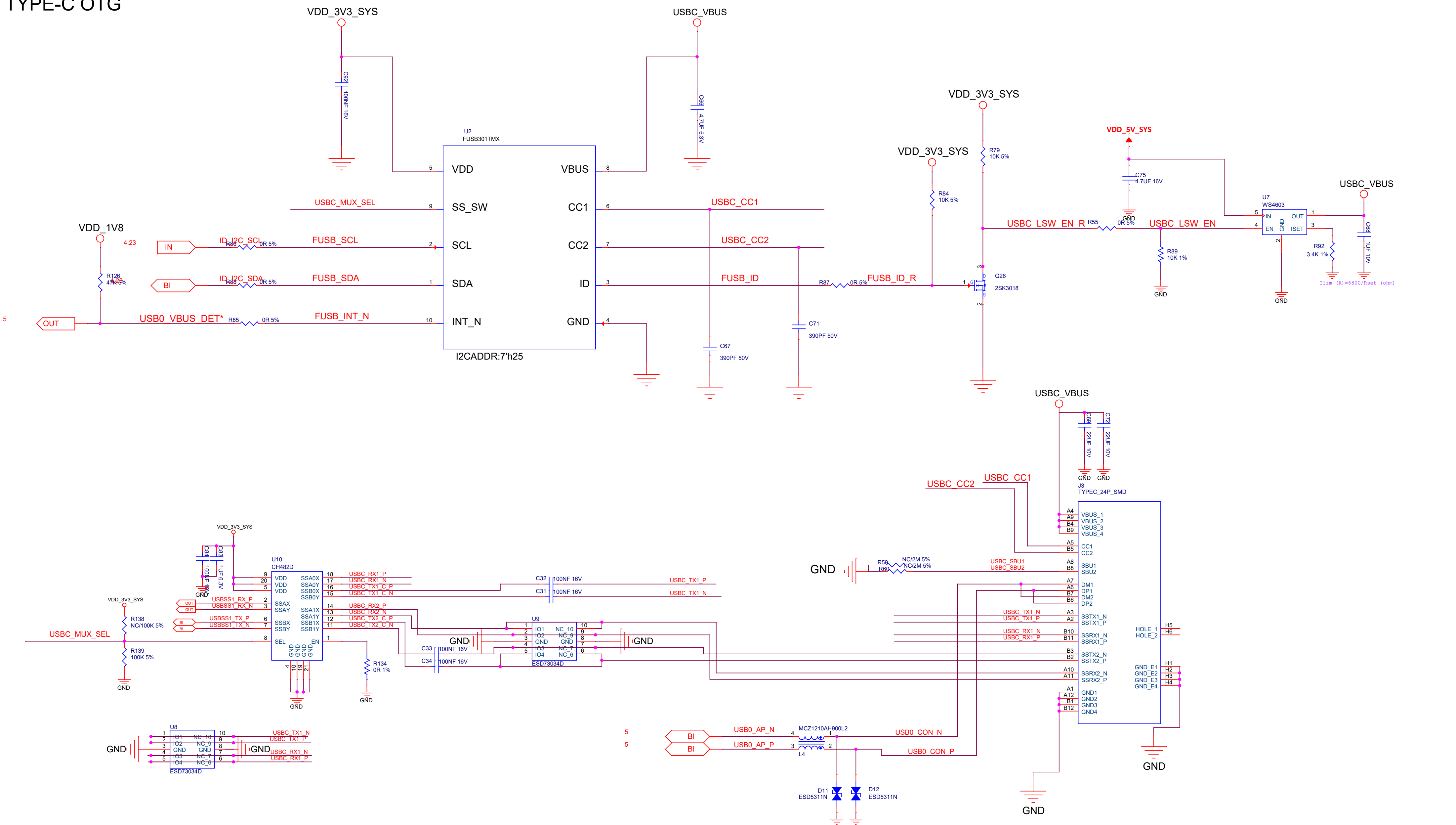


1V8 LDO AND 3V3 AO RAILS

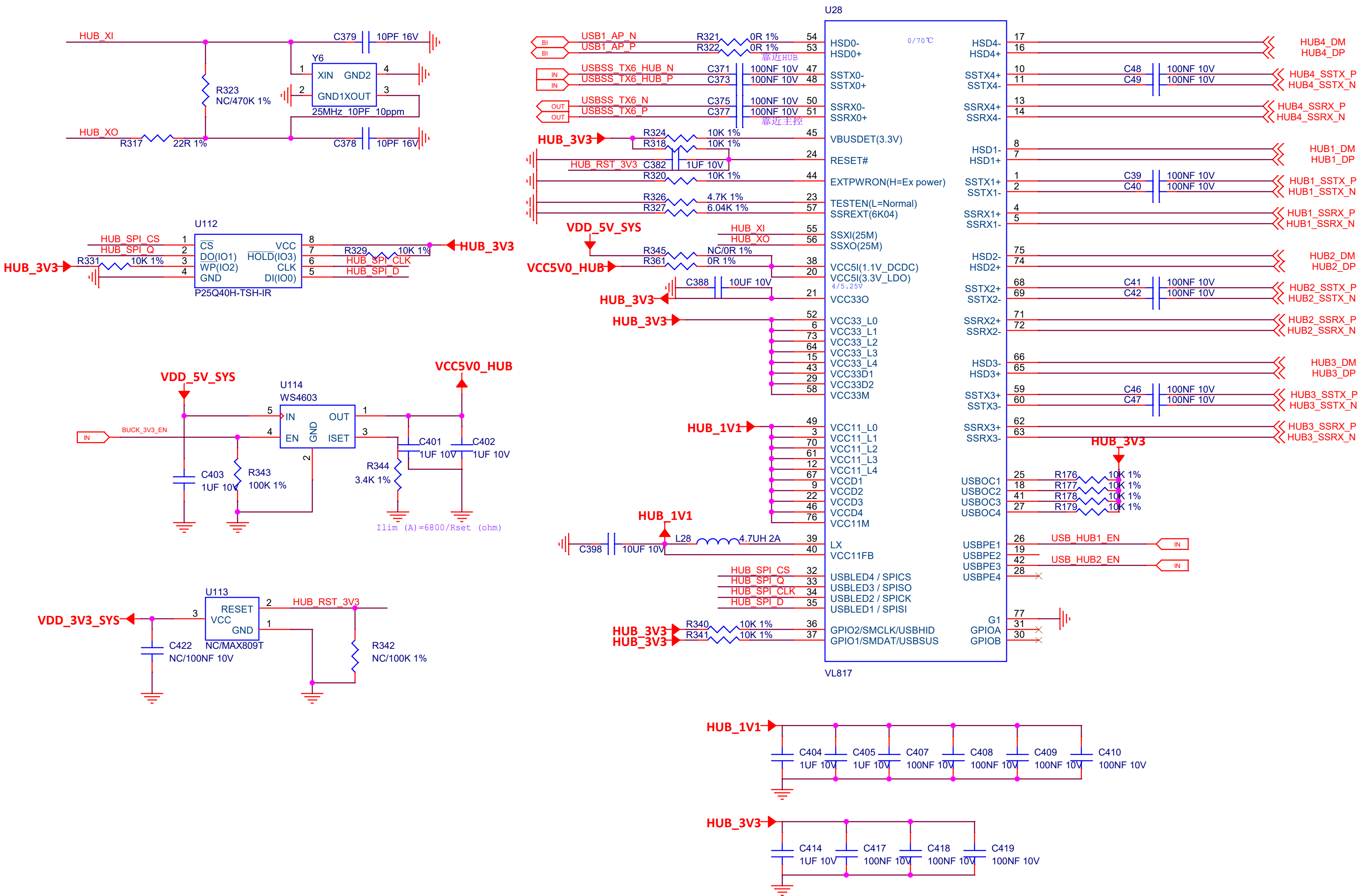


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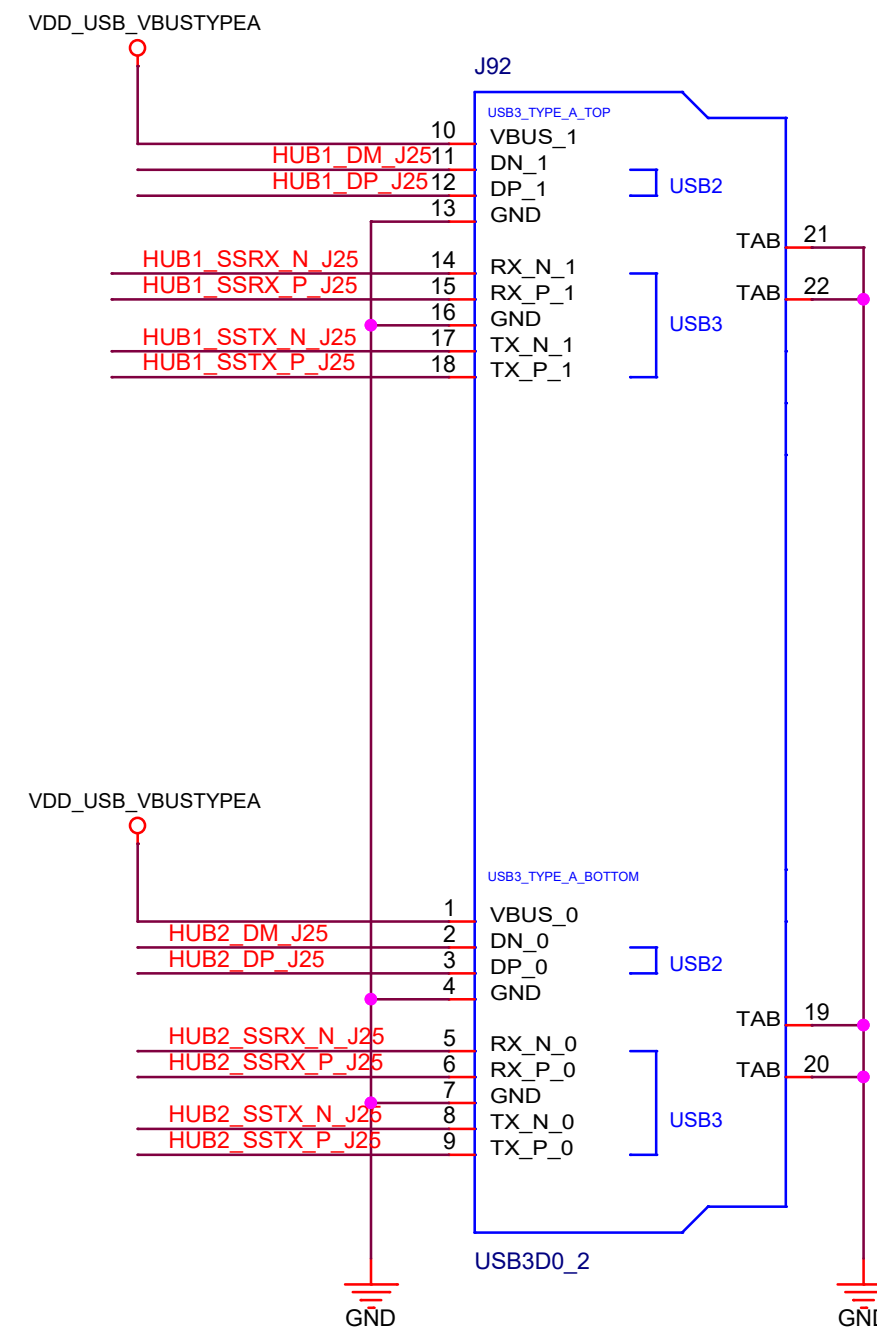
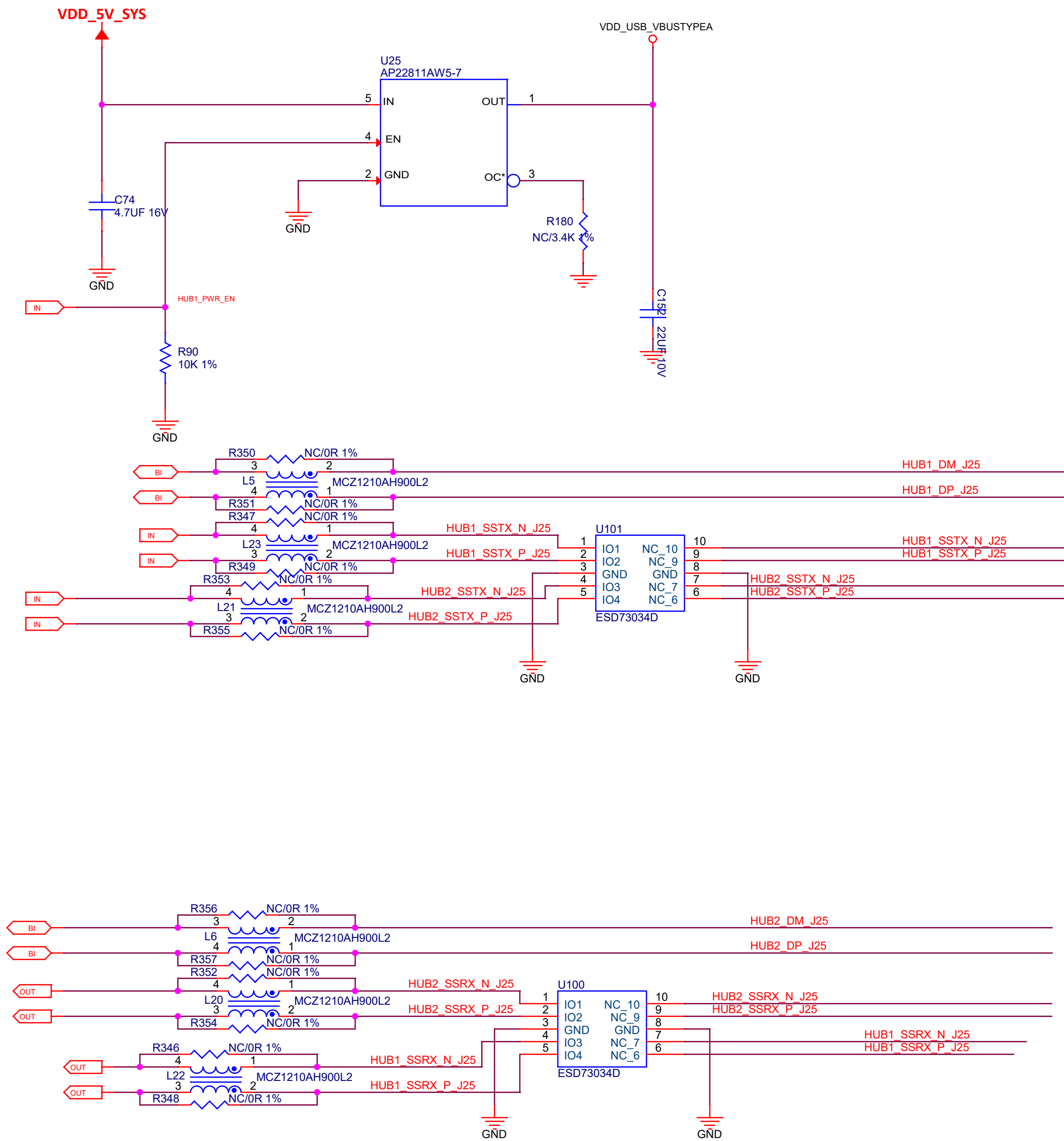
TYPE-C OTG



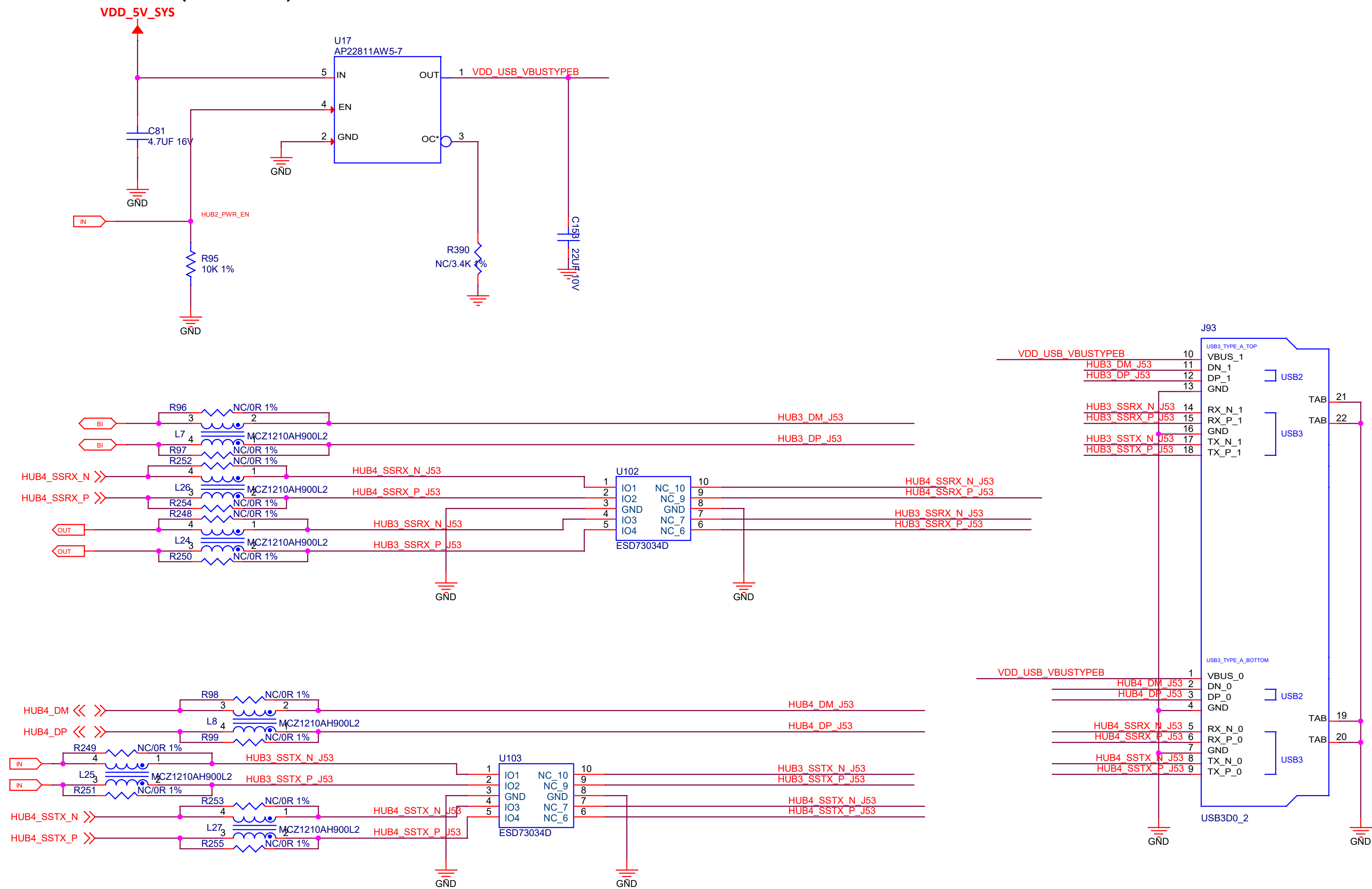
USB3.1 HUB x4 PORTS



USB 3.0 TYPE A x2 CONNECTOR (1 OF 2)

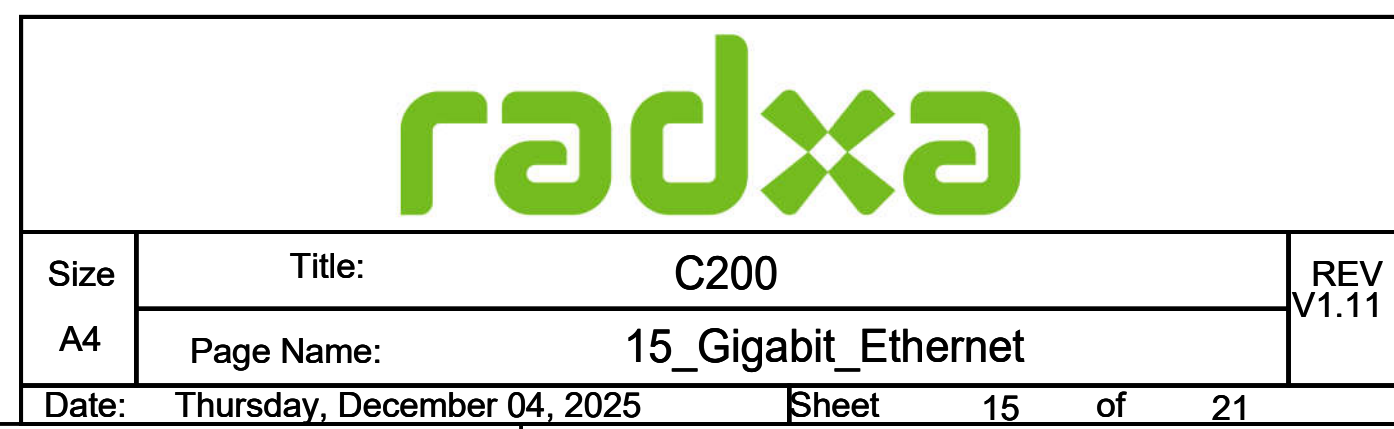


USB 3.0 TYPE A x2 CONNECTOR (2 OF 2)

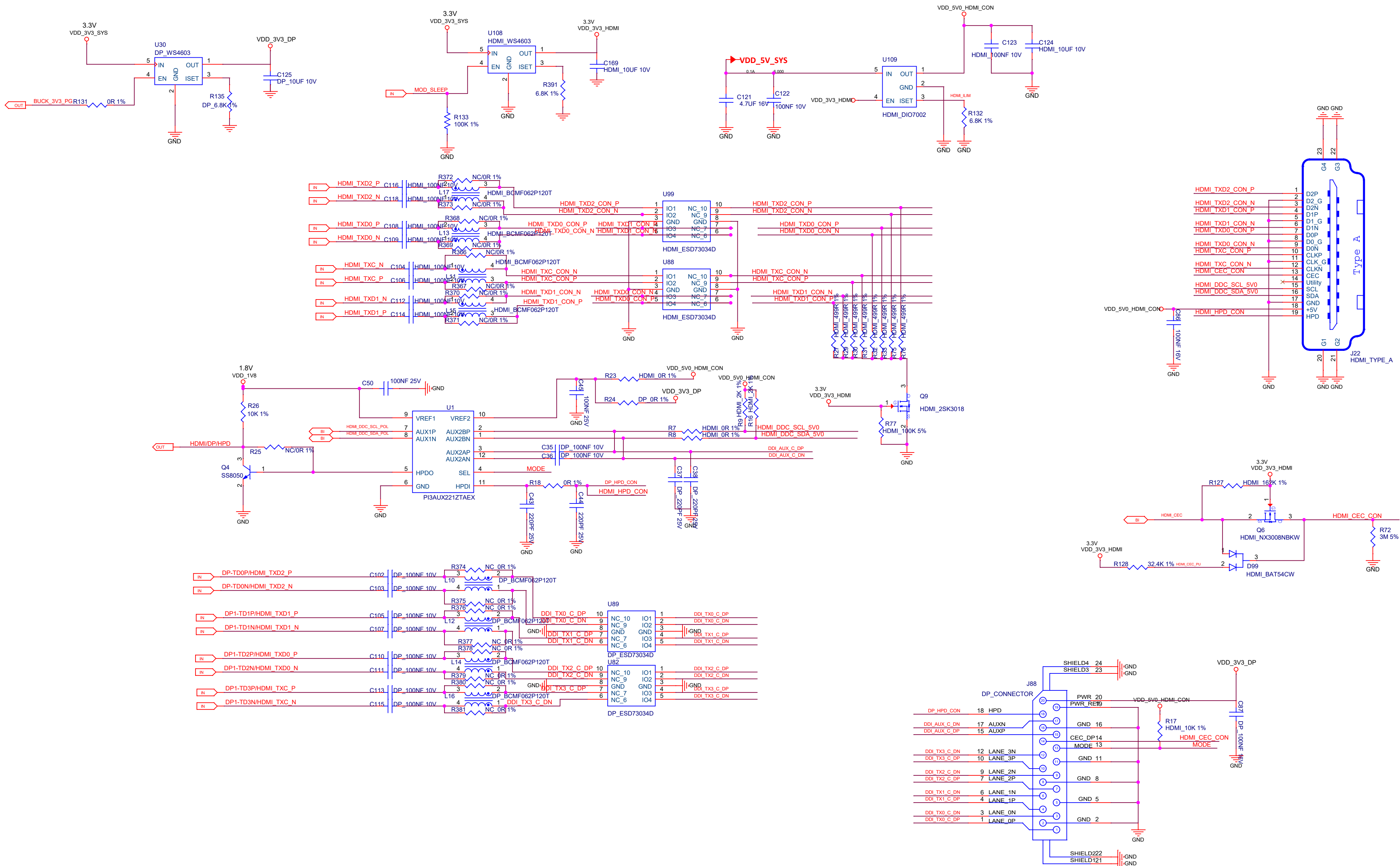


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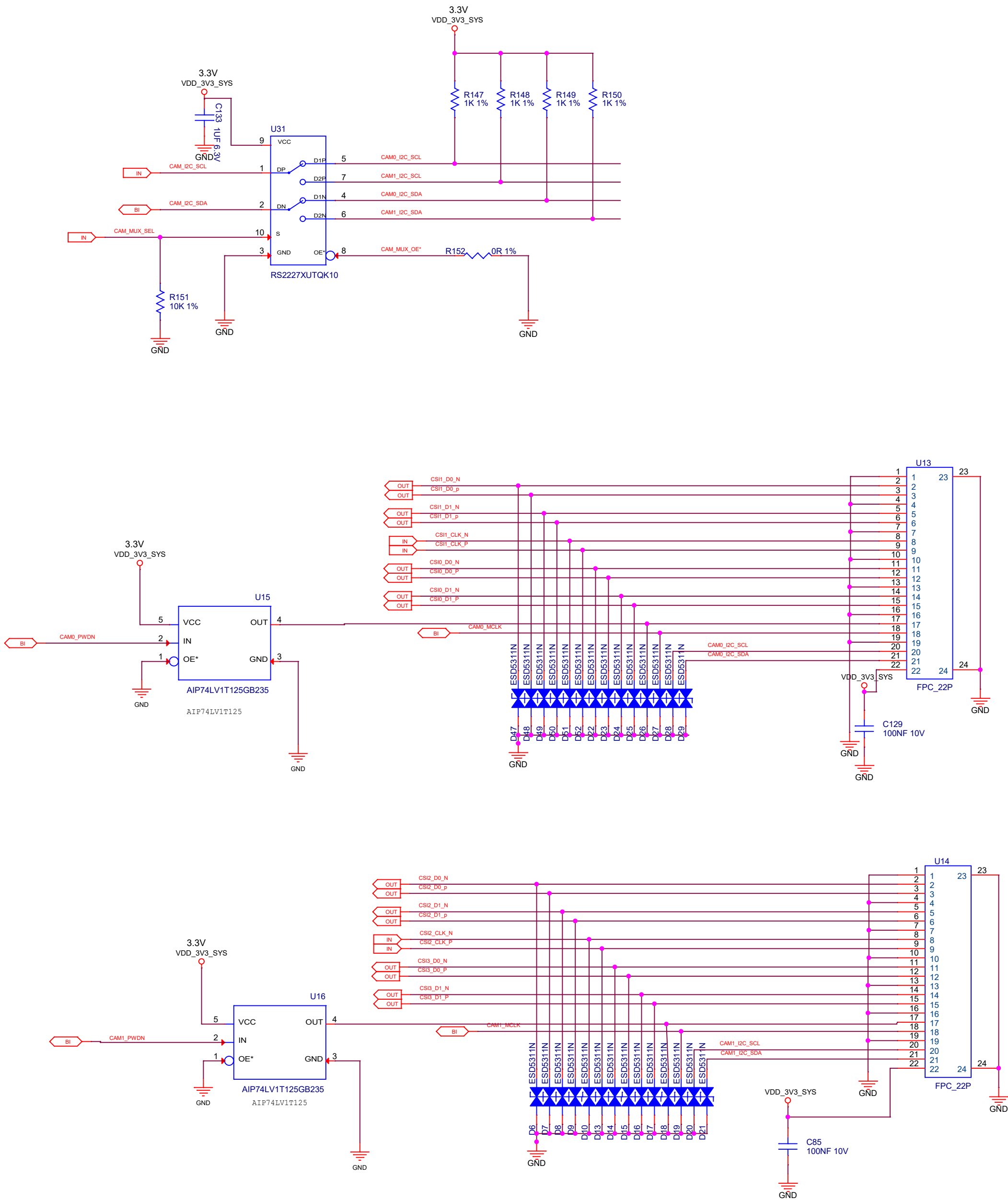
Size	Title: C200		REV
A2	Page Name: 14_BUTTON_MCU		V1.1
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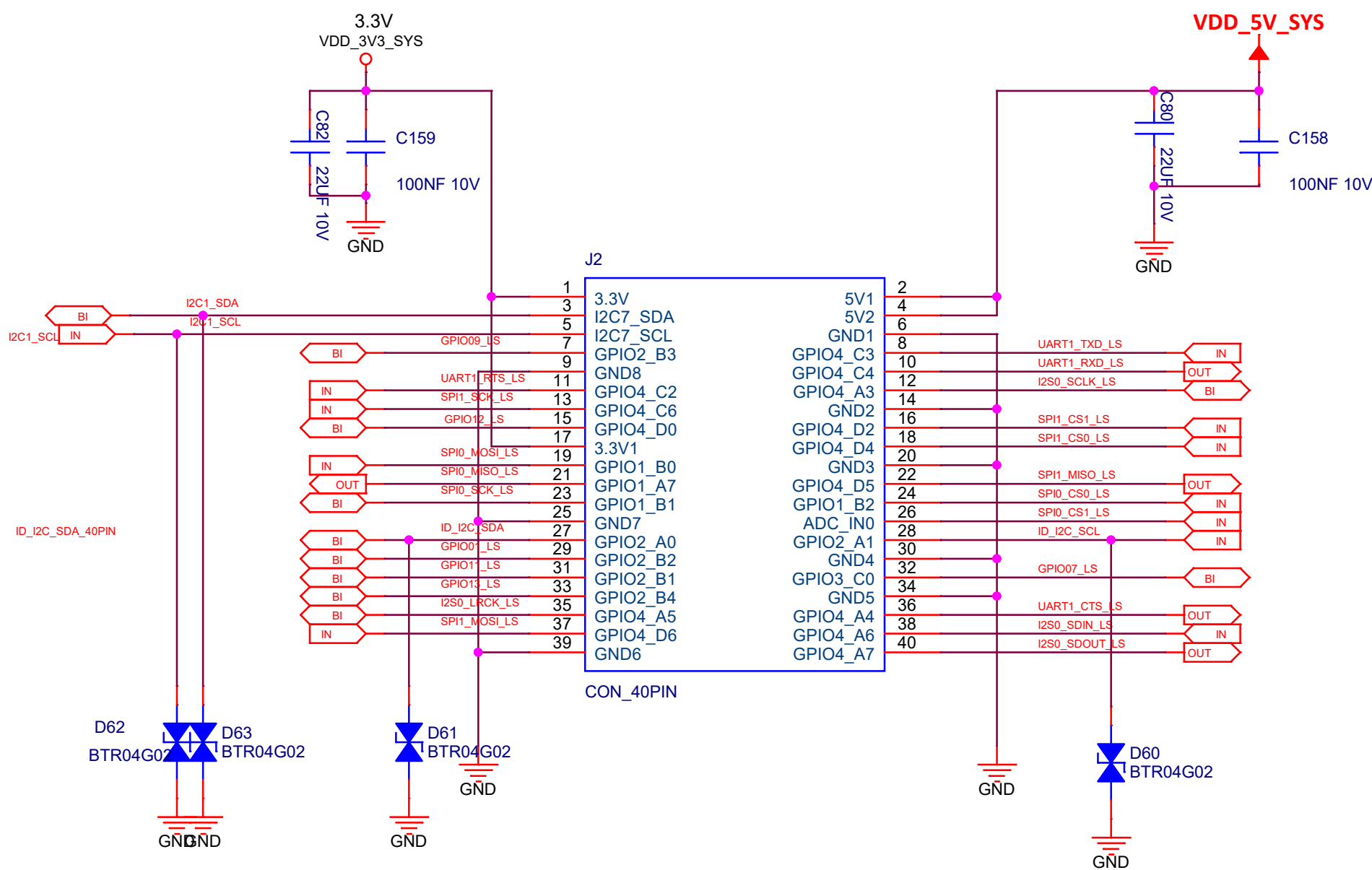
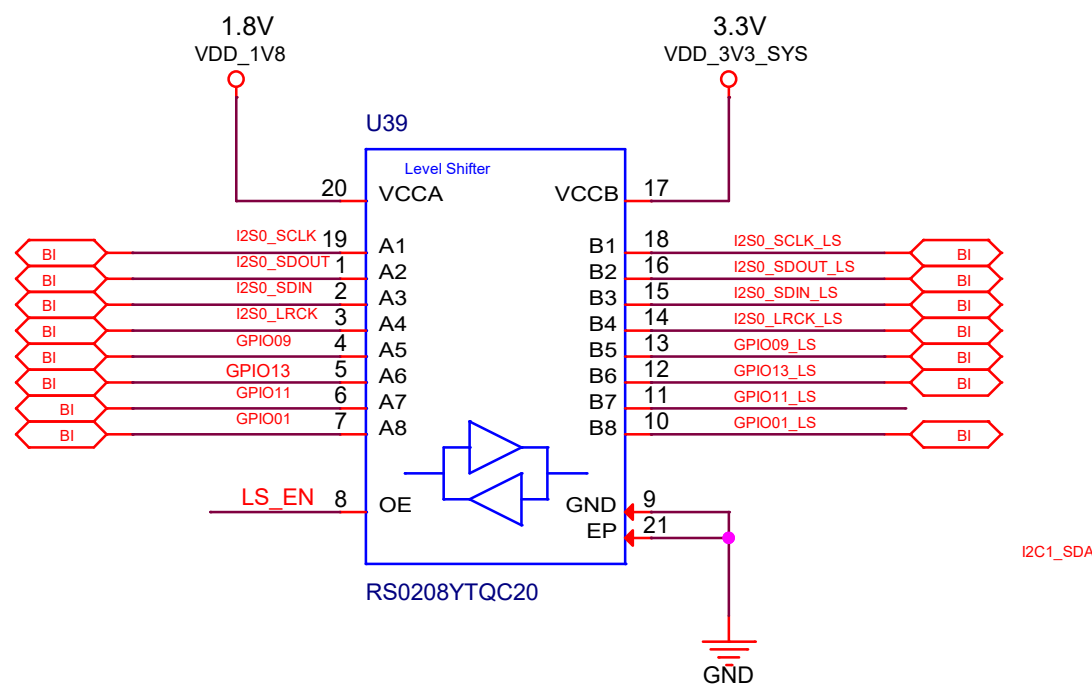
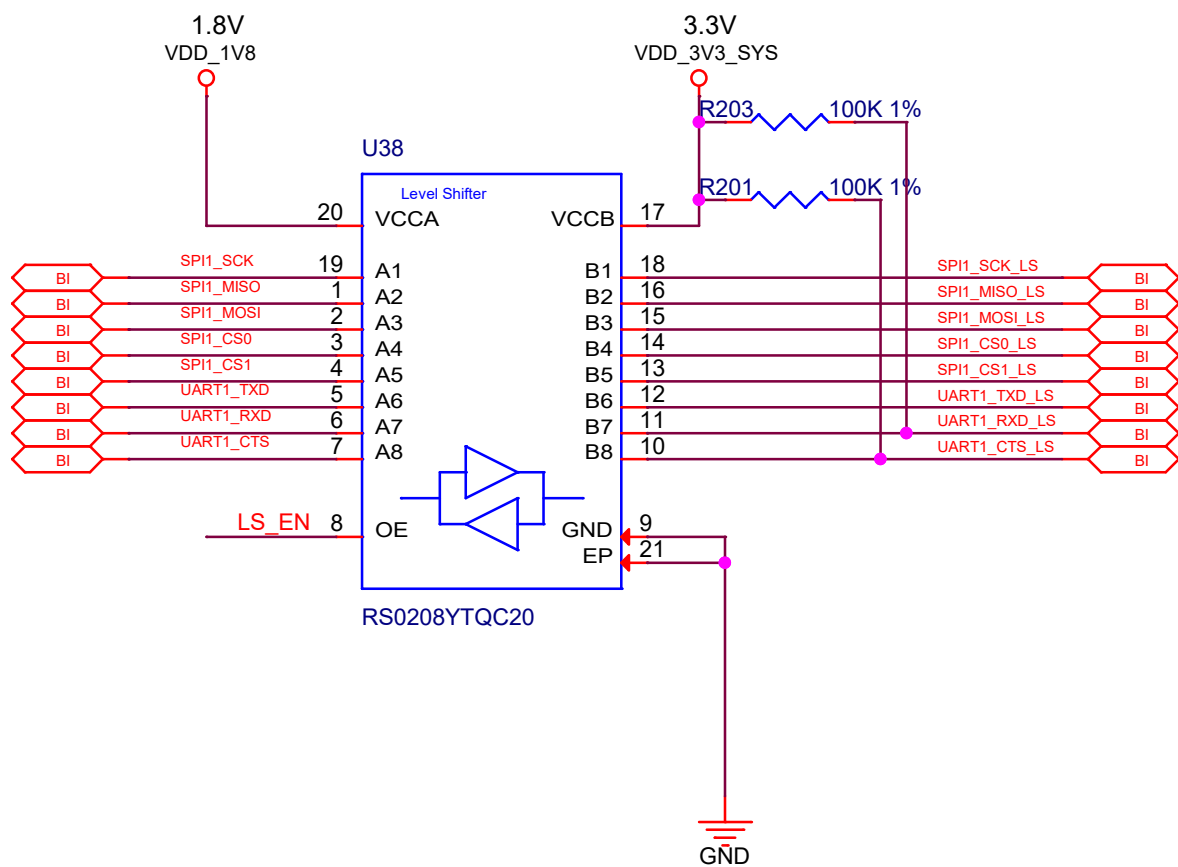
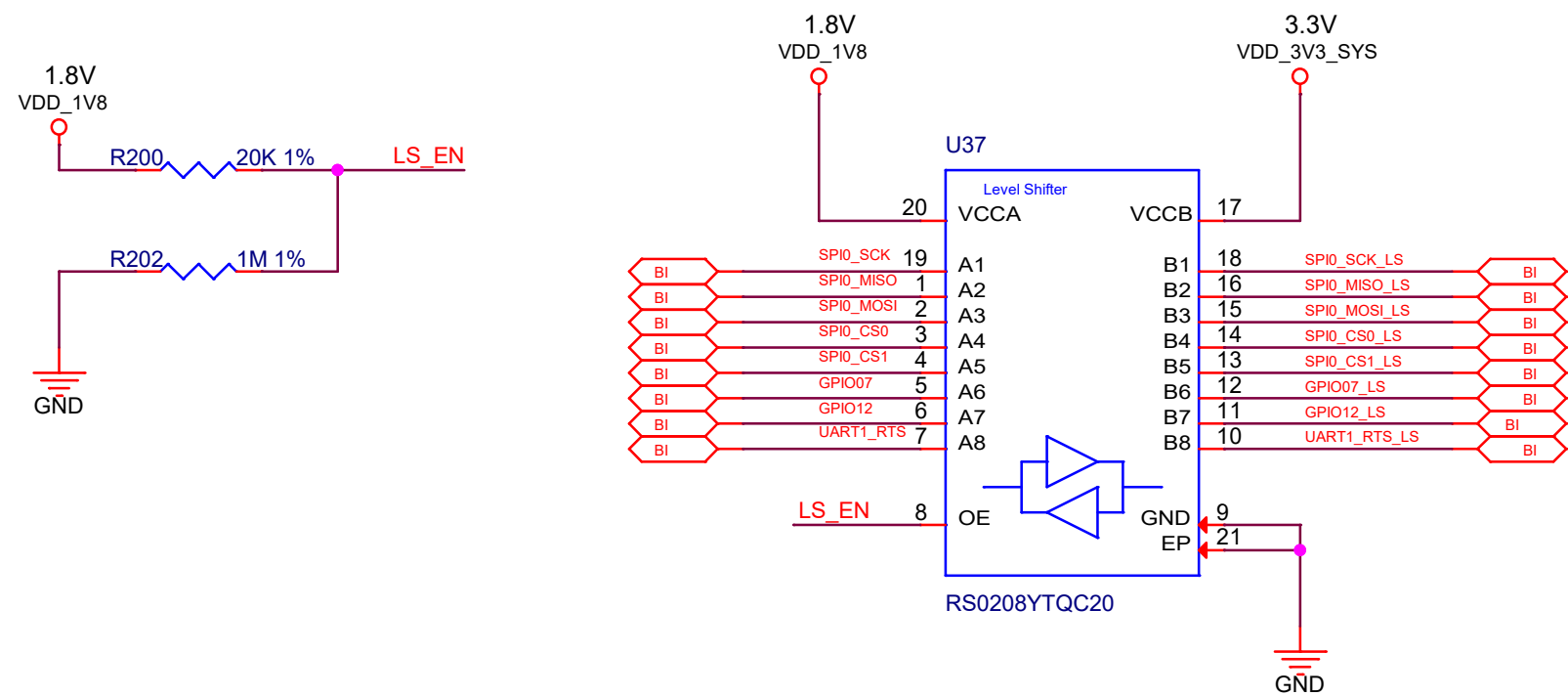
HDMI&DP



CSI CAM CONNECTOR

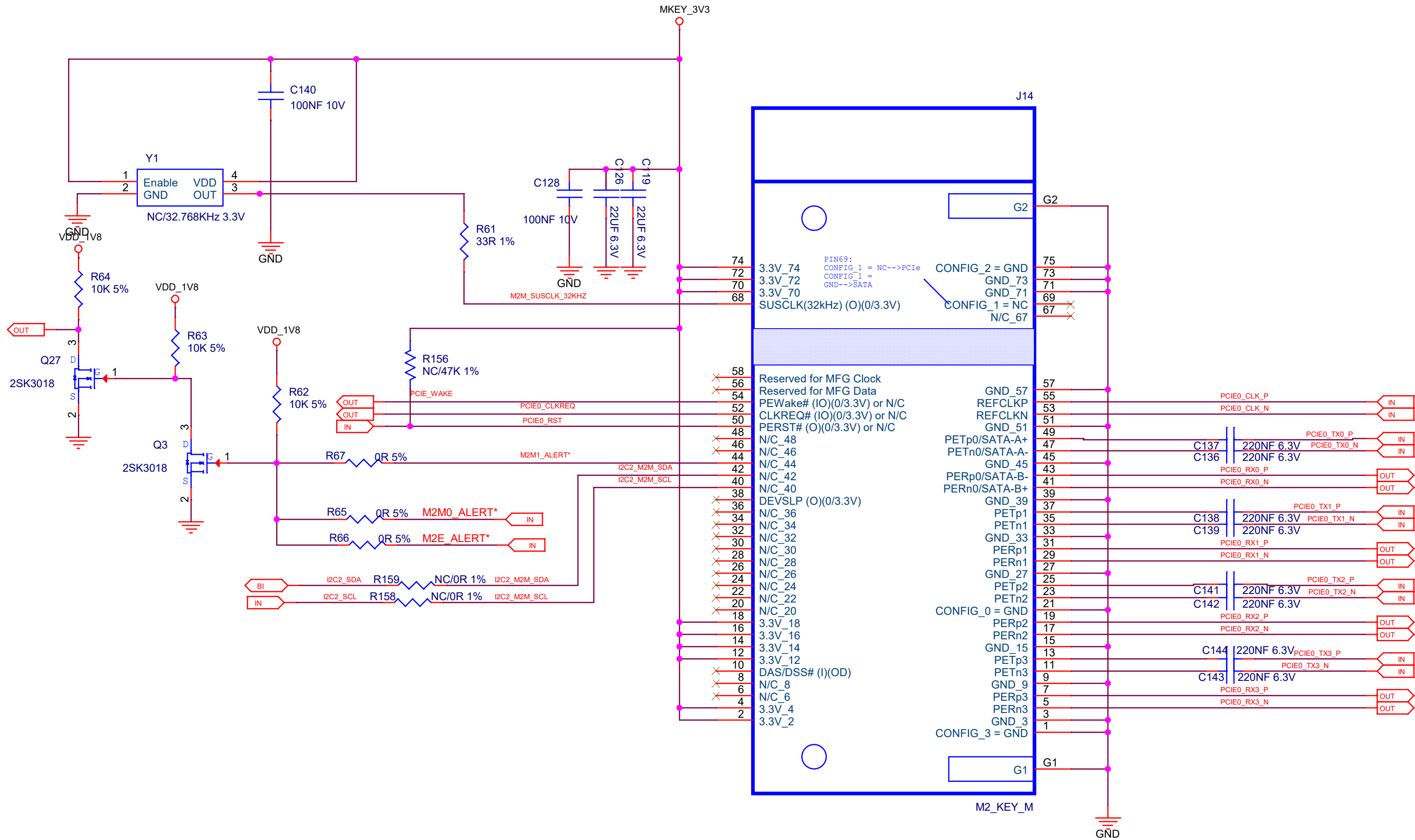


40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS

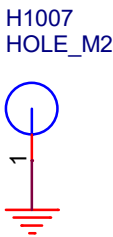


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M.2 KEY-M (NVME)

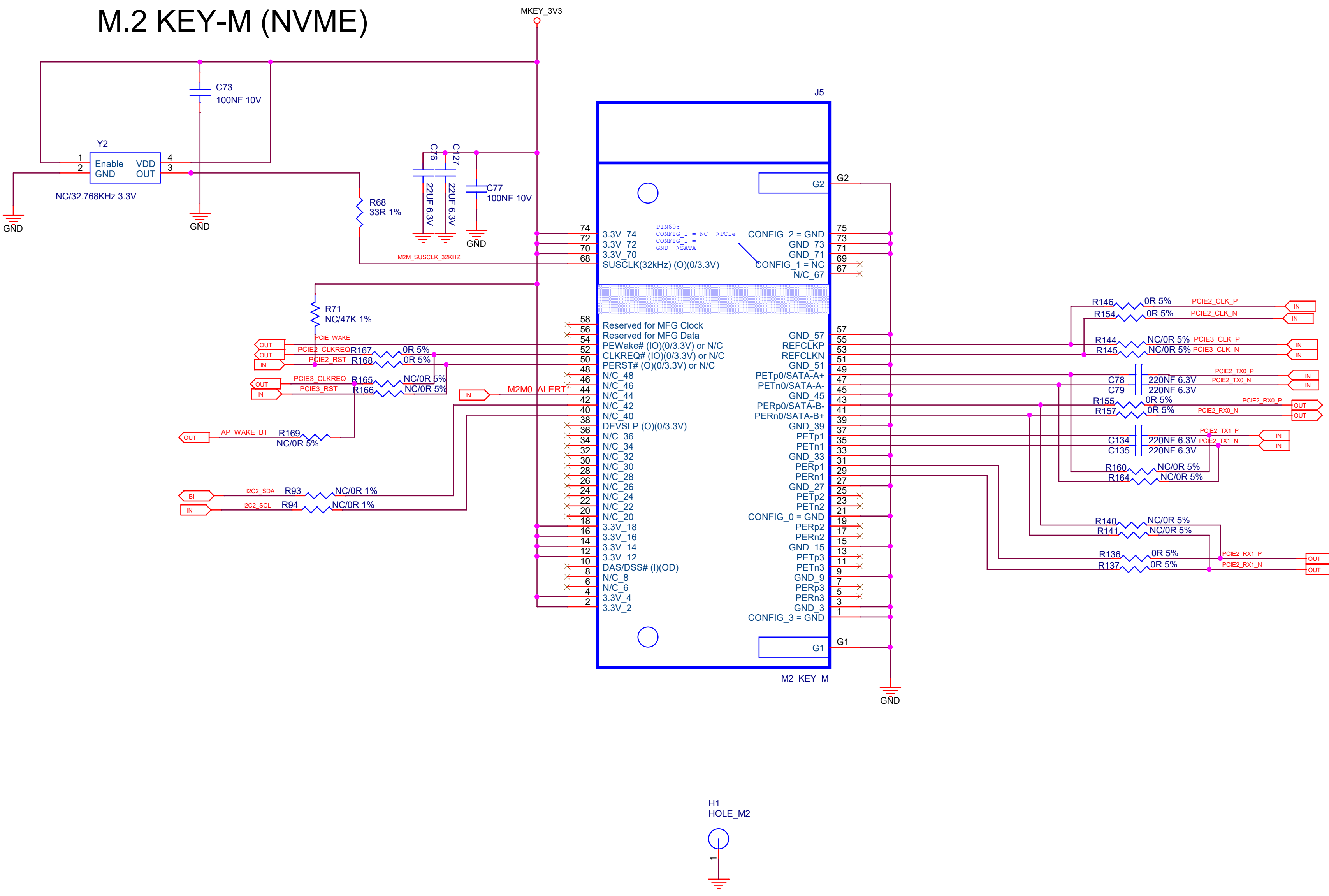


M2 J14_HOLE

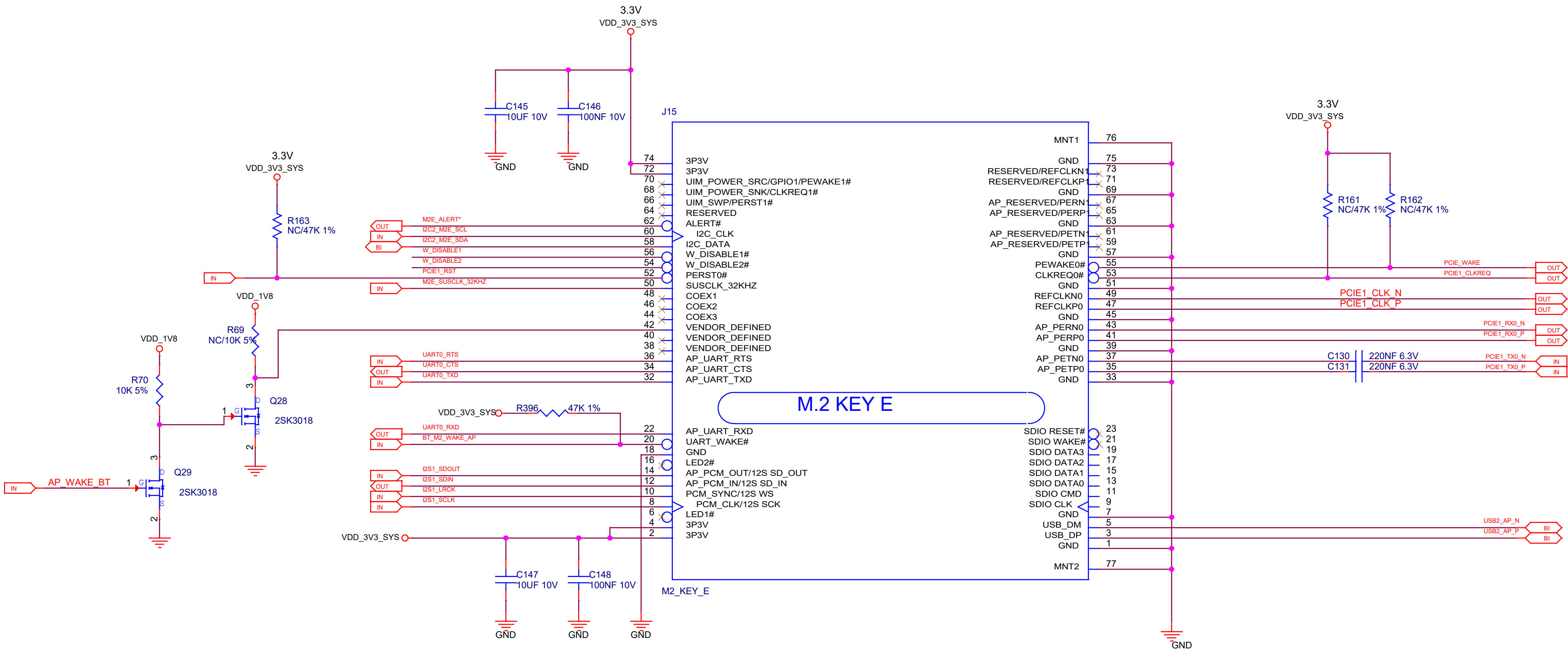


KEY M

M.2 KEY-M (NVME)



M.2 KEY-E (WIFI/BT)



M2 J15_HOLE

