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Radxa C200

schematic

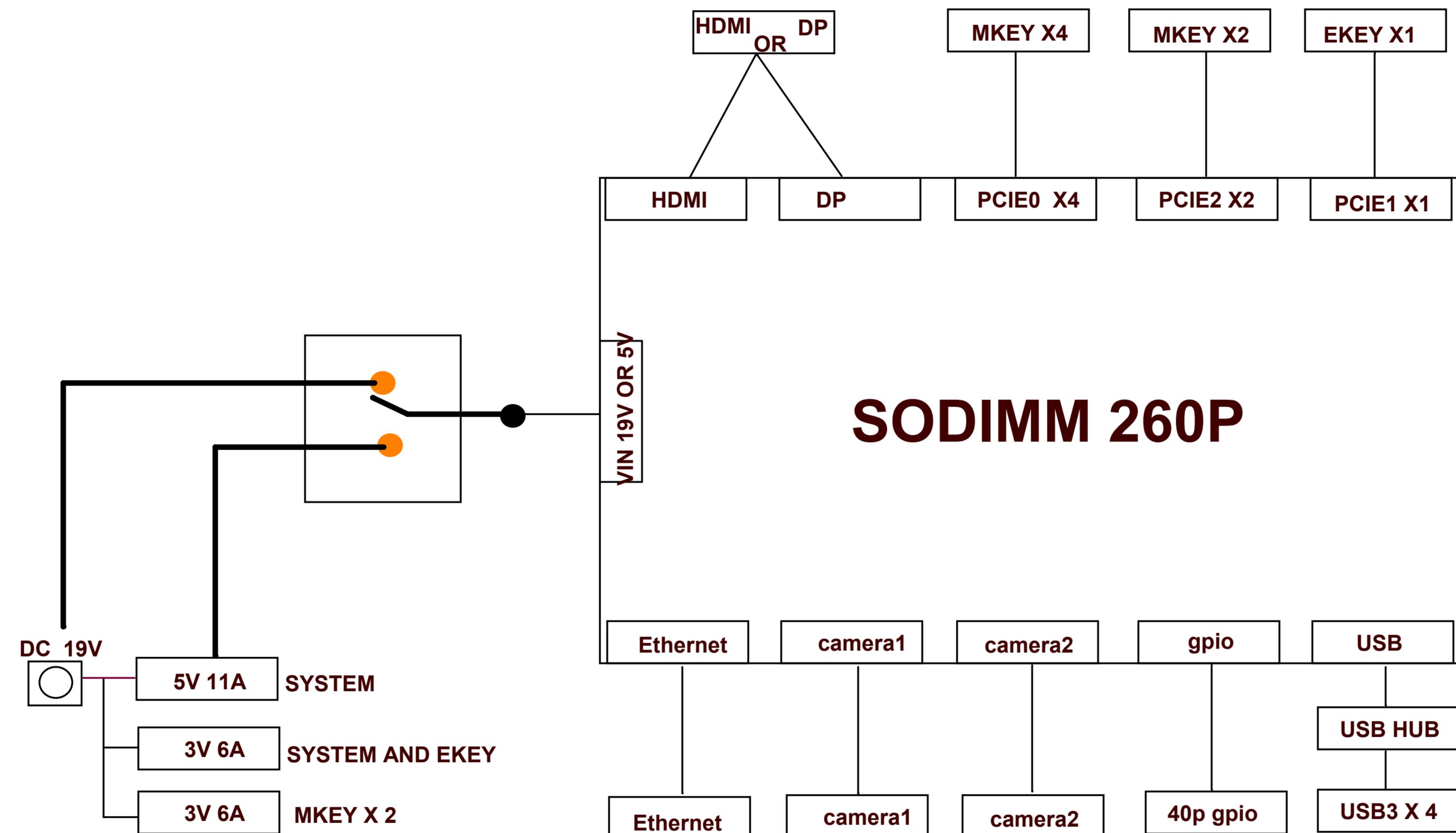
V1.11



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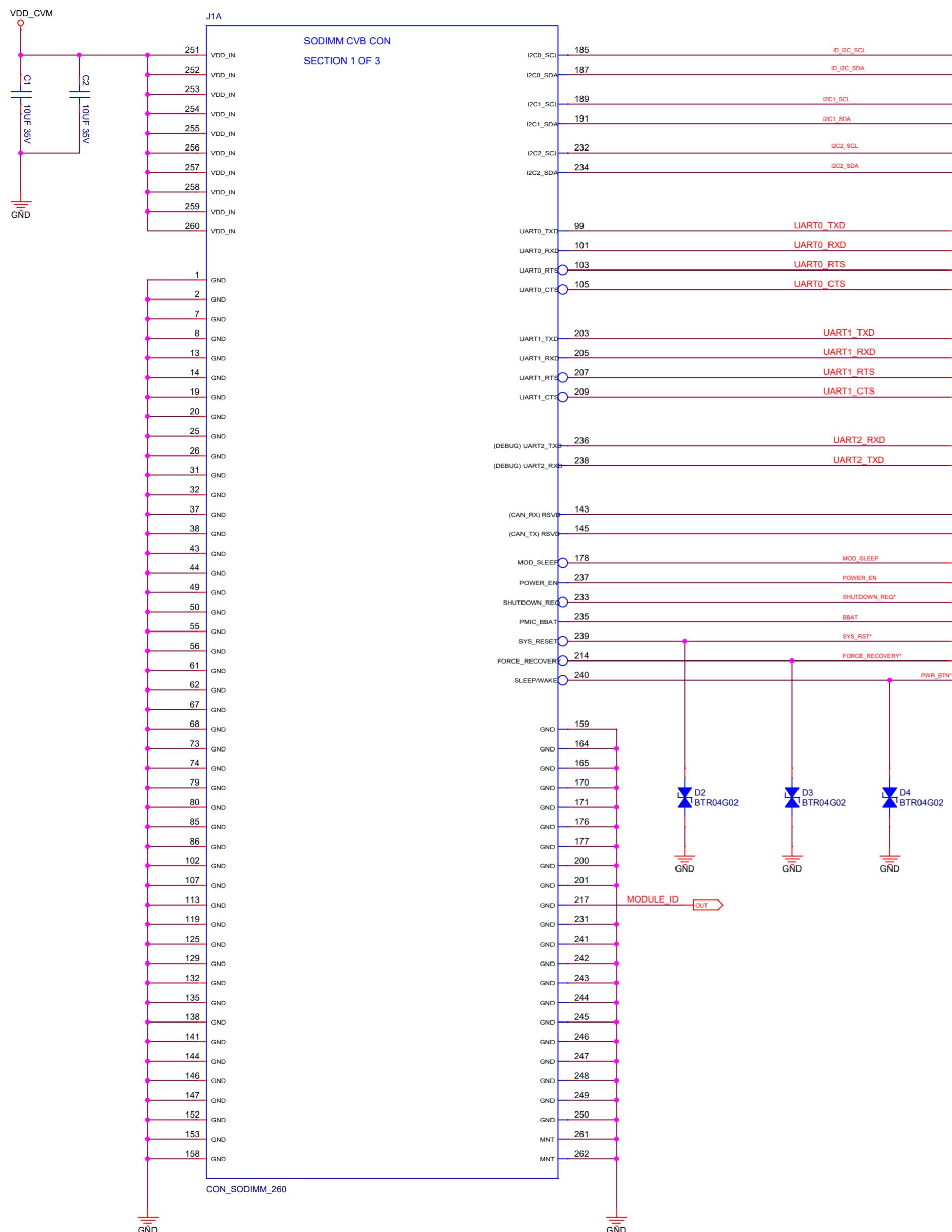
Block diagram



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| Size | Title: | C200 | REV |
| A4 | Page Name: | 03_Block diagram | V1.11 |

5V/20V



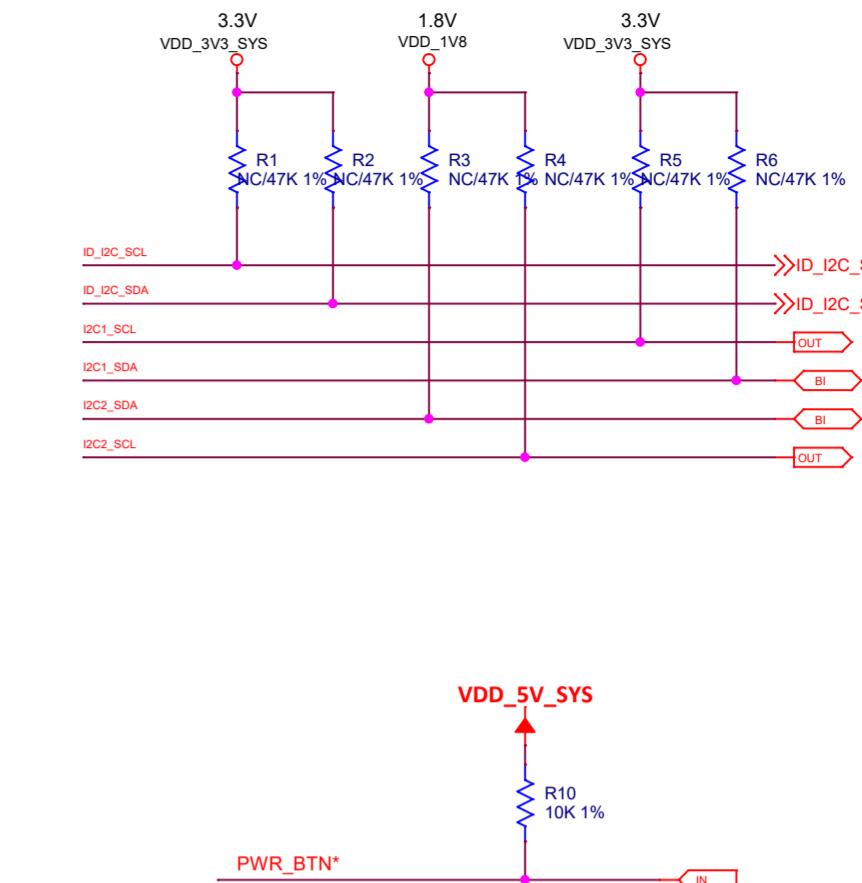
40PIN GPIO EXPANSION HEADER

EKEY WIFI/BT

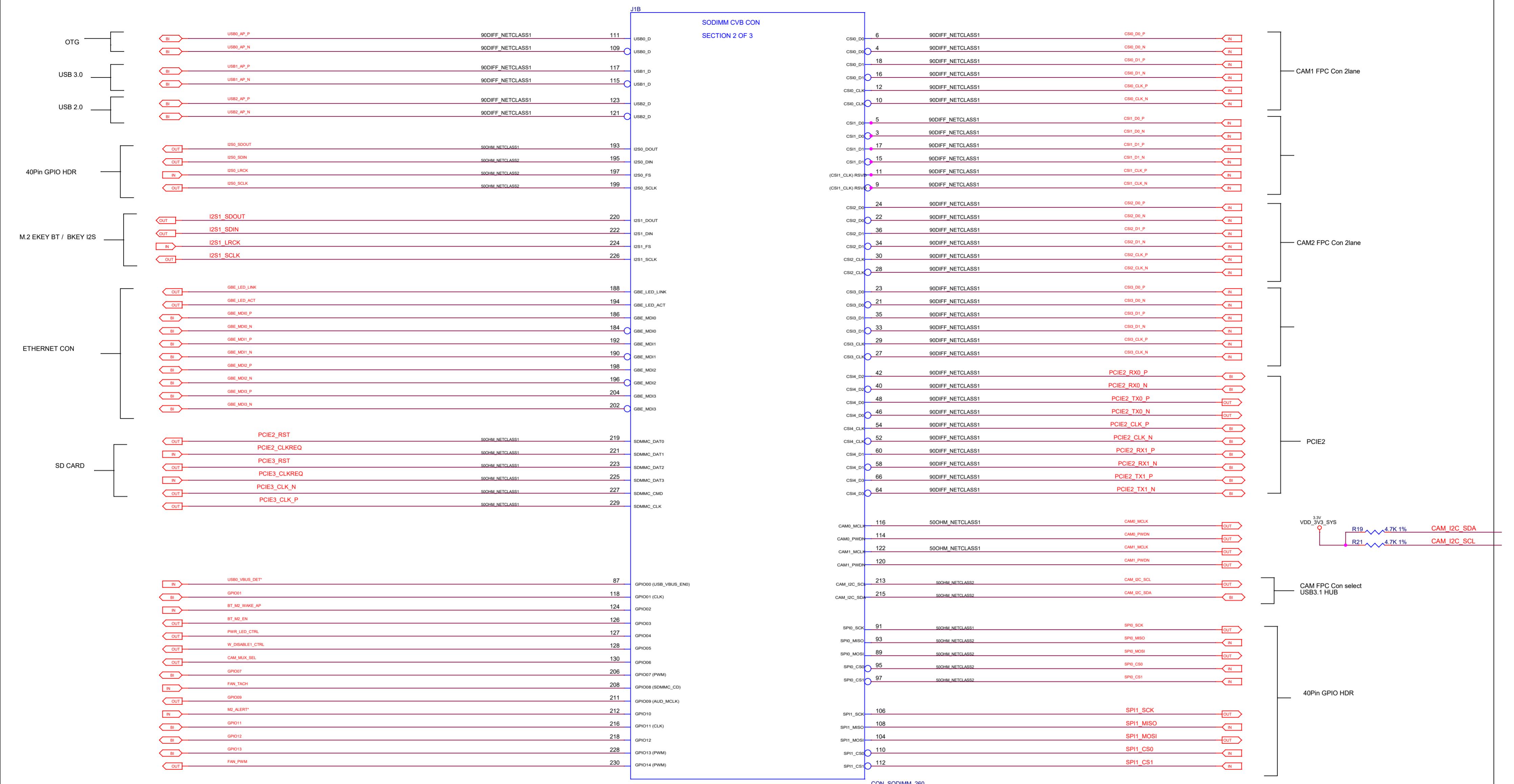
40PIN GPIO EXPANSION HEADER

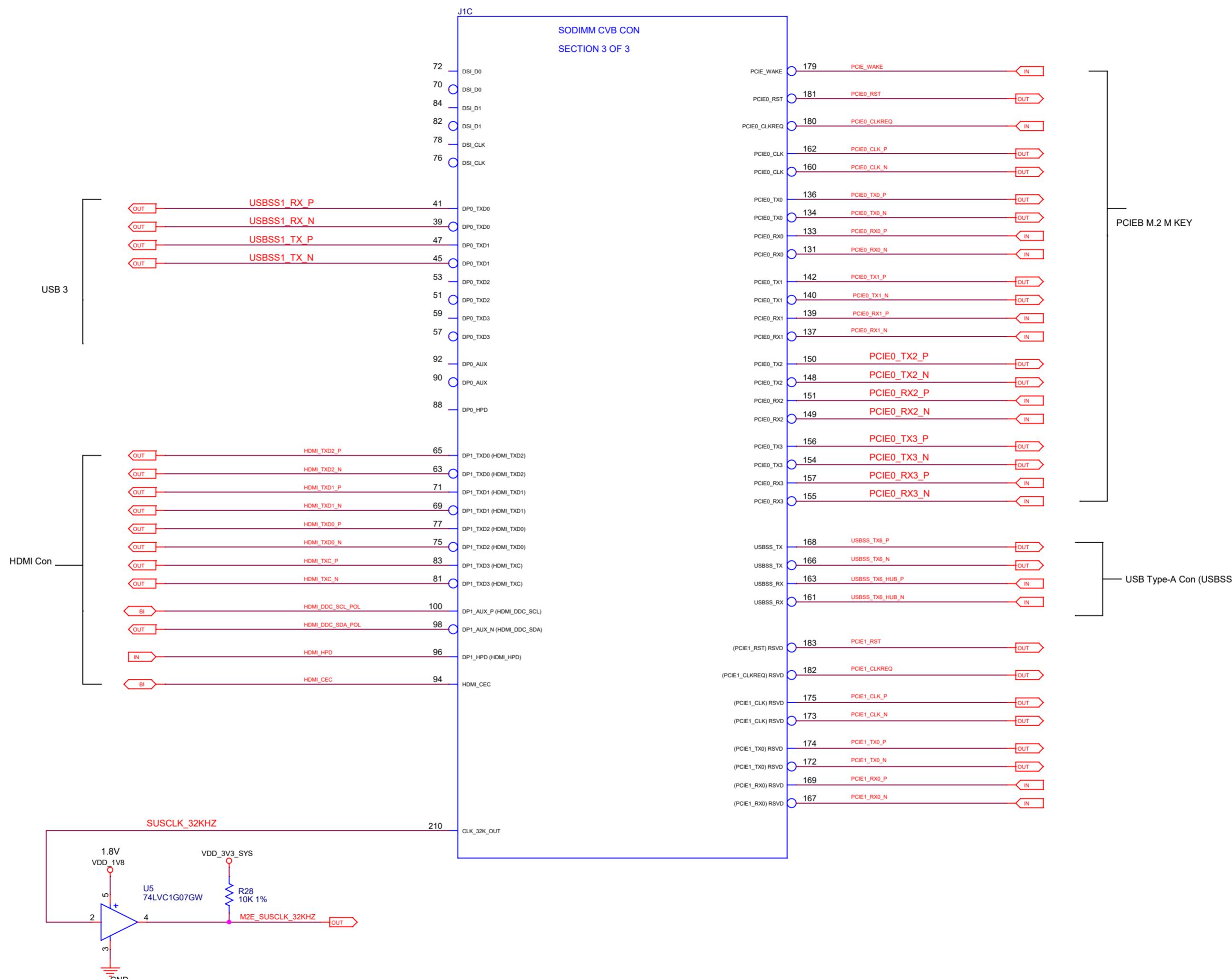
DEBUG

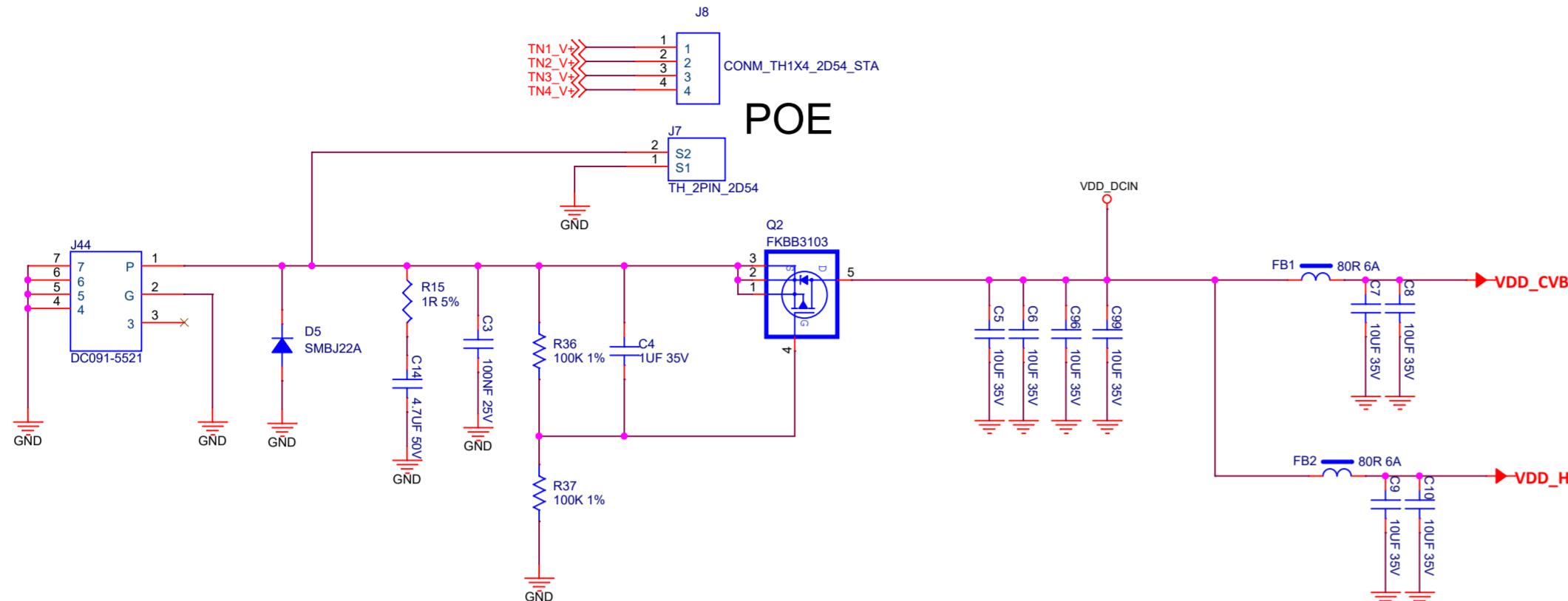
CONN_TH1X4_2D54_STA



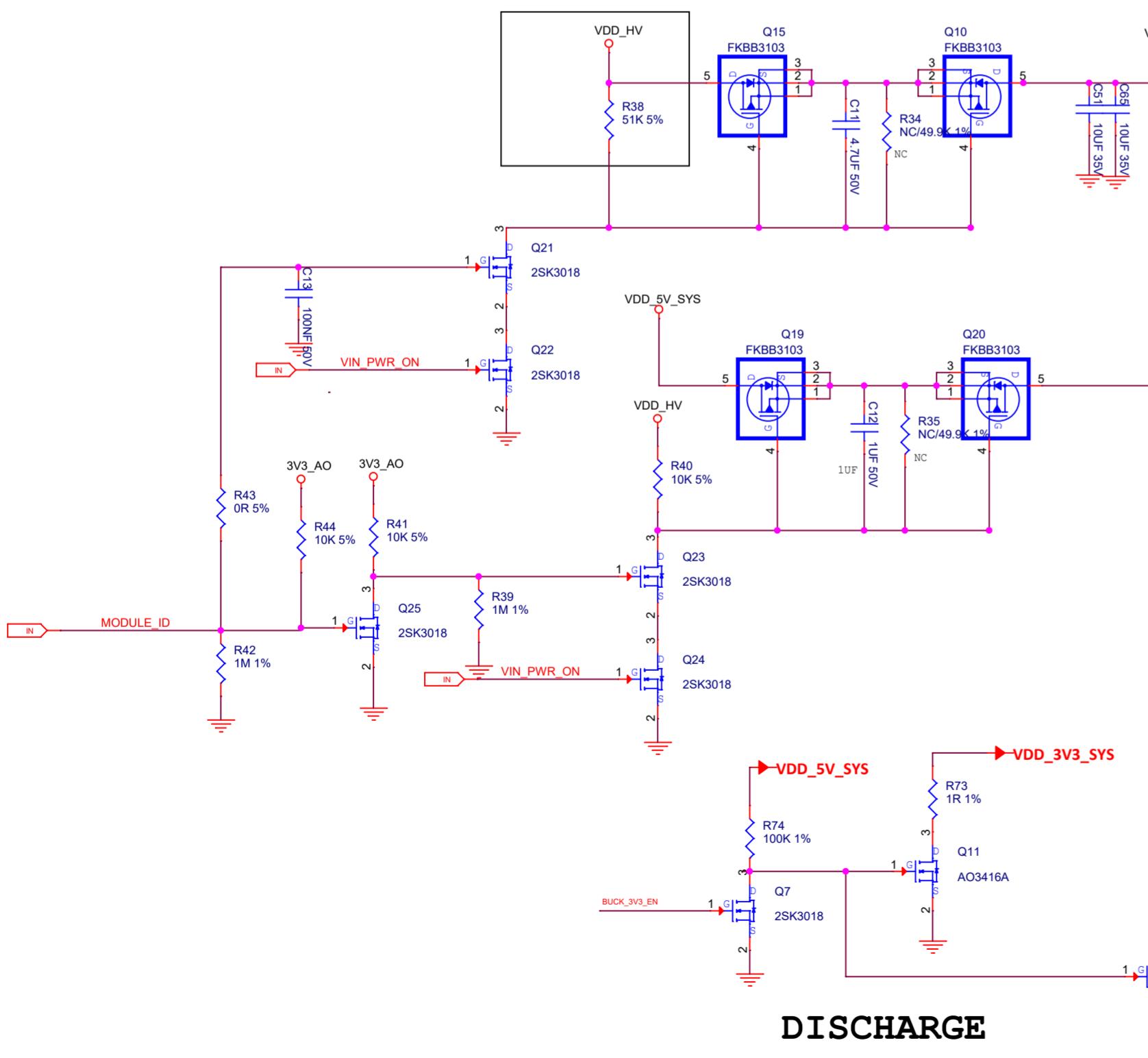
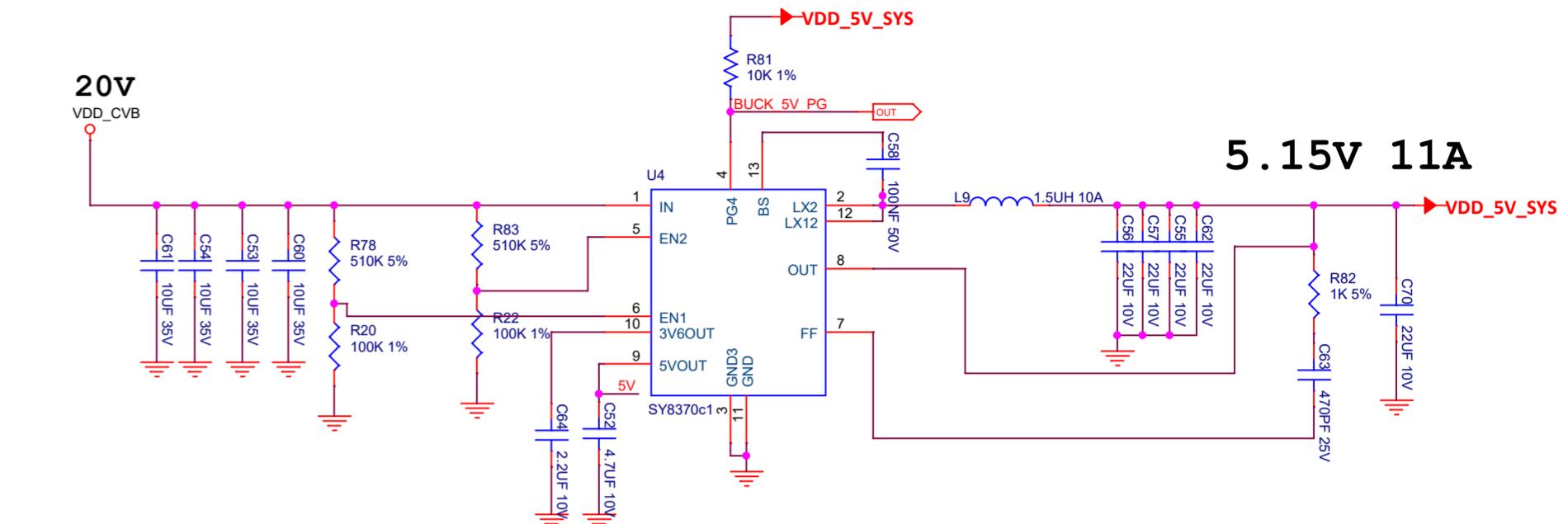
GPIOs, USB2.0, I2S, SPI, GBe and CSI



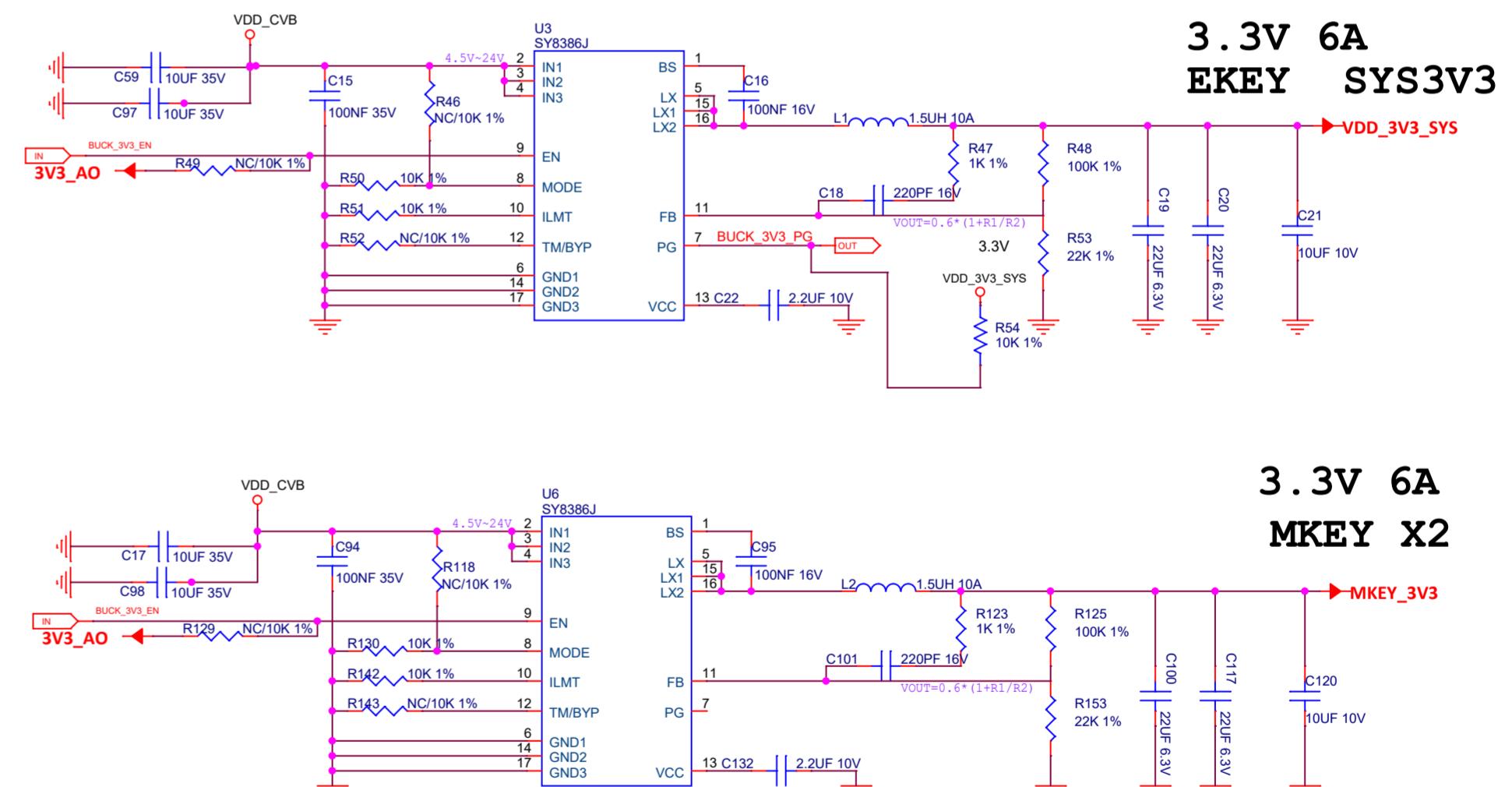


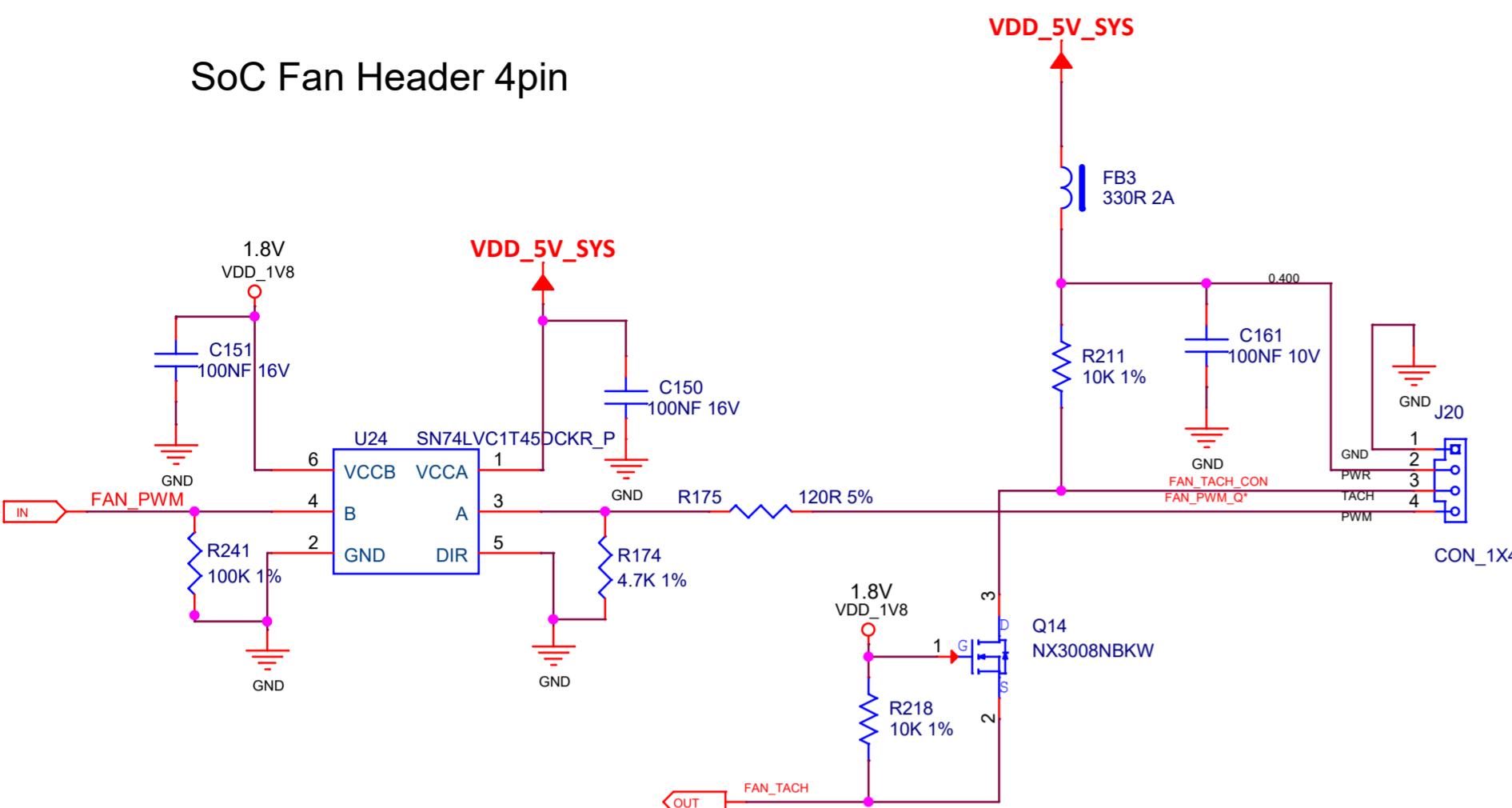
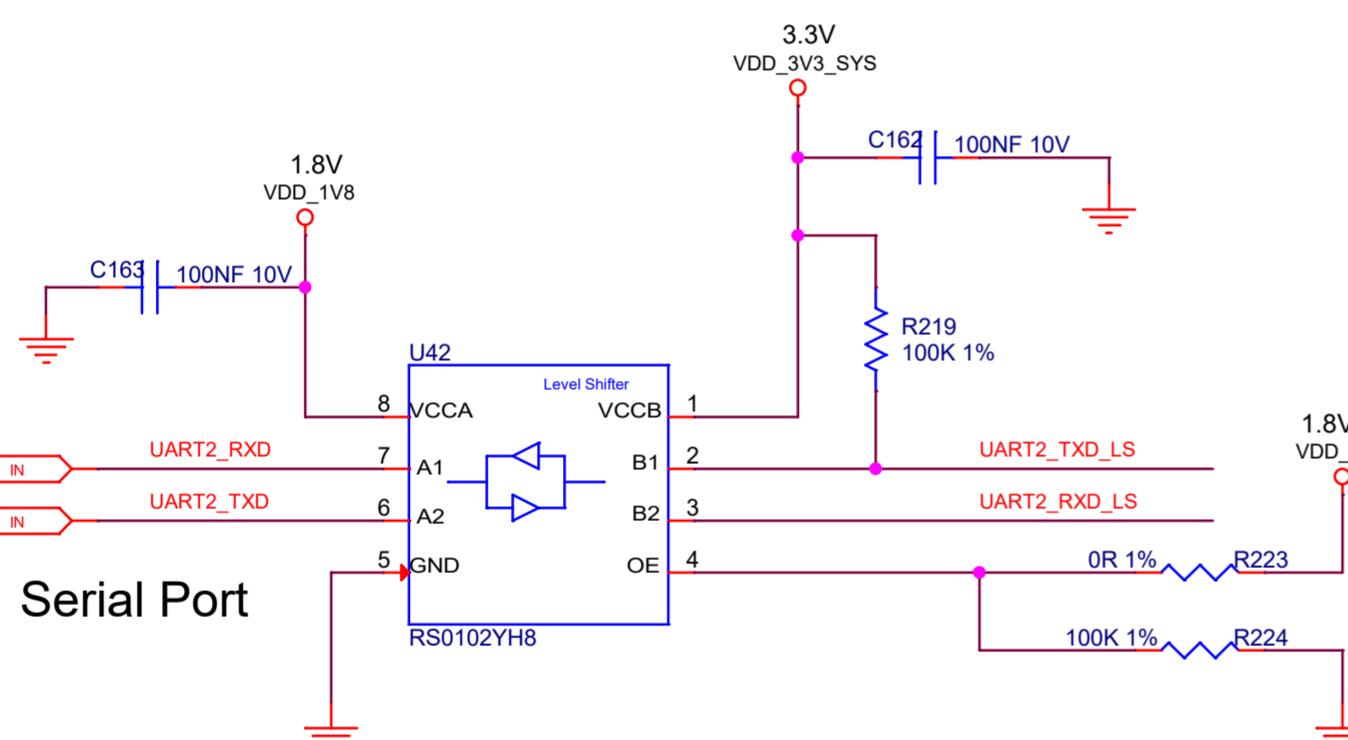
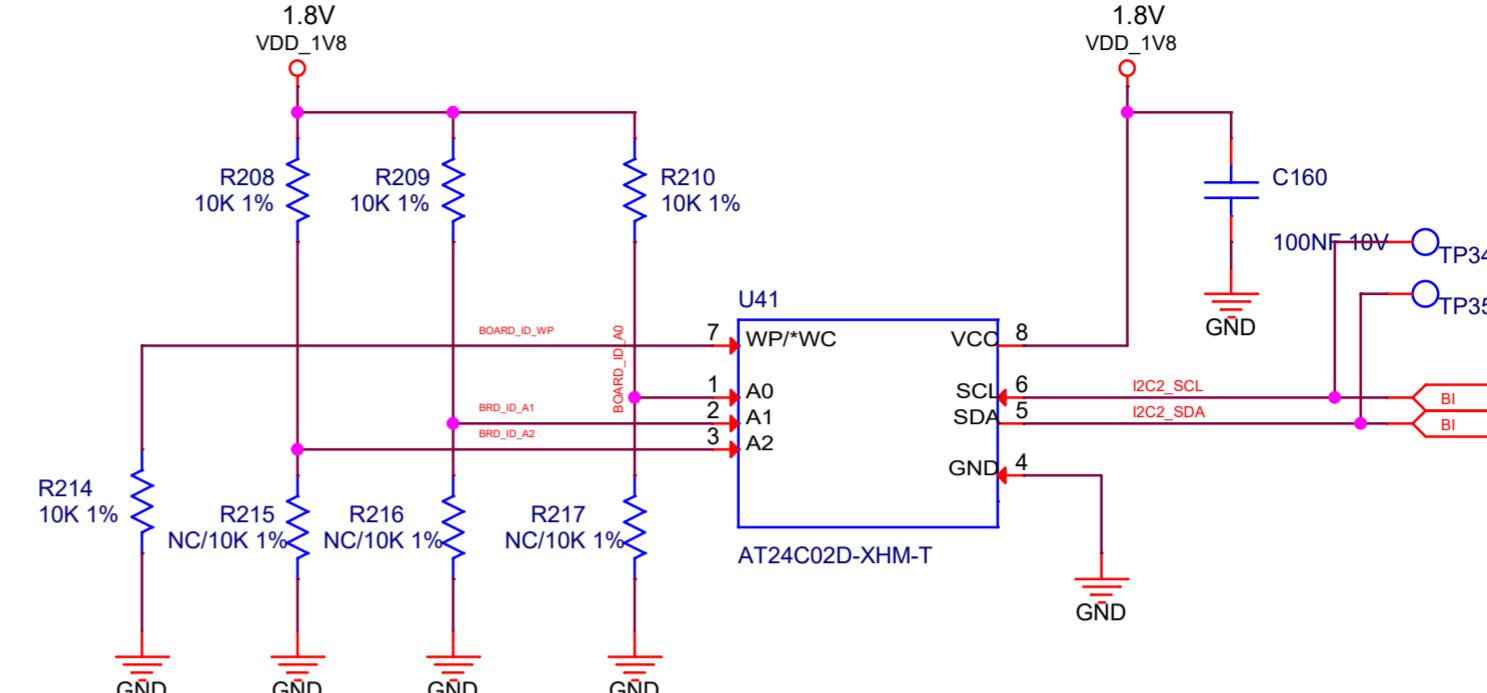
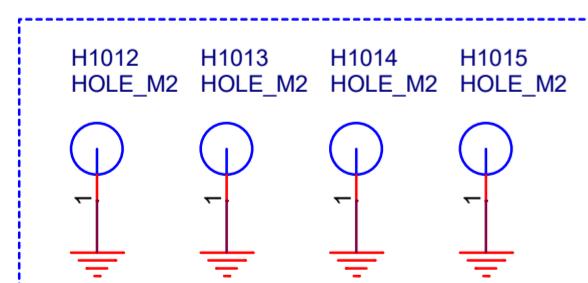
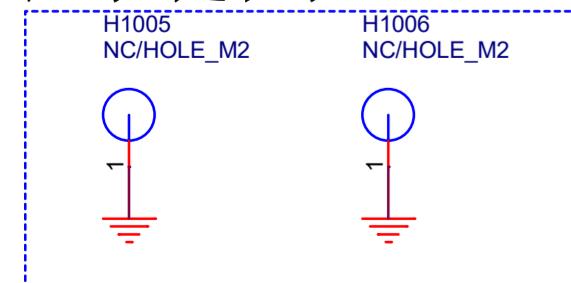
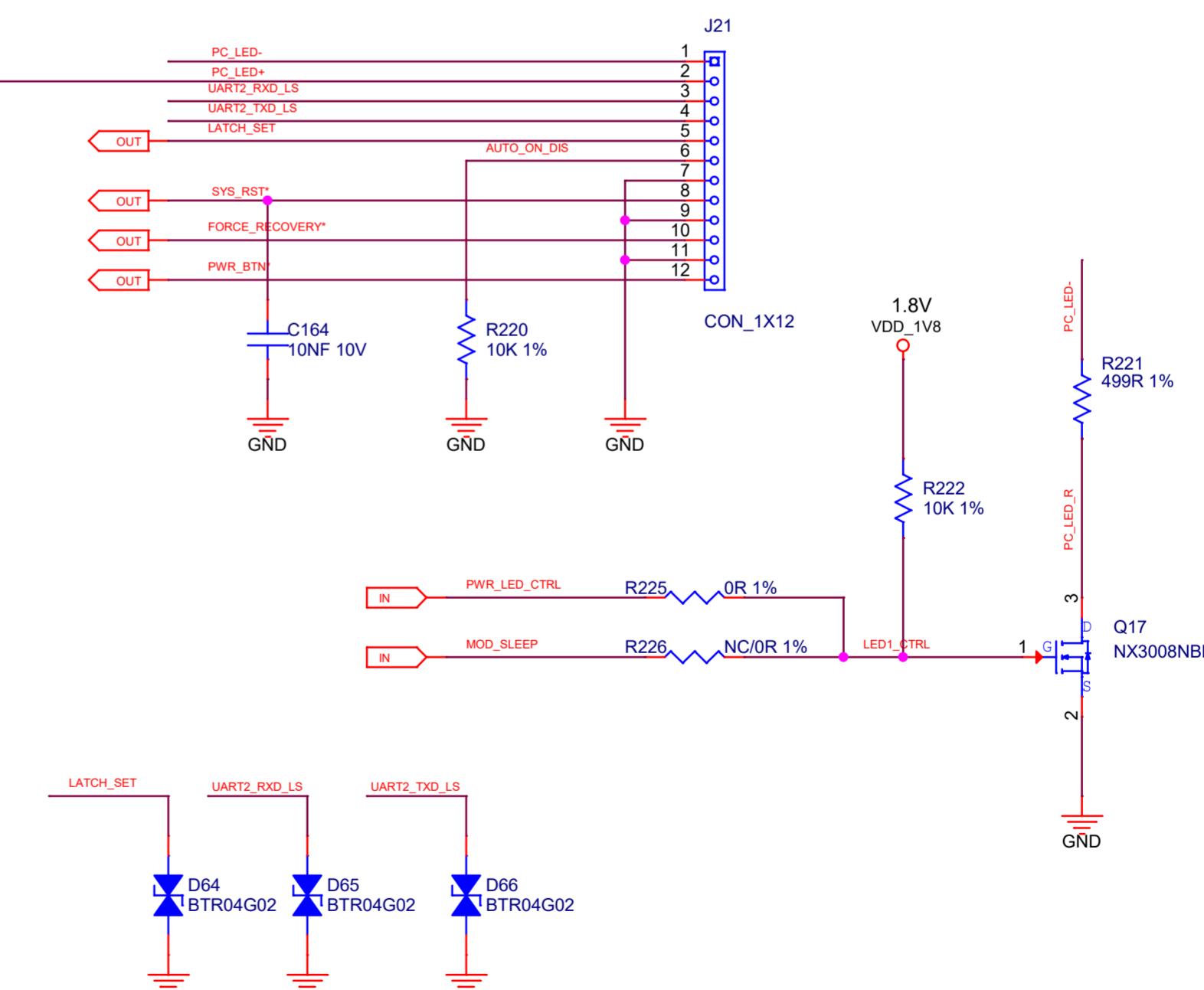


PRE-REG 5V DCDC

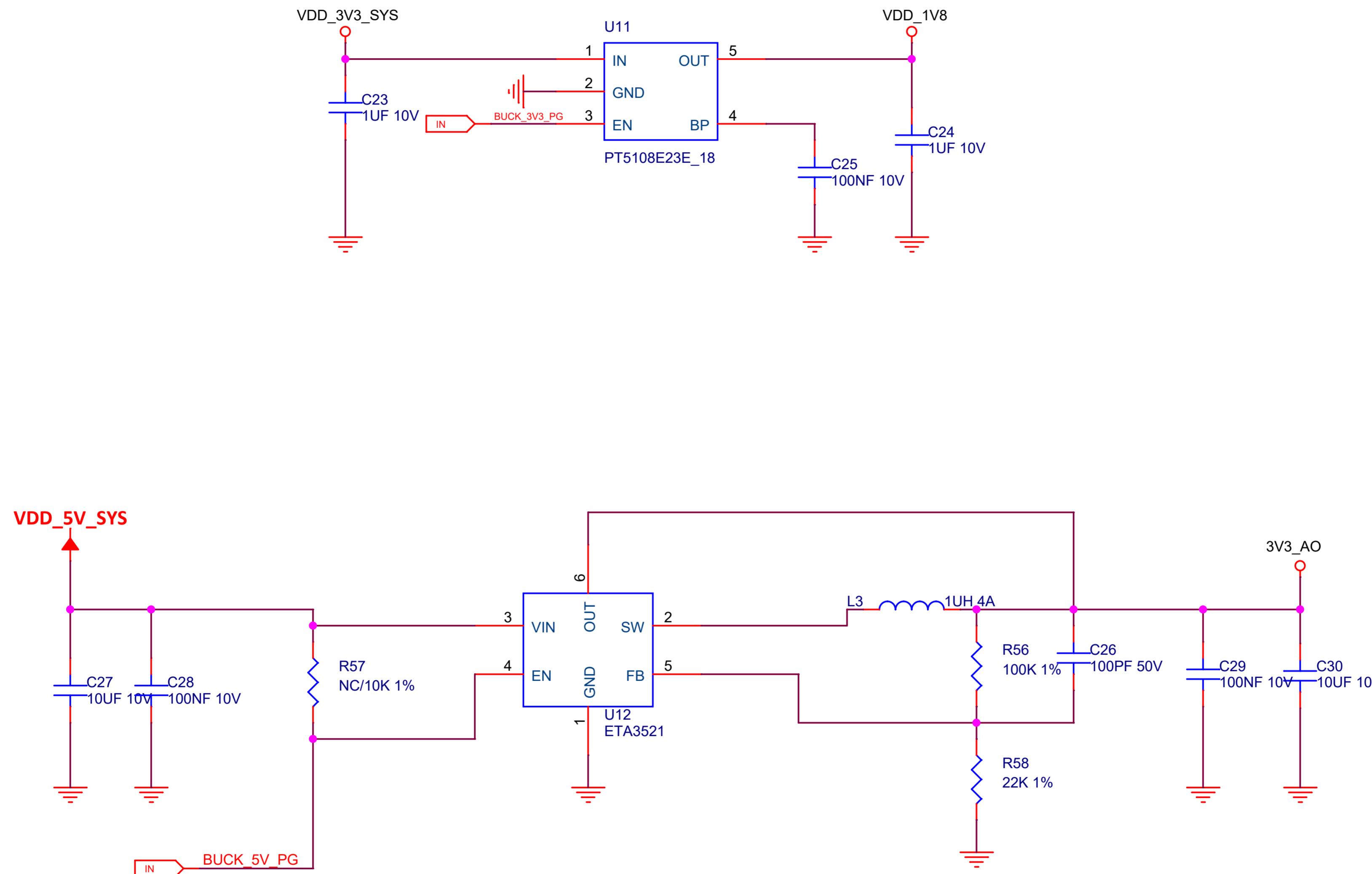


DISCHARGE



SoC Fan Header 4pin**Carrier board config****PCB 定位孔 HOLE****座子 定位孔 HOLE****Optional Button Header Pinout**

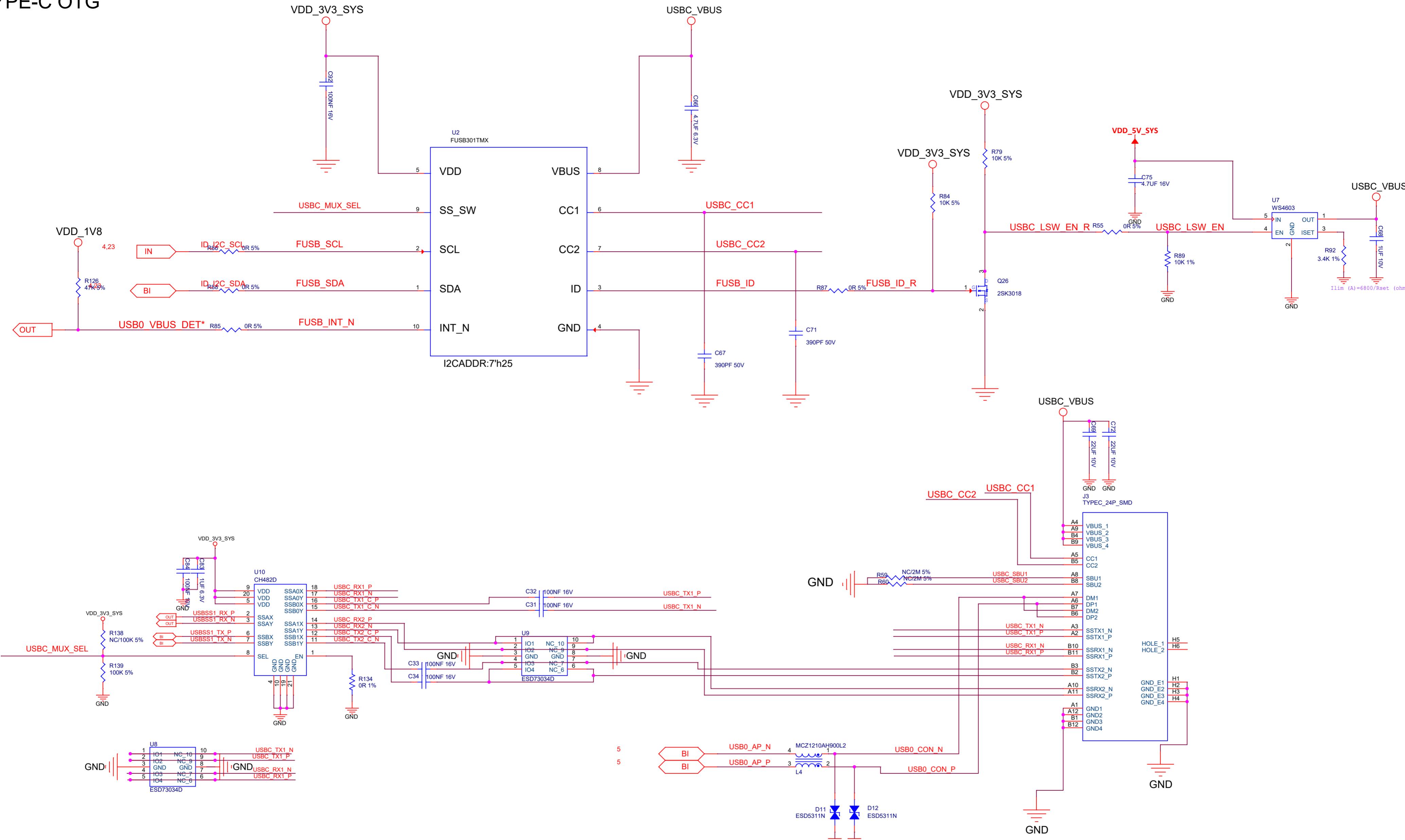
1V8 LDO AND 3V3 AO RAILS



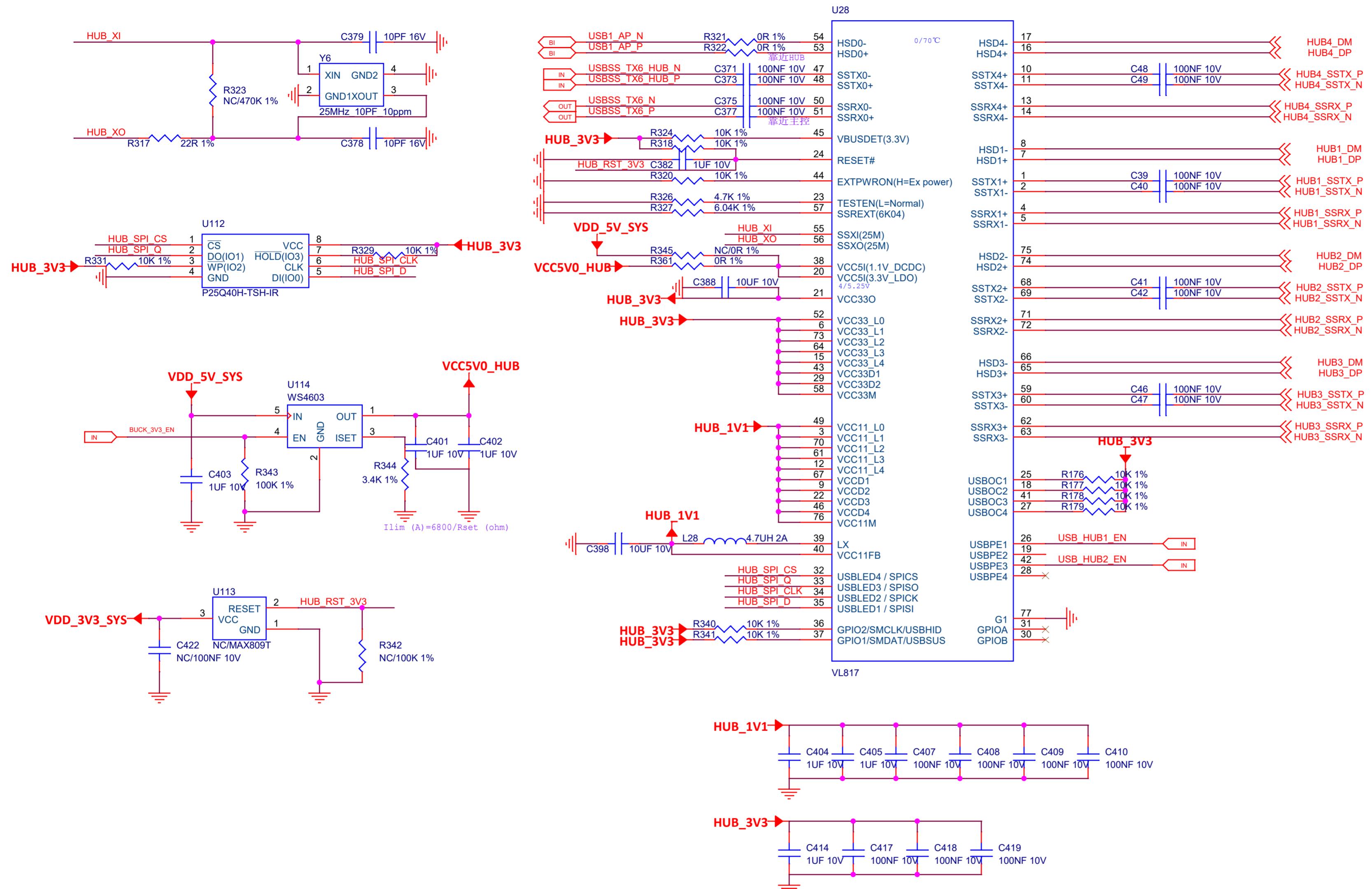
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| A4 | Page Name: | 09_1V8 LDO & 3V3 AO Rails | V1.1 |
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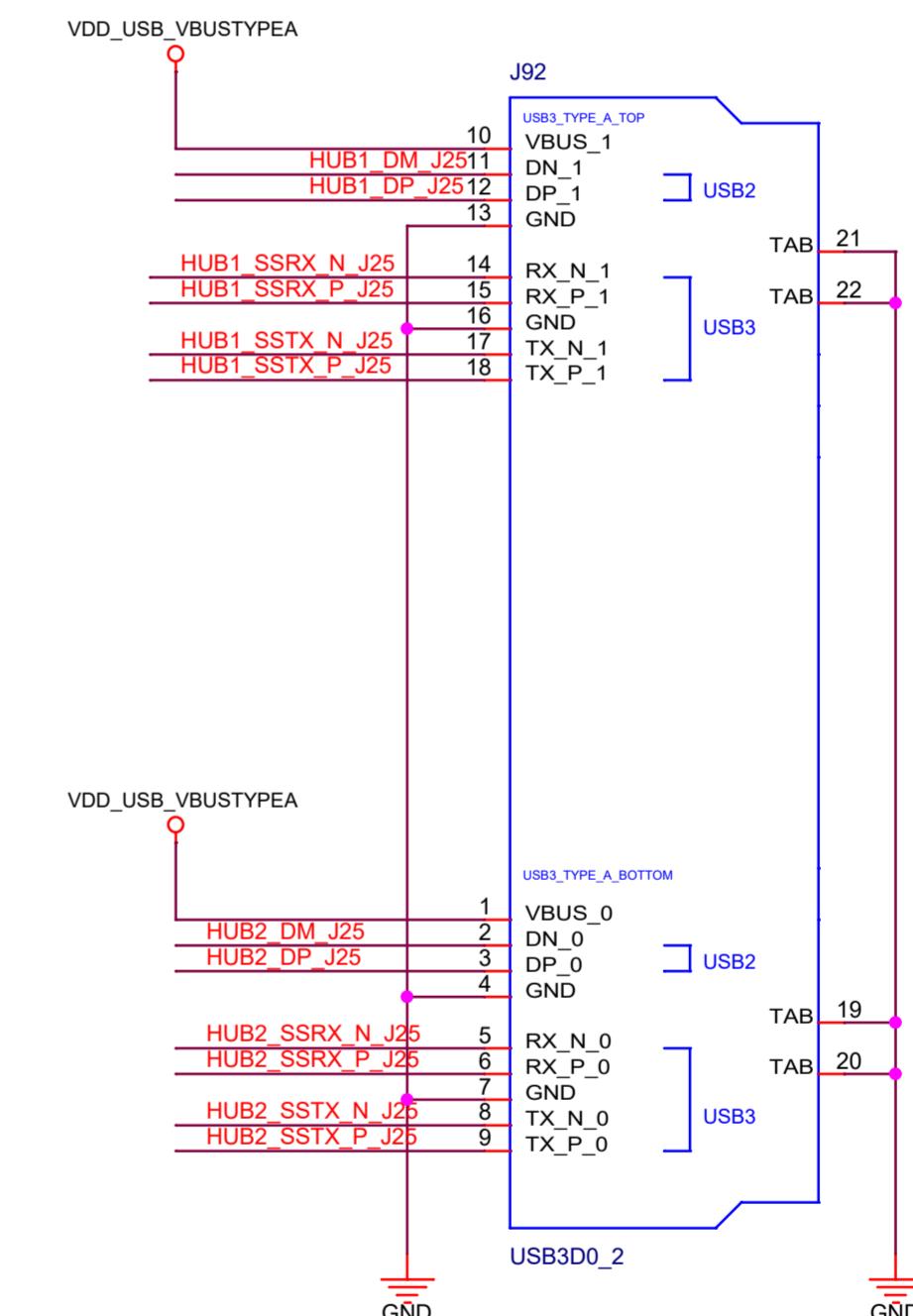
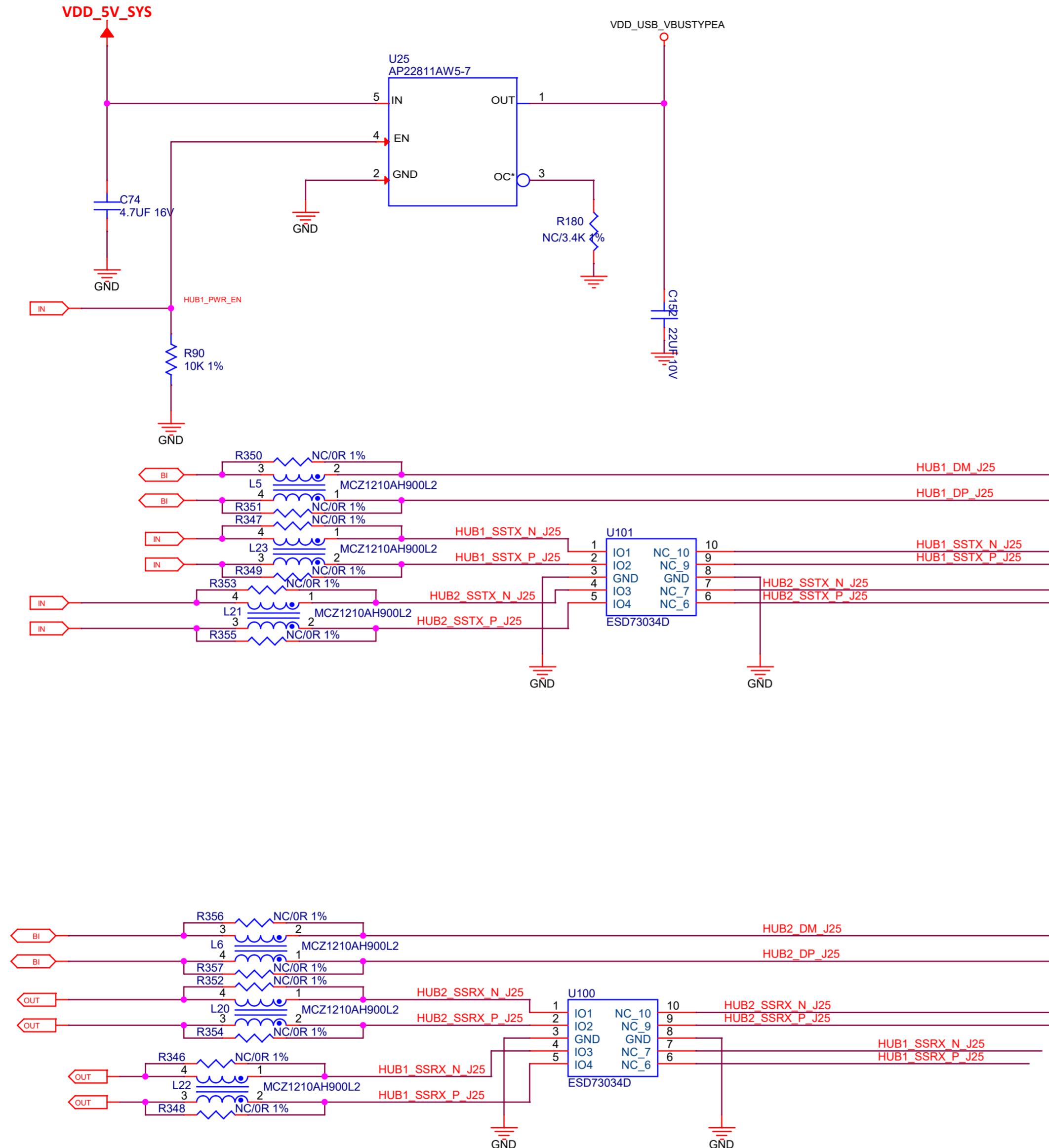
TYPE-C OTG



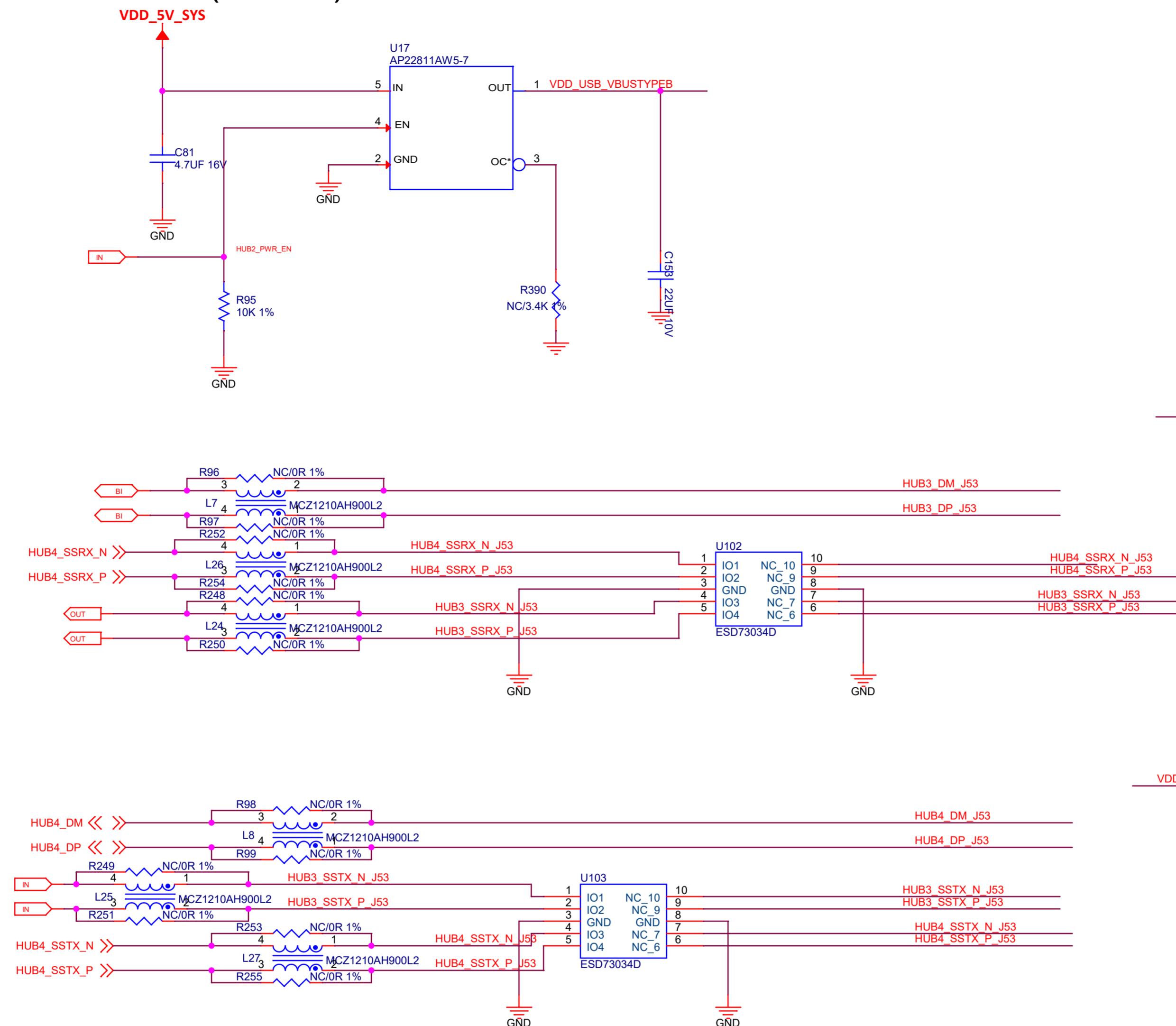
USB3.1 HUB x4 PORTS



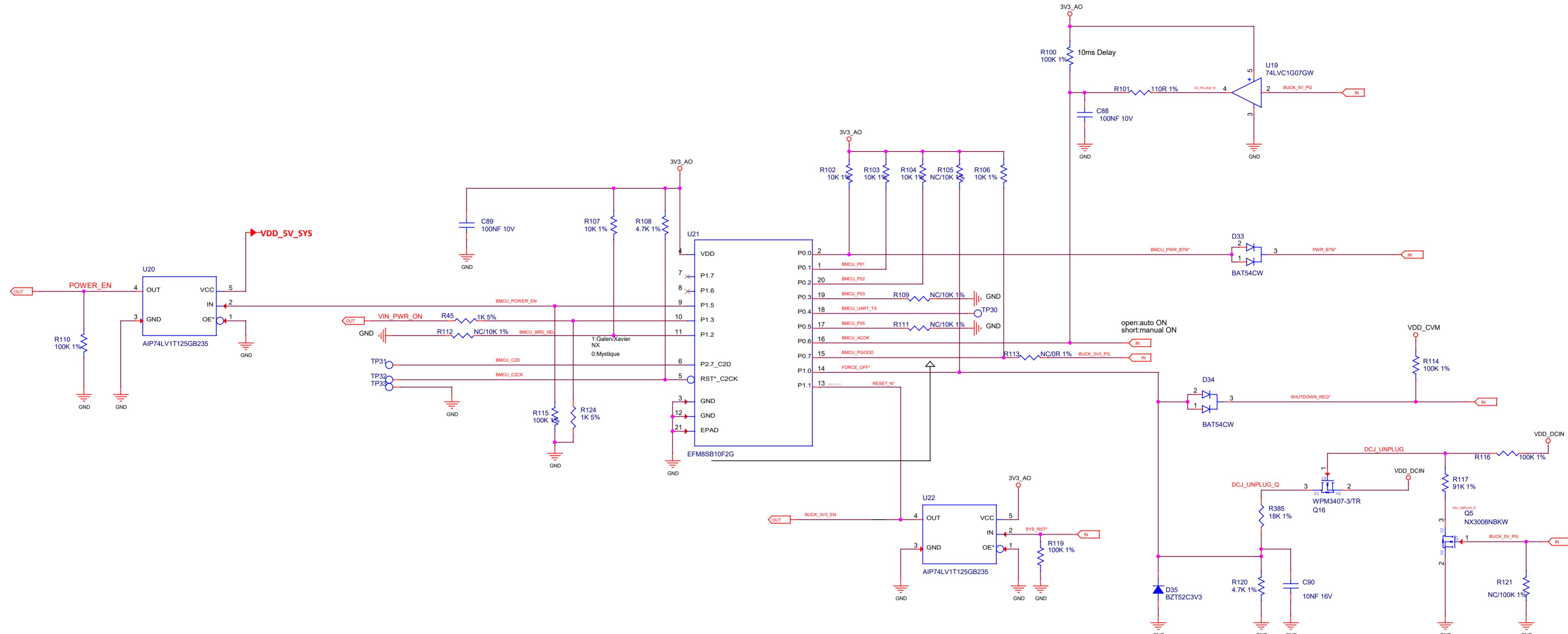
USB 3.0 TYPE A x2 CONNECTOR (1 OF 2)

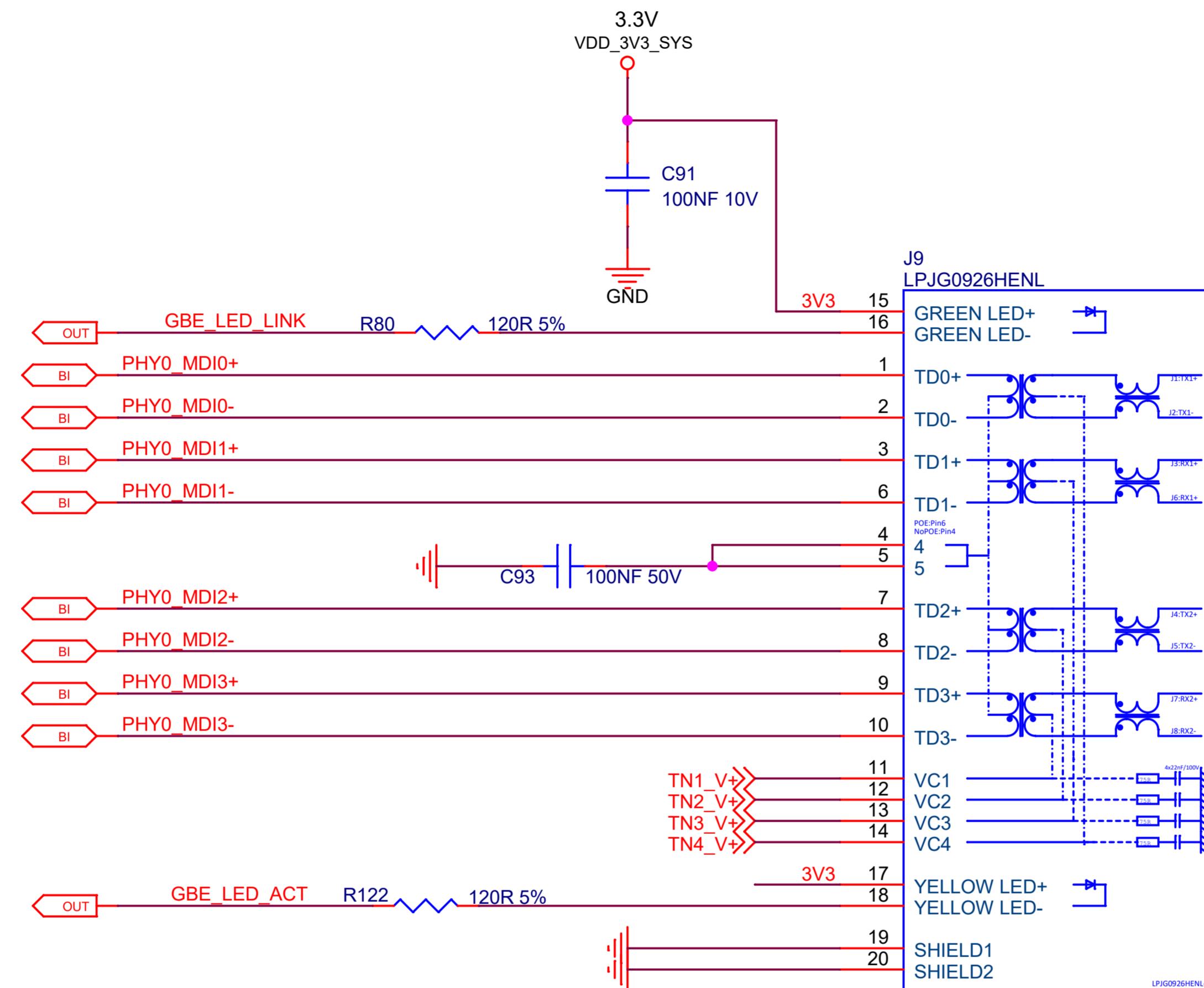


USB 3.0 TYPE A x2 CONNECTOR (2 OF 2)



BUTTON MCU FOR POWER UP

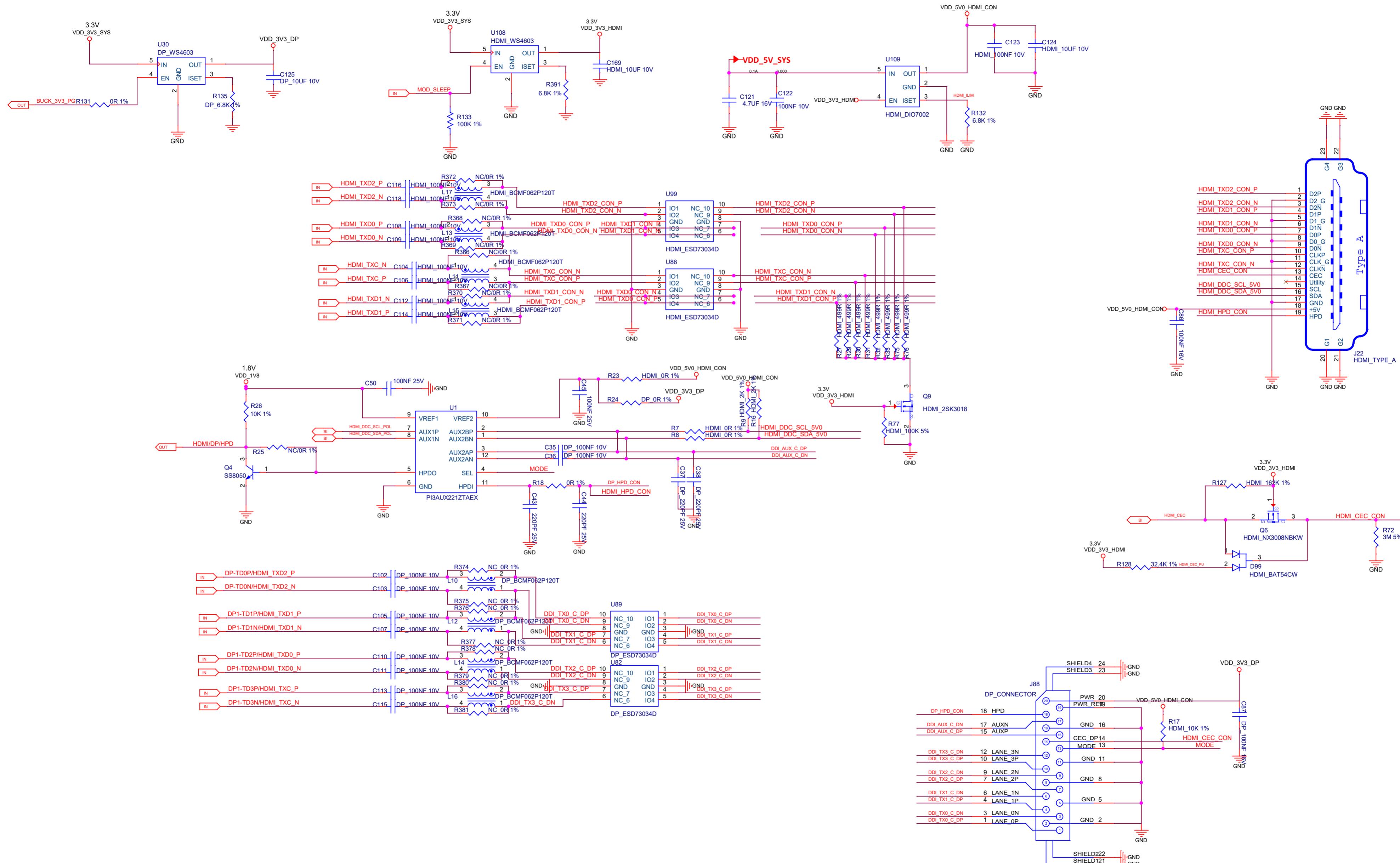




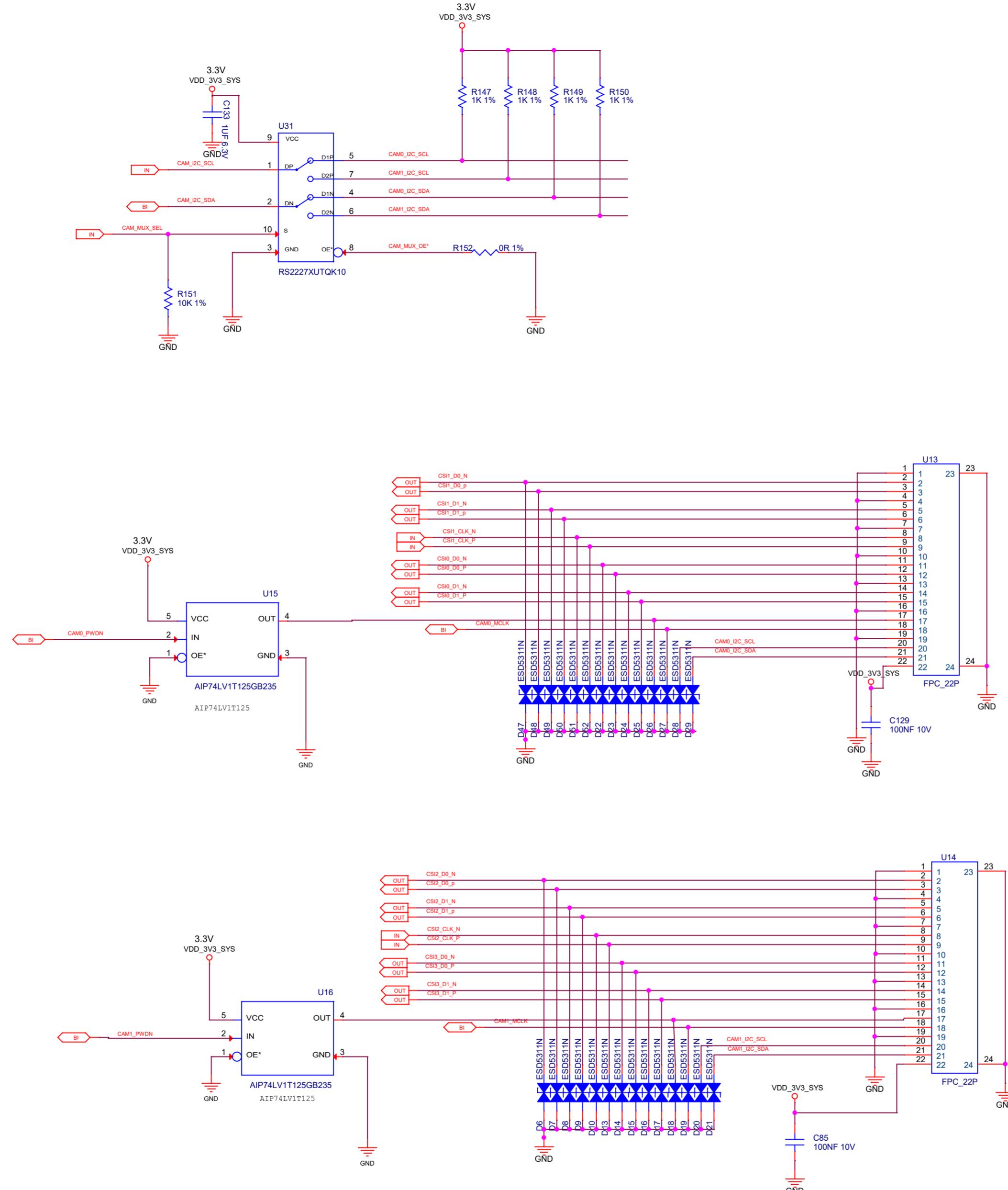
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| Size | Title: | C200 | REV |
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| A4 | Page Name: | 15_Gigabit_Ethernet | V1.11 |
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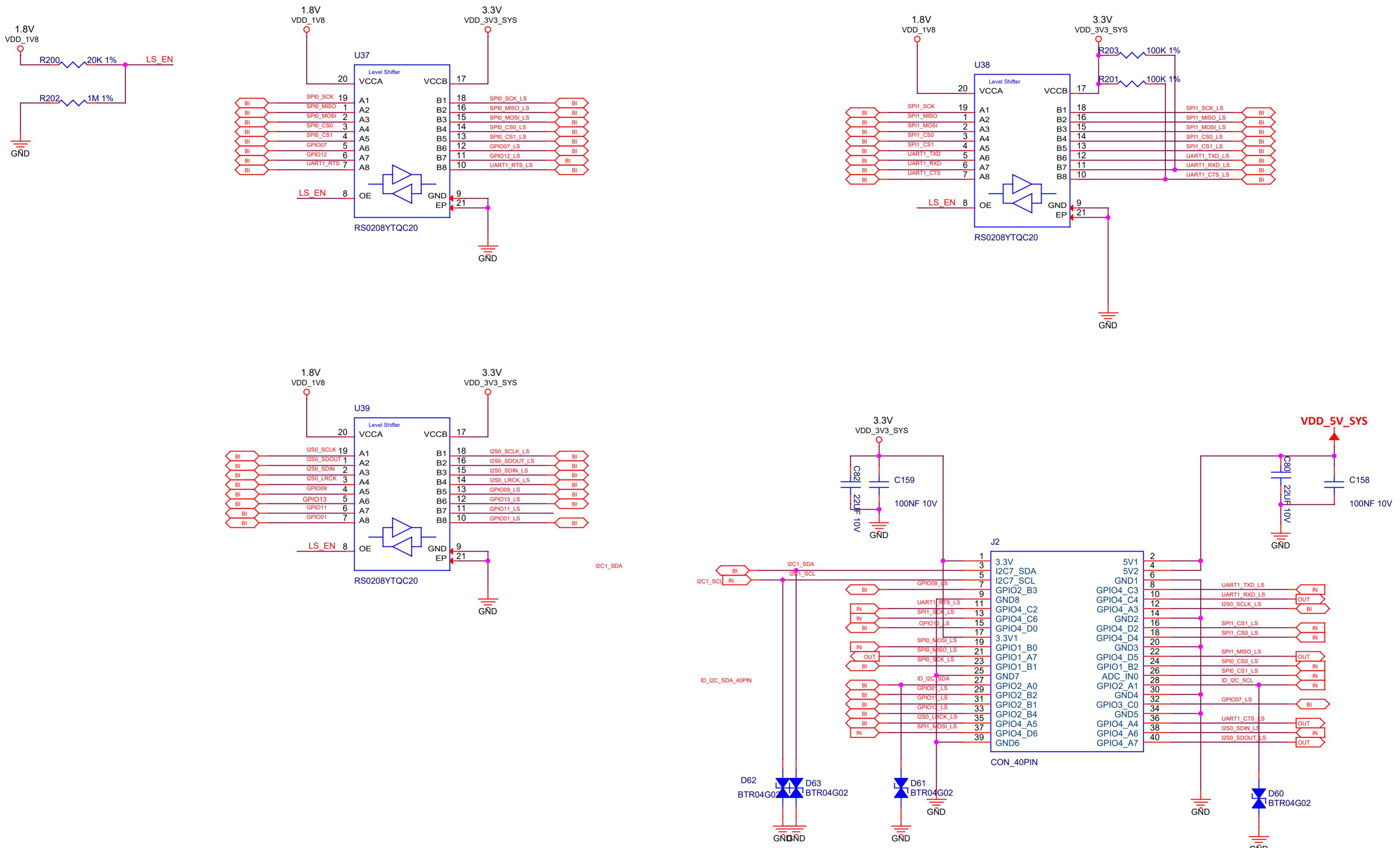
HDMI&DP



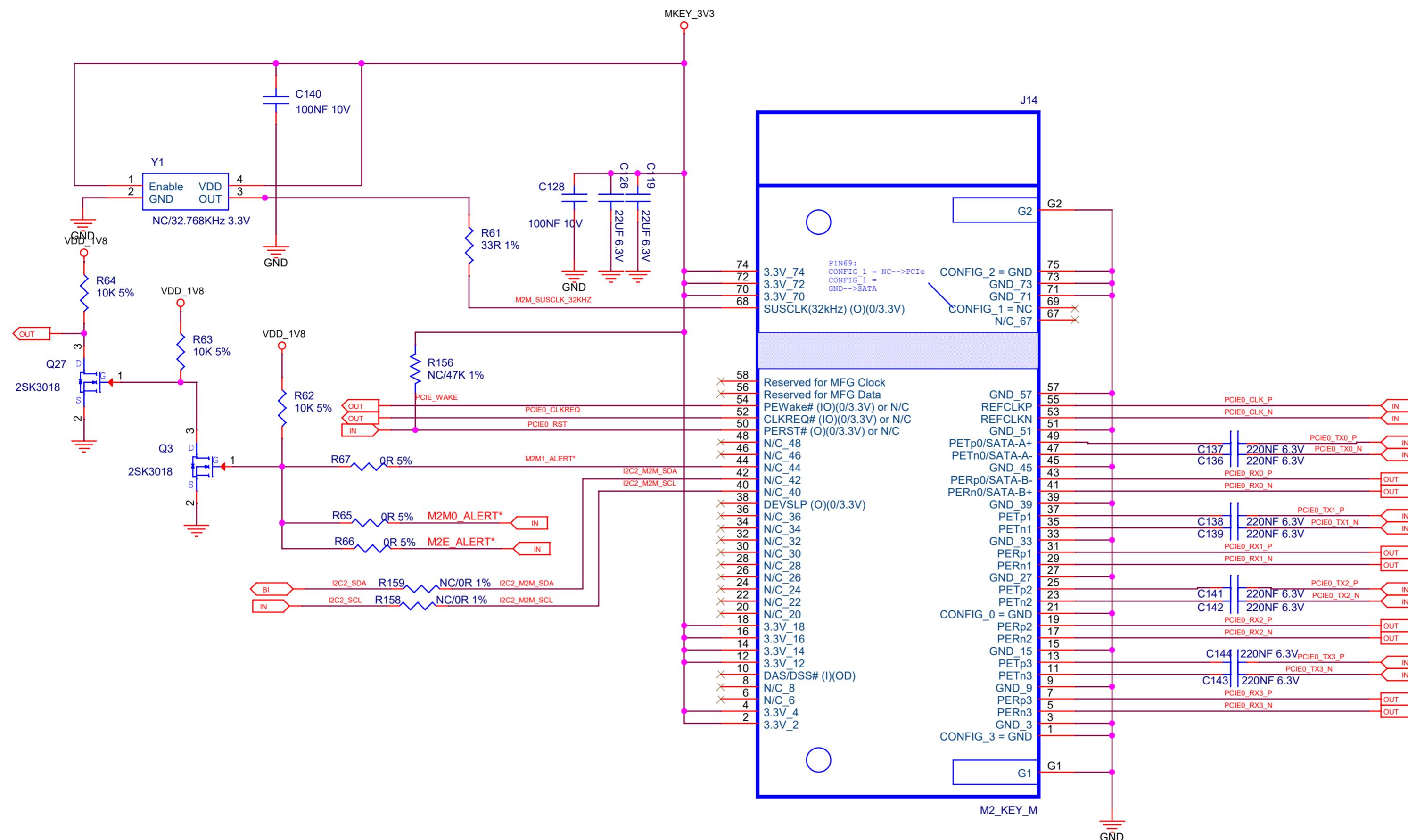
CSI CAM CONNECTOR



40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS



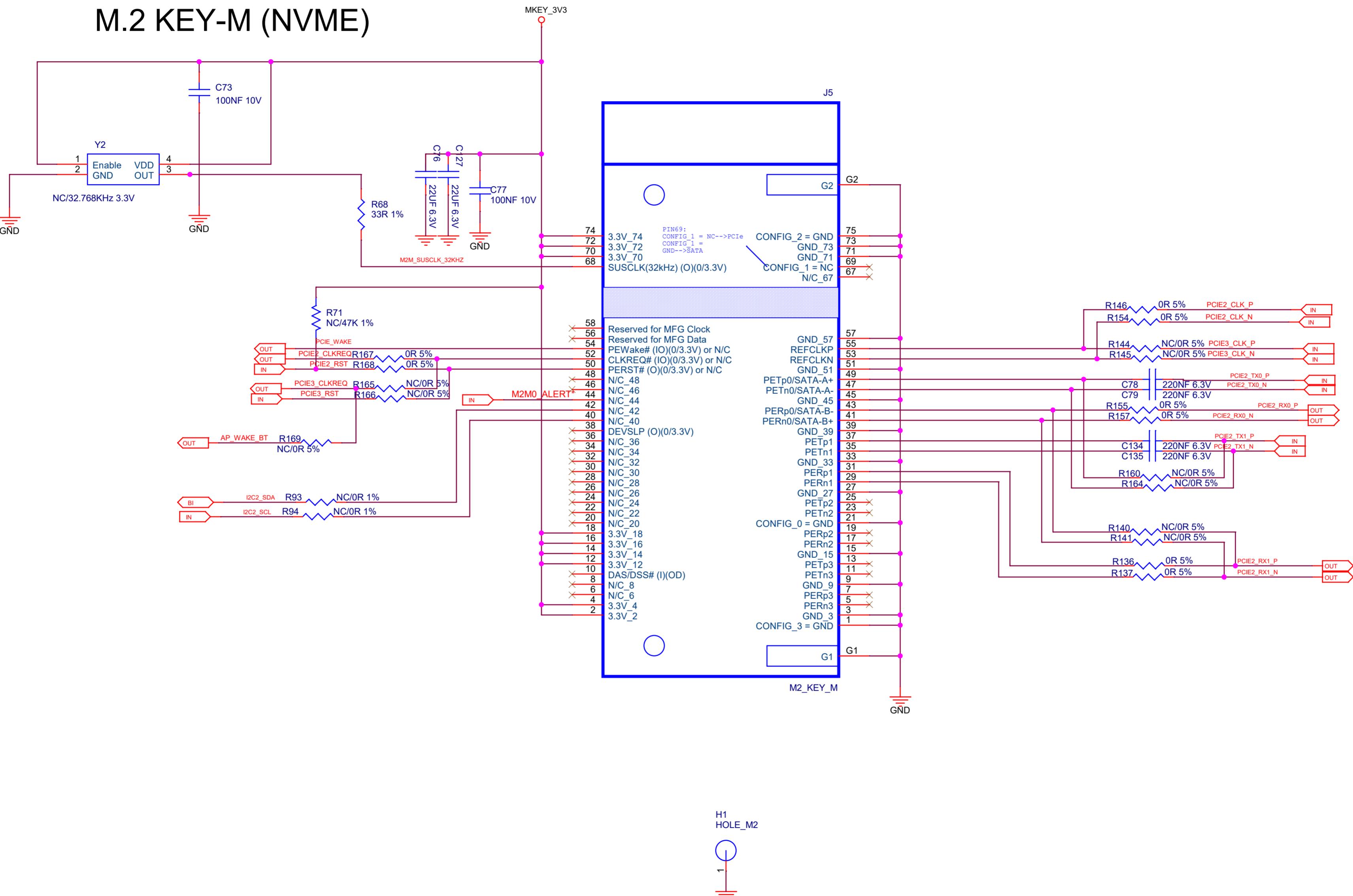
M.2 KEY-M (NVME)



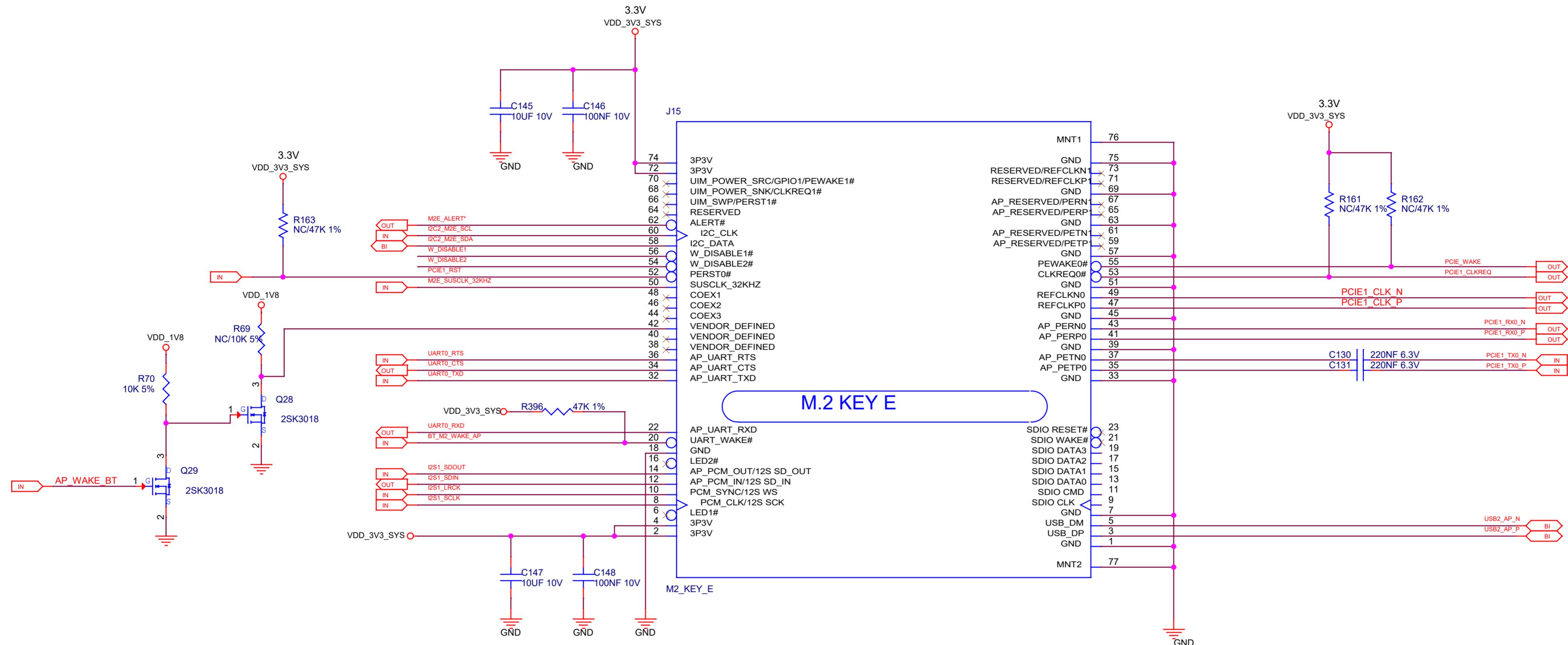
M2 J14_HOLE


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| Custom | Page Name: | 19_KEY_M_X4 | |
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M.2 KEY-E (WIFI/BT)



M2 J15_HOLE

