Two-dimensional ferroelectric tunnel junction: the case of SnSe

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Ferroelectric tunnel junctions (FTJs) have triggered considerable attention in the advanced applications of non-volatile

information devices. However, critical thickness for ferroelectricity limits FTJs towards their applications at the nanoscale.

Recently, some two-dimensional (2D) materials have been reported to show intrinsic in-plane ferroelectricity. Here, through the

opposite dopants on the two sides of the monolayer group-IV monochalcogenides (GIVMs), we design a novel 2D-FTJ employing

p-type semiconductor/ferroelectric/n-type semiconductor homostructure, coupling the electron tunneling with the robust in-

plane ferroelectricity. This structure makes it possible to dynamically modulate tunneling barrier height as well as the width

through ferroelectric switching, and promises to realize a significantly enhanced tunneling electroresistance (TER) effect.

Combining first-principles calculations with non-equilibrium Green's function formalism, herein, we reveal this mechanism by

using In:SnSe/SnSe/Sb:SnSe homostructure as a prototypical example, where a large TER effect of around 1460 % is obtained.

Compared with the normal vertical FTJs, this lateral 2D-FTJ enormously improves the TER ratio and shows exciting prospects

for device applications with higher data integration and reliable information retention in the future.

Ferroelectrics exhibiting spontaneous electric polarization (P<sub>s</sub>) play a crucial role in the modern information era. The

opposite polarization states  $+P_s$  and  $-P_s$  in ferroelectric materials can be switched upon application of an electric field. This

binary operation under uniform polarization reversal makes ferroelectrics a promising material for use in non-volatile

memory applications, and opens up a wide range of practical applications in data storage devices<sup>1-5</sup>. Among the prototypical

model devices, ferroelectric tunnel junctions (FTJs), which are typically composed of a thin ferroelectric layer sandwiched

by two metallic electrodes, have attracted significantly increasing research interest<sup>6-11</sup> owing to the availability of giant

tunneling electroresistance (TER) effect<sup>12,13</sup>. The mechanism can be affected by changing different electrodes or adopting

asymmetric electrode/ferroelectric interfaces. The electron tunneling mechanism requires that the ferroelectricity of insulating barrier should be sustained at nanometer scale, which is decisive for the occurrence of direct tunneling<sup>14</sup>. Over the past few decades, fortunately, the emergence of high-quality ultrathin ferroelectric films<sup>15-19</sup> makes it possible to achieve the FTJ devices.

However, the preparation of stable ferroelectricity in a few unit cells is still challenging. For example, as the pioneer of ferroelectrics, perovskite oxides thin films<sup>20-24</sup> have been widely adopted as the ferroelectric tunneling barrier, whereas sophisticated experimental approaches such as strain engineering<sup>25</sup>, interface engineering<sup>26</sup>, and careful control of epitaxial growth<sup>27</sup> are generally required to maintain ferroelectricity in nanometre-thick oxide films. On account of the critical thickness in traditional ferroelectrics<sup>28-30</sup>, another approach is to find inherent atomic-thick materials with robust nanoscale ferroelectricity. Recently, a great deal of new members of ferroic family have been substantially demonstrated at layered van der Waals materials<sup>31-37</sup>. In particular, the discovery of intrinsic ferroelectricity in two-dimensional (2D) materials is attractive for breaking through the restriction of the critical thickness in traditional ferroelectrics, and hence they are showing great potential for the applications of next-generation nanoelectronic devices.

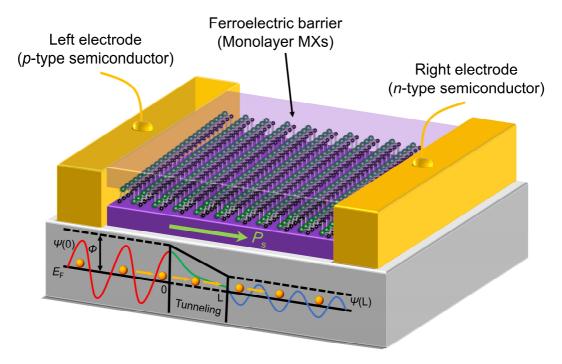


Figure 1 | Schematic diagram of a two-dimensional (2D) ferroelectric tunnel junction (FTJ) device. A pristine monolayer MXs semiconductor with in-plane ferroelectricity serves as a tunneling barrier. The left and right electrodes are obtained by p-type and n-type doping of the same MXs, resulting in a homostructural tunneling device. The inset illustrates the wave nature of the quantum tunneling. As the ferroelectric polarization ( $P_s$ ) is rightward polarized, the left energy barrier ( $\Phi$ ) is raised up beyond the energy of electrons. Through the quantum-mechanical phenomenon, in this case, the electrons tunnel across the barrier material in the nature of evanescent state since its amplitude decays exponentially through the barrier.

In this Letter, by combining robust in-plane ferroelectricity with electron tunneling, we propose a distinctive FTJ consisted of homostructural 2D ferroelectric materials, as illustrated in Fig. 1. Different from a conventional vertical tunneling junctions, this lateral 2D-FTJ in our study features a barrier region of group-IV monochalcogenides (MXs) monolayer in ferroelectric state, and two electrodes are formed by doped p-type and n-type MXs semiconductors, constructing a p-type semiconductor/ferroelectric/n-type semiconductor (p-SC/FE/n-SC) homostructure. Owing to the different screening lengths exhibited in the two electrodes, the potential energy profile at the semiconductor/ferroelectric interface is asymmetric. Therefore, the resistance switching in 2D p-SC/FE/n-SC FTJ is associated with the inequivalent tunneling barrier height on the reversal of polarization in the ferroelectric barrier. In addition, the electron tunneling transmission also relies exponentially on the barrier width. Very recently, an enhancement of TER effect depending on ferroelectric modulated barrier width by using a doped semiconductor as the electrode has been reported in experimental works<sup>38-40</sup>. Combining the phenomenological model and first-principles calculations, we demonstrate this 2D p-SC/FE/n-SC FTJ also provides the possibility to realize this barrier width altering mechanism. Owing to the polarization field effect<sup>41</sup>, the majority carriers can be accumulated or depleted near two doped semiconductor surfaces in response to the polarization reversal, leading to the tunability of effective tunneling barrier width. Furthermore, the interfacial ferroelectric insulator becomes conducting as a result of reversible metallization of the barrier according to the band alignment<sup>42</sup>. Taking the two distinct contributions into account, the effective barrier width can be dynamically manipulated via the electrical control of ferroelectric polarization reversal. Consequently, through ferroelectric modulation of barrier height together with the barrier width in our novel 2D-FTJ, the electronic resistance through the junction can be switched between two states allowing either a large (ON) or small (OFF) flow of the current from one side to the other, is therefore expected to achieve an enhanced TER effect, offering a significant potential in the next-generation memory applications.

### **Results**

**Influence factors of TER effect.** The schematic diagram of the 2D *p*-SC/FE/*n*-SC FTJ with two opposite ferroelectric states is depicted in Fig. 2a. Both electrodes can be realized by heavy impurity doping. Below a critical thin thickness, the behavior of electrons transporting through the barrier can be described by quantum mechanics, i.e., dominated by direct tunneling. According to the basic Wentzel-Kramers-Brillouin (WKB) approximation<sup>43,44</sup>, the TER ratio in terms of ferroelectric polarization reversal can be approximated as follows<sup>45</sup>

TER = 
$$\frac{J_R - J_L}{J_L} = \exp\left[\frac{\sqrt{2m}}{\hbar} \frac{\Delta \Phi}{\sqrt{\Phi}} d\right]$$
 (1)

where  $J_R$  ( $J_L$ ) is the current density in the high (low) conductance state, d is the width of ferroelectric barrier,  $\Phi$  is the average potential barrier height, and  $\Delta \Phi = \Phi_R - \Phi_L$  is the height change with reversal of polarization. It is noteworthy that the barrier width and height are the decisive factors for TER effect, indicating an effective mechanism to realize the TER enhancement as d and  $\Delta \Phi$  increase.

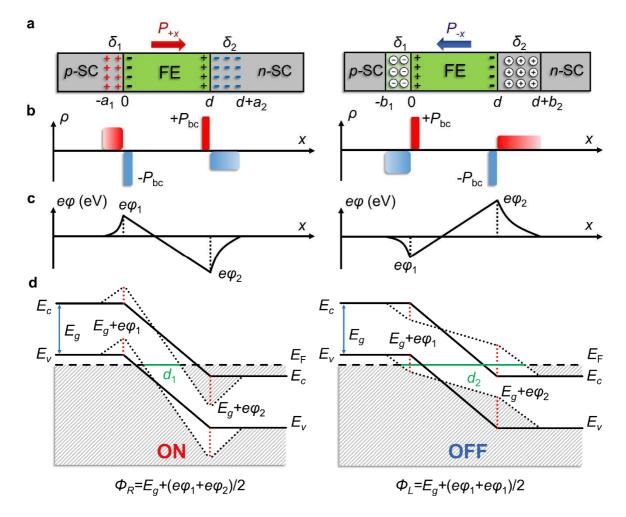


Figure 2 | Switching mechanisms of the 2D p-SC/FE/n-SC FTJ between "ON" (left panel) and "OFF" (right panel) states. (a) The respective schematics of 2D-FTJ as ferroelectric polarization is in  $P_{+x}$  and  $P_{-x}$  states. The black plus and minus symbols in the ferroelectric barrier region represent positive and negative ferroelectric bound charges, respectively. The red plus and blue minus in the semiconductor electrodes represent hole and electrons, respectively. The circled plus and minus symbols represent ionized donors and acceptors respectively. The distributions of (b) charge densities and (c) electrostatic potential energy profiles. (d) The overall potential energy profiles with corresponding band diagrams.

Asymmetric potential profile at interface. The 2D monolayer ferroelectric with barrier width d is sandwiched between p-type doped semiconductor (left electrode) and n-type doped semiconductor (right electrode) with corresponding screening widths of  $\delta_1$  and  $\delta_2$ , respectively. Here, we assume  $\delta_1 < \delta_2$  to illustrate the formation of asymmetric potential

energy. When the ferroelectric polarization is taken into account, the ferroelectric bound charge densities  $\pm P_{bc}$  are induced at the semiconductor/ferroelectric interfaces, as shown in Fig. 2b. In order to quantitatively understand the interplay between the resistive switching and tunneling barrier variation as the ferroelectric polarization reversed, we adopt a Thomas-Fermi screening model<sup>46-48</sup>, and the electrostatic potential within left and right electrodes can be expressed as:

$$\varphi_{i} = \pm \frac{P_{s} d \delta_{i}}{\varepsilon_{0} \varepsilon_{i} \left[ \varepsilon_{FE} \left( \frac{\delta_{1}}{\varepsilon_{1}} + \frac{\delta_{2}}{\varepsilon_{2}} \right) + d \right]}, \quad i = 1 \text{ or } 2$$
(2)

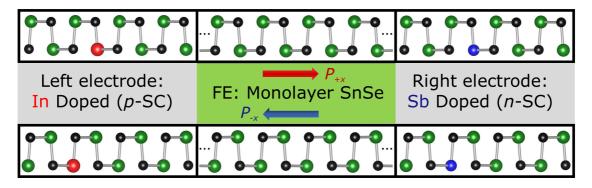
where  $\varepsilon_0$  is the permittivity of free space,  $\varepsilon_{\text{FE}}$  is the relative permittivity of the ferroelectric layer, and  $\varepsilon_i$  is the dielectric permittivity in electrode i (i=1 or 2). The sign of "±" depends on polarization orientation ( $P_s$ ) where positive and negative signs correspond to polarization pointing to and away from this electrode respectively. The Thomas-Fermi screening length in the left or right electrodes can be obtained by  $\delta_i = \frac{1}{e} \sqrt{\frac{\varepsilon_i}{\rho_i}}$ , where  $\rho_i$  is the density of states at the Fermi energy. Based on the equation (2), the electrostatic potential energy at semiconductor/ferroelectric interfaces can be calculated as  $|e\varphi_1| \equiv |e\varphi(0)| < |e\varphi_2| \equiv |e\varphi(l)|$  due to the assumption of  $\delta_1 < \delta_2$ , resulting in asymmetric potential energy profiles, as shown in Fig. 2c.

Mechanism of giant resistance switching. The overall barrier height  $\Phi$  experienced by the tunneling electrons needs to include the potential energy barrier of the ferroelectric insulator. For simplification, a potential barrier height with forbidden gap  $E_g$  seen by tunneling electrons is assumed for the barrier region in paraelectric state. When the ferroelectric polarization points to the right n-type doped electrode (labeled as  $P_{+x}$  state), the average potential barrier height can be obtained as  $\Phi_R = E_g + e(\varphi_1 + \varphi_2)/2$ , as seen in Fig. 2d. In addition to the barrier height, owing to the specific p-SC/FE/n-SC homostructure, the band bending in response to ferroelectric polarization will influence the effective width of the tunneling barrier. Compared with the width of pristine ferroelectric barrier, the regions near left and right semiconductor/ferroelectric interfaces become conducting, reducing the effective barrier width  $d_1 < d$ . Therefore, the device under  $P_{+x}$  state realizes a low resistance "ON" state.

When polarization is pointing to the left p-type doped electrode (labeled as  $P_{-x}$  state), the average potential barrier height  $\Phi_L = E_g + e(\varphi_1 + \varphi_2)/2$  can be calculated analogous to that in the case of  $P_{+x}$  state. With  $\delta_1 < \delta_2$ , the barrier height for right polarization  $\Phi_R$  is smaller than the  $\Phi_L$  for the polarization pointing to left, resulting in different height of tunneling barrier seen by transport electrons. More interestingly, the left and right semiconductor surfaces are depleted of holes and

electrons, respectively, as a result of ferroelectric polarization field effect. The majority carriers disappear near the semiconductor/ferroelectric interfaces, only leaving the immobile ionized acceptors and donors, as shown in the right panel of Fig. 2a. Under this condition, two separated insulating space charge regions will be created near the left and right electrode surfaces. Consequently, the tunneling electrons have to overcome an additional barrier. We further illustrate the mechanism by showing the band diagram in Fig. 2d. The valence band of p-type semiconductor bends downwards below the Fermi energy ( $E_F$ ), while the conduction band of p-type semiconductor bends beyond the p-type semiconductor barrier width, i.e., p-type semiconductor bends beyond the p-type semiconductor barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier width, i.e., p-type semiconductor barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier width, i.e., p-type semiconductor barrier and higher tunneling barrier is introduced in p-type semiconductor barrier.

Configuration of tunnel junction device. To realize the model of p-SC/FE/n-SC FTJ based on all-2D materials, we construct a homostructure implemented in the monolayer GIVMs<sup>49-51</sup> which display intrinsic in-plane ferroelectric polarization with high Curie temperature<sup>52-54</sup>. Taking monolayer SnSe as an example, the atomic structures of such 2D-FTJ at two polarization states have been illustrated in Fig. 3. The in-plane polarization  $P_s$  of SnSe reaches up to  $1.89 \times 10^{-10}$  C/m. More excitingly, its estimated Curie temperature  $T_c$  is around 460 K, implying the possibility of devices working at room temperature. Compared with the widely studied ferroelectrics such as BaTiO<sub>3</sub> and PbTiO<sub>3</sub> with out-of-plane ferroelectric polarization  $^{55}$ , the atomic-thick pristine SnSe displays in-plane ferroelectric polarization along x axis on the 2D plane, namely its  $P_{+x}$  and  $P_{-x}$  states. Note that the in-plane polarization along y axis is exactly equivalent to that along x axis owing to the symmetry of monolayer SnSe. The left electrode is hole doped by replacing a Sn cation with an In atom (In:SnSe), while the right electrode is electron doped with Sn substituted by Sb atom (Sb:SnSe). The doping concentration of each semiconductor electrode reaches up to 6.2×10<sup>20</sup> elections/cm<sup>-3</sup>, resulting in a heavily doped condition. Then, the homostructure is constructed by stacking unit cells along the [100] direction, where the pristine ferroelectric SnSe with 18 unit cells are in the middle of p-type and n-type semiconductor electrodes. In contrast to the conventional Metal-1/ferroelectric/Metal-2 FTJ12 or all-oxide FTJ13 in which lattice mismatch is naturally generated by the electrodes and ferroelectric due to their different lattice constants, our In:SnSe/SnSe/Sb:SnSe homostructure completely eliminates the lattice constant mismatch and will not introduce abrupt structure distortions at the ferroelectric/electrode interfaces. More importantly, the ferroelectric polarization of monolayer SnSe in this 2D-FTJ is spontaneous, which avoids sophisticated external methods such as chemically functionalized approach<sup>56</sup> to maintain ferroelectricity, making it more applicable for experimental investigations and use in further practical devices.



**Figure 3** | **Schematic diagram of the** *p***-type semiconductor**/**ferroelectric**/*n***-type semiconductor homostructure.** The green region is the ferroelectric SnSe monolayer. The red and blue arrows correspond to two opposite orientations of polarization along *x* axis. The grey regions represent the left and right electrodes composed of hole and electron doped SnSe semiconductors, respectively. Only a few unit cells are presented for illustrating the ferroelectric barrier region and electrode regions here because of the page width limitations.

Effects of inequivalent ferroelectricity. The stability and robustness of the in-plane ferroelectricity in 2D ferroelectric materials sandwiched between two doped electrodes lie at the heart of such 2D-FTJ device. To examine the stability of this in-plane ferroelectricity, we show the total energy evolution during the of ferroelectric switching in Fig. 4 by calculating the double-well potential profile for the In:SnSe/SnSe/Sb:SnSe homostructure. On account of the two types of electrodes, the energy potential profile shows an asymmetric feature associated with two inequivalent energy minima, which has also been observed in our previous study on tricolor-superlattice-based FTJ with asymmetric ferroelectric/electrode interfaces<sup>57</sup>. The normalized atomic displacement  $\lambda$  is used to describe the ferroelectric distortion amplitude. Different from a free-standing ferroelectric material with symmetric potential profile, the paraelectric state with the energy maximum is approximately located around  $\lambda = 0.2$ , which slightly deviates from the coordinate of zero. The  $P_{-x}$  state with the lowest energy occurs at  $\lambda = -1$ , while the  $P_{+x}$  state is stabilized at  $\lambda = +1$ . The energy difference between these two opposite ferroelectric states is estimated to be 161.25 meV for the whole structure. These results confirm the stability of in-plane ferroelectricity in monolayer SnSe-based FTJ, and also reveal the characteristics of asymmetric ferroelectricity which will be important to achieve the enhanced TER effect in the following sections.

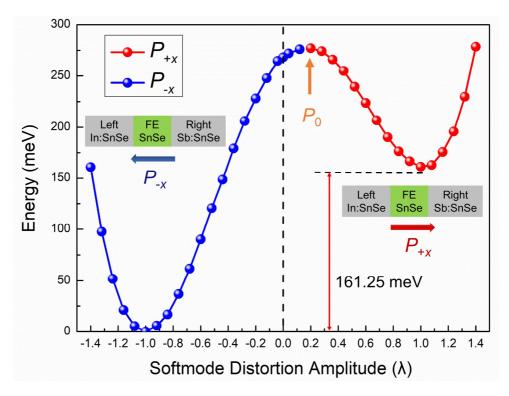


Figure 4 | Calculated asymmetric potential energy profile as a function of soft mode ferroelectric distortions in 2D p-SC/FE/n-SC FTJ. The ion displacements along [100] directions are characterized by  $\lambda$ , where  $\lambda = +1$  and -1 correspond to the ' $P_{+x}$ ' and ' $P_{-x}$ ' states, respectively. The energy is summed up to all the atoms in the p-SC/FE/n-SC supercell. The ' $P_{-x}$ ' state with minimum energy is set as the reference.

Interfacial metallization of ferroelectric barrier. Near the semiconductor/ferroelectric interfaces, the reversible metallization of insulating barrier may be brought about by polarization reversal<sup>42</sup>. To study this underlying property and further understand the change of barrier width, we calculate the layer-resolved local density of states (LDOS) for this 2D p-SC/FE/n-SC FTJ. As shown in Fig. 5a, for the hole doped electrode on the left, the valence band maximum (VBM) lies beyond the  $E_F$ , whereas the conduction band minimum (CBM) dips below  $E_F$  in the electron doped electrode on the right. According to the difference in work function step between two electrodes<sup>58</sup>, a notable built-in electric field is generated here, pointing from right to left across the ferroelectric SnSe barrier. When the ferroelectric polarization is pointing to the right, the built-in electric field is parallel to the depolarizing field, leading to an obvious band bending existing in the band alignment, where the valence band or conduction band of ferroelectric SnSe layers near the interface lies in the Fermi level. As a result, the effective tunnel barrier for the electrons tunneling is reduced in terms of these additional conducting regions added on the left and right interfaces.

If the polarization orientation is reversed to left, we can find a relatively slight band bending appearing in Fig. 5b as the built-in electric field is antiparallel to the depolarizing field. In addition, fewer SnSe unit cells become metallic compared to Fig. 5a. Consequently, the effective tunneling barrier width in Fig. 5b is wider than that in Fig. 5a. Note that the same direction of band bending for two opposite ferroelectric states indicates the built-in electric field is stronger than the depolarizing field.

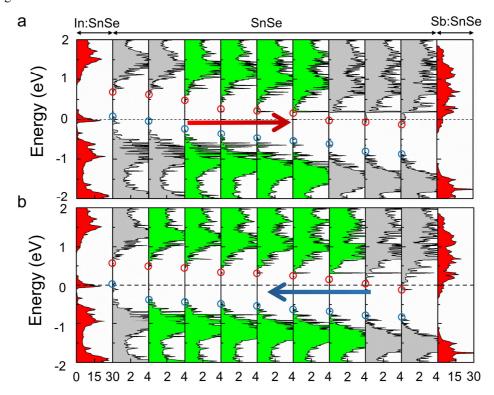


Figure 5 | Layer-resolved local density of states (LDOS) for (a) right and (b) left ferroelectric polarization states. The red and blue circles indicate the position of the CBM and VBM, respectively. The zero in energy axis is  $E_F$  marked by the black dashed line. The red shaded areas denote the electrode regions, the grey shaded ones represent the reversible metallization areas near the interfaces and the green ones correspond to the effective ferroelectric barrier regions. Here each LDOS of SnSe regions is summed up in two unit cells of SnSe monolayer in sequence.

## **Discussion**

Based on the above first-principles calculations, not only the height but also the width of the barrier can be tuned as a result of ferroelectric polarization reversal in 2D p-SC/FE/n-SC FTJ, which is consistent with the phenomenological model analysis. In order to evaluate the performance of SnSe-based 2D p-SC/FE/n-SC FTJ, non-equilibrium Green's function formalism calculations implemented in Atomistix ToolKit (ATK) is used to study the electrical conductance and TER effect. The central scattering region of this tunneling device is constructed by pristine SnSe monolayer, with semi-infinite p-type and n-type SnSe electrodes attached on the left and right sides, respectively. Transmission coefficients and reflection matrices are determined by matching the wave functions of the scattering region with linear combinations of propagating Bloch states in the electrodes. Since the electronic states at the  $E_F$  dominate the transport properties, the zero-bias electrical conductance within the Landauer-Büttiker formula<sup>59</sup> can be evaluated as:

$$G = G_0 \sum_{\mathbf{k}_{\parallel}} T\left(E_{\mathrm{F}}, \mathbf{k}_{\parallel}\right). \tag{3}$$

Here  $G_0 = 2e^2/h$  is the conductance quantum, e is the electron charge and h is the Planck's constant. The  $T(E_F, \mathbf{k}_{\parallel})$  is transmission coefficient at the Fermi energy for a given Bloch wave vector  $\mathbf{k}_{\parallel} = (k_x, k_y)$  in the two-dimensional Brillouin zone, which corresponds to the periodicity in the plane of the junction. In terms of integrating the transmission probability for states at the Fermi energy over the 2D Brillouin zone, we find that the total conductance  $G_R = 1.295 \times 10^{-5} \, \text{G}_0$  for the polarization state pointing to the right electrode. However,  $G_L$  is decreased to be  $8.306 \times 10^{-7} \, \text{G}_0$  when the ferroelectric orientation is reversed to the left. Following the conventional definition<sup>13</sup>, the TER ratio here is defined as:

$$TER = \frac{G_R - G_L}{G_I}.$$
 (4)

As a result, the reversal of ferroelectric polarization in such 2D *p*-SC/FE/*n*-SC FTJ will lead to a significantly enhanced TER effect, which is approximately about 1460 %. Note that the migrations of electrons or holes near the semiconductor surfaces are nearly ignored during the ATK calculations, where only considering the electron cloud diffusion, making the change of barrier width underestimated by ferroelectric polarization reversal. Hence, a giant TER effect can be expected in the practical applications, which is in accord with the enhanced results reported in Wu's work<sup>38,40</sup>.

In order to intuitively understand the large changes in the conductance ratio during polarization reversal, the  $\mathbf{k}_{\parallel}$ resolved transmission at  $E_F$  has been plotted in Fig. 6. For the in-plane ferroelectric polarization along x axis, the main
contribution to the transmission comes from the region around the edges of 2D Brillouin zone. However, we notice that the
transmission peak in the  $P_{-x}$  state is significantly reduced compared to that of the  $P_{+x}$  state, which verifies the characteristics
of a sizable TER effect existing in this proposed In:SnSe/SnSe/Sb:SnSe homostructure.

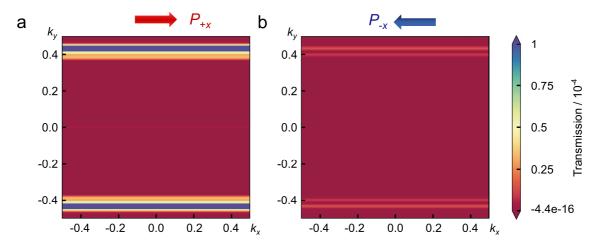


Figure 6 | The  $k_{\parallel}$ -resolved transmission in 2D Brillouin zone at the Fermi energy through the tunnel junction. (a) Under  $P_{+x}$  and (b)  $P_{-x}$  states. Polarization directions are shown by arrows. The two panels use the same color bar.

Finally, we further study the electronic structure across the 2D p-SC/FE/n-SC FTJ device by carrying out the analysis of real-space projected LDOS (PLDOS). The corresponding results for the two polarization states are plotted in Fig. 7, where the band bending at the left interface ( $L_{interface}$ ) and right interface ( $R_{interface}$ ) are clearly displayed, revealing the p-type and n-type doping features in the left and right electrodes, respectively. The green regions with high electronic density of states demonstrate the doped semiconductor electrodes. Moreover, the tunneling barrier width can be defined by the black regions without electronic states between the left and right electrodes. As expected, the effective tunneling regions will stay away from the initial semiconductor/ferroelectric interfaces due to the interfacial metallization of ferroelectric barrier. Note that the electrons tunnel across the junction under  $P_x$  state will experience a much wider barrier compared with that of the  $P_{tx}$  state, which is completely consistent with our model analysis and calculations.

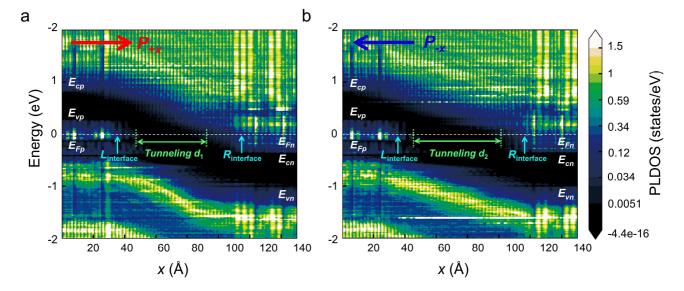


Figure 7 | Projected local density of states (PLDOS) across the 2D p-SC/FE/n-SC FTJ device with (a)  $P_{+x}$  and (b)  $P_{-x}$  states. The abscissa is the Cartesian coordinate of central region along the x-direction. The color bar on the right indicates the DOS amplitude.  $E_{ci}$  and  $E_{vi}$  are the conduction band minimum and valance band maximum of the electrode with i-type (i = p, n) doping, respectively. The Fermi level (i.e.,  $E_{Fp}$  and  $E_{Fn}$ ) is set to zero. The interface of semiconductor/ferroelectric is initially set to be located around 35 Å and 114 Å.

To conclude, we have proposed an all-2D materials based ferroelectric tunnel junction with the structure configuration of *p*-type semiconductor/ferroelectric/*n*-type semiconductor. We find that not only the tunneling barrier height but also the barrier width can be continuously tunable as a result of the polarization reversal. In particular, the mechanism of barrier width variation is not only ascribed to depletion/accumulation of majority carriers near the semiconductor surface in response to the reversal of ferroelectricity, but also influenced by the reversible metallization of barrier near the ferroelectric surface due to the band alignment. This well-designed 2D *p*-SC/FE/*n*-SC FTJ brings up a new idea approaching the giant

TER effect for the non-volatile resistive switching memories, which is of great importance in paving the way for the blueprint of the future nanodevice applications.

#### **Methods**

Ab initio calculations. The geometry optimizations and electronic structure calculations of slab models are performed within density-functional theory (DFT) using the projector augmented wave (PAW) method<sup>60</sup>, as implemented in the Vienna ab initio Simulation Package (VASP)<sup>61,62</sup>. The exchange correlation potential is described by generalized gradient approximation (GGA) of Perdew-Burke-Ernzerhof (PBE) functions<sup>63</sup>. The kinetic-energy cutoff of 500 eV is applied to the plane wave expansion and a Γ-centered  $1\times12\times1$  k-points grid is adopted for Brillouin zone sampling. All the structures are optimized until the Hellmann-Feynman forces tolerance below 1 meV/Å and self-consistent convergence for electronic energy is  $10^{-6}$  eV. A vacuum space of 15 Å is used to avoid interactions between adjacent layers.

Quantum transport calculations. The device properties of the 2D-FTJs are calculated using density functional theory plus non-equilibrium Green's function formalism<sup>64,65</sup> (DFT+NEGF approach) as implemented in Atomistix ToolKit-Virtual NanoLab (ATK-VNL) software package<sup>66</sup>. Double- $\zeta$  plus polarization basis set is employed, and a real-space mesh cut-off energy of 80 Hartree is used to guarantee the good convergence of the device configuration. The electron temperature is set at 300K. The 1×21×101 k-points are used for the self-consistent calculations to eliminate the mismatch of Fermi level between electrodes and central region. An increased number 201 of k-pints along y axis is adopted during the calculations of transmission spectra and spatially resolved projected local density of states.

**Data availability.** The data that support the findings of this study are available from the corresponding author upon reasonable request.

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#### **Author contributions**

C.G.D conceived the idea and supervised the work. X.W.S, Y.W.F and B.B.T did the model analysis. X.W.S carried out the first-principles calculations and non-equilibrium Green's function transport calculations and did the data analysis with the assistance of Y.W.F. B.B.T and Y.W.F contributed to the interpretation of the results. X.W.S, Y.W.F and C.G.D co-wrote the paper. All the authors reviewed and modified the manuscript.

### **Additional information**

Competing financial interests: The authors declare no competing financial interest.

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