

**Name: VIJETHA SHRAVYA**

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**OBJECTIVE:**

To obtain a creative and challenging position in an organization that gives me an opportunity for self improvement and leadership, while contributing to the symbolic growth of the organization with my technical, innovative and logical skills. Enhance my learning and develop my career. Work for the betterment of society and for the people.

**ACADEMICS:**

Qualification	Discipline	Institution	Board/University	Year	Score
BE	ECE	Rajarajeswari College Of Engineering, Bengaluru	VTU,Belagavi, Karnataka ,India.	2015-2019	1 <sup>st</sup> sem:6.58 2 <sup>nd</sup> sem:7.17 3 <sup>rd</sup> sem:6.57 4 <sup>th</sup> sem:6.29 5 <sup>th</sup> sem: 7.46
PUC	PCMB	Narayana PU College, Girinagar, Bengaluru	PUC Board, Karnataka	2013-2015	80%
SSLC	General	S.S.M HIGH SCHOOL,TR.Nagar, Bengaluru	SSLC Board, Karnataka	2012-2013	88.32%

**INTERNSHIP : APSIS SOLUTIONS,kanakapura main road,bengaluru** (design &development in embedded,IOT, raspberry-pi,PCB,android app,python scripting,hexapod robot)

**WORKSHOPS:**

**1. PROTEUS SIMULATION AND PCB DESIGN**

**Organized by:** Electronics and Communication Department

**Description:** We had a hands-on experience on PCB design where students were taught about proteus simulation and key information about PCB designing were given.

**2. ARDUINO WORKSHOP**

**Organized by:** ECE Branch Students

**Description:** The sessions were conducted based on arduino and robotics.How to use arduino board were taught. Different concepts were taught during the session and also participated

in mini-robot race.

#### **TECHNICAL SKILLS:**

- **Languages:** certified in (C,C++&JAVA), Verilog, VHDL,Embedded C
- **Designing Tools:** Solid Edge,Xilinx,Matlab,CCStudio
- **Others:** Arm Cortex M3-LPC1768(basic)

#### **NON-TECHNICAL SKILLS:**

- Learnt Carnatic music
- Participated in technical events conducted by National level Technical Symposium
- Participated in cultural fests and have won prizes

#### **AREA OF INTEREST:**

- Research and Development in areas of Embedded Systems.
- VLSI designing ( Verilog & VHDL), Testing and verification of VLSI modeling.
- Working on projects for defense organizations.
- Innovating new techniques in field of agriculture.

#### **PERSONAL DETAILS:**

**DOB:** 24/06/1997

**Father:** KRISHNA BHARADWAJ

**Languages known:** Kannada, English, Hindi

**Hobbies:** Photography, reading novels, travelling, writing poetry, numismatist, listening to music, watching football, teaching in government schools during free time.

#### **ACTIVITIES:**

- Working as a core & executive member and trustee in a non- governmental organization called, **YOUTH FOR REVOLUTION**. We have members across the state who have been actively involving in the events conducted by our organization.
- Involved in the events conducted by **Youth For Seva**.

**Declaration :** I here by declare that the above information furnished is true to the best of my knowledge.

**Date:**

**Place:** Bengaluru

**(Vijetha Shravya)**