**CHANDANA R P**

Chamundeshwari Extension, Ramanagar-562159

Ph no:  +91 9743664133

E-mail id: chandanavyshnav@gmail.com

**Career objective**

To secure a challenging position in Electronic industry which will enable me to provide best of my Technical, analytical & professional skills. And also sharpen my skills and knowledge and where I can have a good scope for learning and implementing new technologies, which could make me prosper in the future.

**Educational qualification**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Course** | **Board/University** | **Name of the institution** | **Year of passing** | **Marks obtained** |
| Bachelor of degree in Electronics and Communication | Visvesvaraya Technological University | RajaRajeswari college of Engineering,Bengaluru | 2018 | 56.39%  (pursuing) |
| 12th standard | Pre-University Education. | Cambridge Group of Institution,Channapatna. | 2014 | 80.83% |
| 10th standard | Karnataka Secondary Education Board. | Tilak memorial English school,Ramanagaram. | 2012 | 88.16% |

**Semester GPA details**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| FIRST | SECOND | THIRD | FOURTH | FIFTH | SIXTH | TOTAL AGGREGATE |
| 58.88% | 58.8% | 56.22% | 52.33% | 63.66% | 48.55% | 56.39% |

**Personal strength**

* Strong communication skills
* Disciplined and punctual
* Detail conscious
* Ability to survive in any situation.

**Technical Skills**

Programming language: Basics of C programming.

Software:  MS Office, Xilinx, MATLAB, Keil µvision.

 Assembly level language: Microprocessor, Microcontroller, Verilog, VHDL.

**Accomplishments**

Secured first class in National level maths talent quest examination in school level.

Secured first class in Internatioal chinthana science examination in school level.

Took part in the workshops organized by the department namely ‘Proteus Simulation &

PCB design’, ‘Design Thinking, Innovation and Implementation using Intel Edision Boards’, ‘Android technology’, ‘Learn Python’, ‘Touch of PLC in real world applications’.

 Worked as a coordinator in the National level Technical Symposium organized by our

Department.

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**Hobbies**

* Playing badminton
* Pencil sketching
* Computing.

**Personal details**

**Date of Birth :** 27 January 1996

**Father name :** R S Prasanna Kumar

**Mother name :** Rekha K S

**Gender :** Female

**Nationality :** Indian

**Languages known :** English, Kannada, Hindi.

**Declaration**

I hereby declare that all the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above mentioned particulars.

**Date:**

**Place:**

Yours obediently

(CHANDANA R P)