Name: Amber khurshid

Section: BAI-4A

Roll No: 22P-9295

COAL LAB TASK #03

This programs adds three numbers using memory variables.

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Step 1:

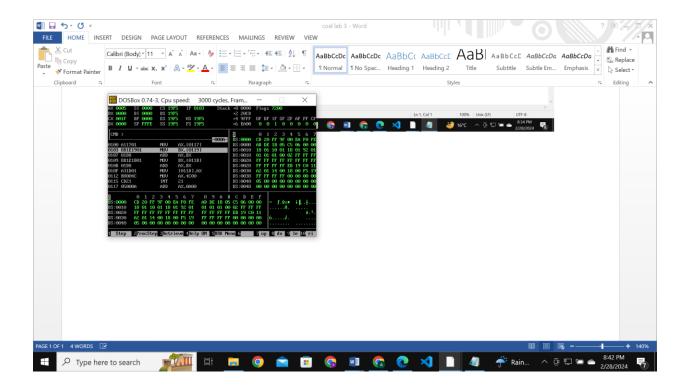
mov ax, [num1]

In assembly language it is given as:

0100 A11701

0100 shows the memory address of the instruction.

A11701 here A1 is opcode for mov instruction and 1701 is the address from where the value is to be moved in ax register.



Step 2:

mov bx, [num2]

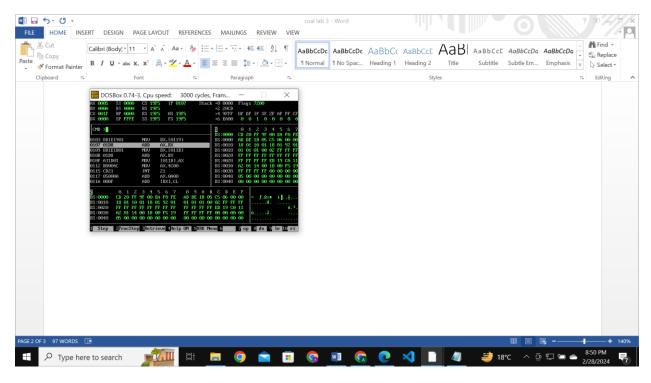
In assembly language it is given as:

0103 8B1E1901

0103 shows the memory address of the instruction.

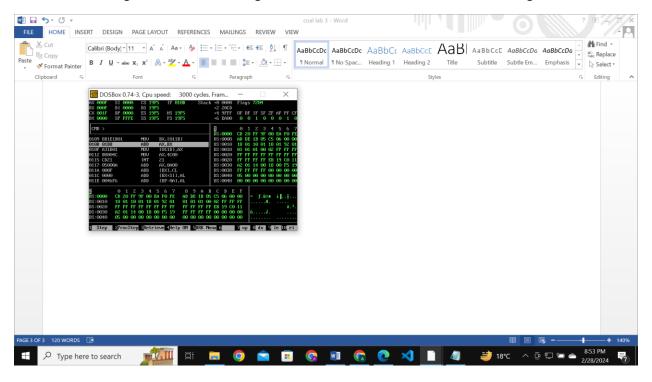
8B1E1901 here 8B is opcode for mov instruction and 1E1901 is the address from where the value is to be moved in ax register.

Here we are adding the value stored in the memory location "num3" having address 011B into the BX register.



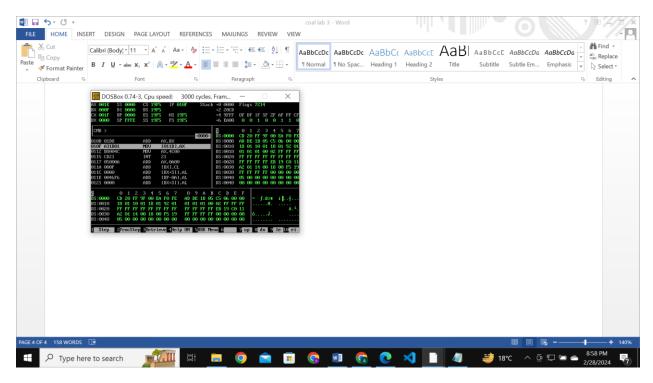
Step 3:

Here we are adding the value from bx register into ax and result is then stored in ax register.



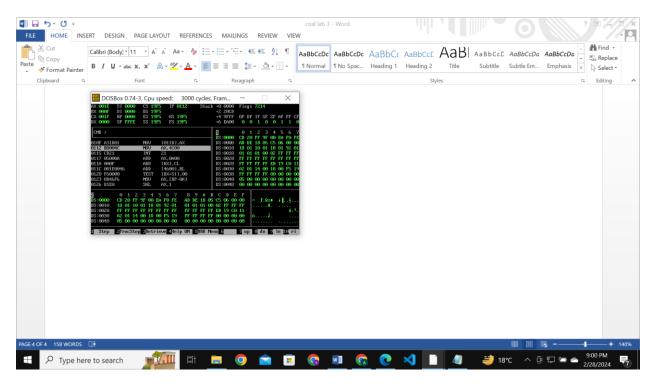
Step 4:

Here the value stored in ax register is moved into the memory location num4. This operation is represented by the opcode 'a3' with the address where the value should be stored.



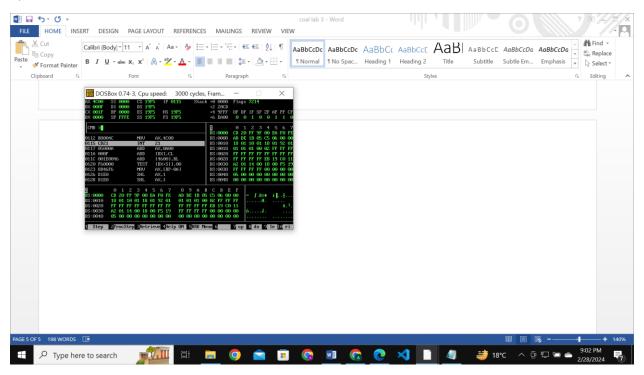
Step 5:

The instruction "mov ax, 0x4c00" moves the hexadecimal value "4c00" into the "ax" register. In assembly language, this operation is represented by the opcode "B8" followed by the value to be moved.



Step 6:

Here we have moved the hexadecimal value "4c00" into the "ax" register and is represented by the opcode "B8" with the value to be moved.

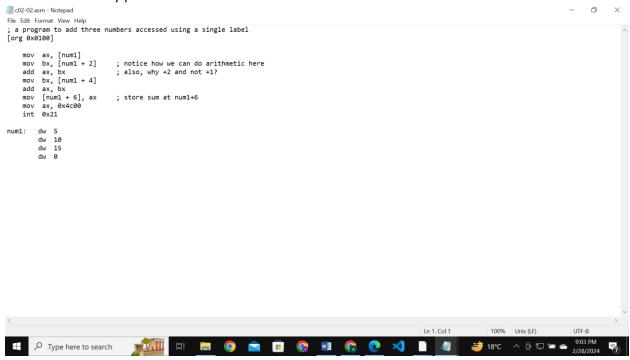


C02-02.asm

The instruction "mov bx, [num1 + 2]" is an arithmetic operation.

It is adding 2 to the address and getting the value stored there and moving it into "bx" register.

The values 5, 10, 15, and 0 are all defined under a single memory variable, "num1". And every addition of 2 to the memory points to the next value.



C02-03.asm

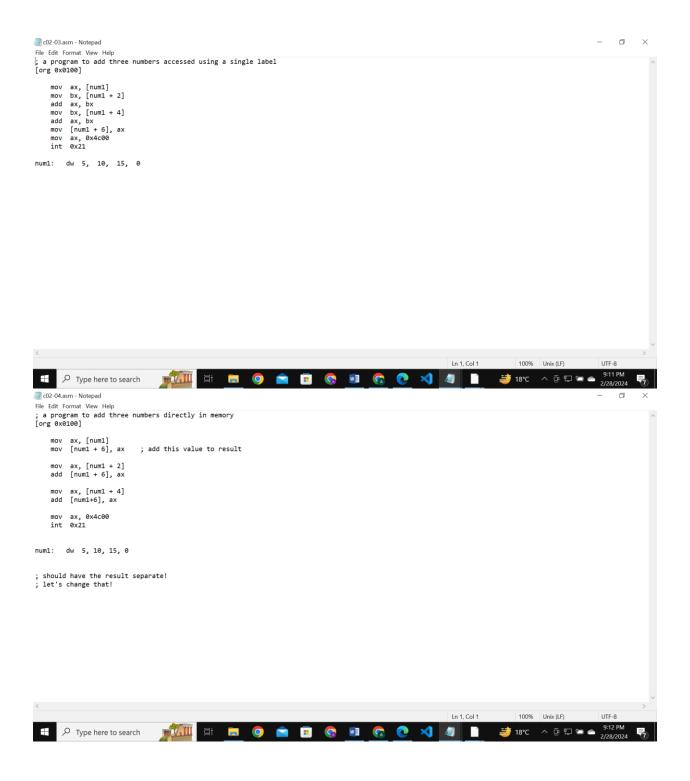
C02-02.asm and c02-03.asm

Here the label num1 points to the address of the value 5,10,15 and 0.0 contains the result of all the arithmetic operations performed. And every addition of 2 to the memory points to the next value.

C02-04.asm

C02-03.asm and c02-04.asm

The two provided code files differ primarily in their handling of result storage. In File 1, the result is stored in the same memory space as the input numbers, potentially altering the original data. On the other hand, File 2 adopts a cleaner approach by keeping the input numbers intact and storing the result separately. File 1 utilizes registers (ax and bx) for arithmetic operations and employs the same memory location (num1) for both input and result, emphasizing a register-centric methodology. In contrast, File 2 directly performs arithmetic operations in memory without extensive register use, separating input numbers (num1) from the result, which is stored at num1 + 6. This approach operates more straightforwardly in memory with fewer register operations, contributing to a more streamlined code structure.



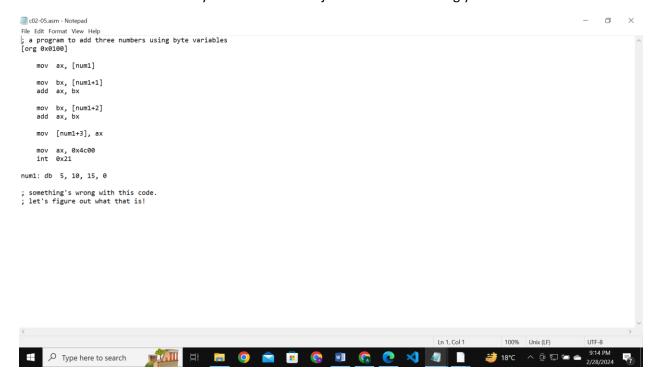
C02-05.asm

C02-04.asm and c02-05.asm

The provided code represents a program designed to add three numbers using byte variables. However, there appears to be an issue in the code. In File 1, the program loads each number from the num1 array

into the ax register, performs addition operations, and then stores the result back into memory. It utilizes words (16 bits) for each number in the num1 array and employs only the ax register to execute arithmetic operations. The code treats each number as a 16-bit word and accesses memory in 16-bit chunks (word-by-word).

In contrast, File 2 uses bytes (8 bits) for each number in the num1 array, employing both ax and bx registers. Ax is used to load numbers, while bx holds the intermediate sum during addition. The program performs addition using the bx register to accumulate the sum before storing the result back into memory. File 2 treats each number as an 8-bit byte and accesses memory in 8-bit chunks (byte-by-byte). The distinction in data size between the two files (words in File 1 and bytes in File 2) might be the source of the issue. Ensure consistency in data size and adjust the code accordingly for a successful execution.

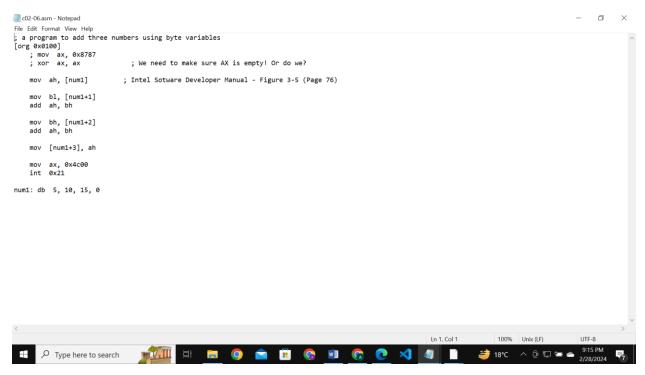


C02-06.asm

C02-05.asm and c02-06.asm

The provided code constitutes a program aimed at adding three numbers using byte variables. In File 1, the ax register is utilized to accumulate the sum, and the bx register is employed to load each number from memory. The addition operation is directly performed using the add instruction with the ax register.

Contrastingly, File 2 utilizes the ah, bl, and bh registers to manage each byte of the numbers and accumulate the sum in the ah register. The addition of numbers is executed using the add instruction with the ah register, along with a temporary bh register for each byte. Both programs access memory using byte-by-byte chunks (db directive). The commented-out code suggests a consideration for ensuring the ax register is empty, although the necessity for this is left open-ended. The provided code demonstrates a byte-oriented approach, emphasizing the manipulation of individual bytes in the registers for the arithmetic operations.



C02-06b.asm

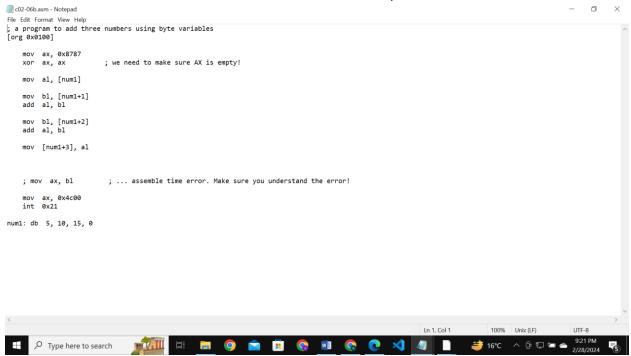
C02-06.asm and c02-06b.asm

In x86 assembly language, AH and AL constitute the higher and lower halves, respectively, of the AX register, which is a 16-bit general-purpose register. Similarly, BH and BL represent the higher and lower halves of the BX register, another 16-bit general-purpose register. Therefore, AH and AL signify the high and low bytes of the AX register, while BH and BL represent the high and low bytes of the BX register.

Regarding File 1, on Line 6, it loads the first number from the num1 array into the ah register. On Line 8, it loads the second number into the bl register. However, Line 9 contains an error as it attempts to add the uninitialized values in ah and bh registers, which hold garbage values. The correct operation should involve adding ah and bl. On Line 11, the third number is loaded into the bh register, and on Line 12,

there's a similar error in trying to add the uninitialized values in ah and bh registers instead of ah and bh. Finally, Line 14 stores the result, the sum in ah, into the memory location num1 + 3.

In File 2, Lines 4-5 initialize ax with the value 0x8787 and then clear it to ensure it's empty. Line 7 loads the first number from the num1 array into the al register, and Line 9 loads the second number into the bl register. Lines 10 and 13 correctly add the values in al and bl registers. Lines 12 and 15 handle the third number and store the result, the sum in al, into the memory location num1 + 3.



C02-07.asm

C02-06b and C02-07.asm

In File 1, AX is first initialized with 0x8787 and then cleared to ensure it is empty. Subsequently, AL and BL are employed to sequentially add byte values from memory.

On the other hand, in File 2, AX is cleared using the XOR operation in preparation for subsequent addition operatins. Meanwhile, BX serves as a pointer to access byte values in memory sequentially. The

result of the computations is stored in AX, representing the final sum.

