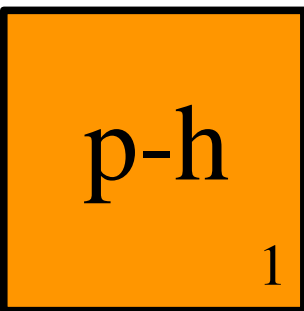
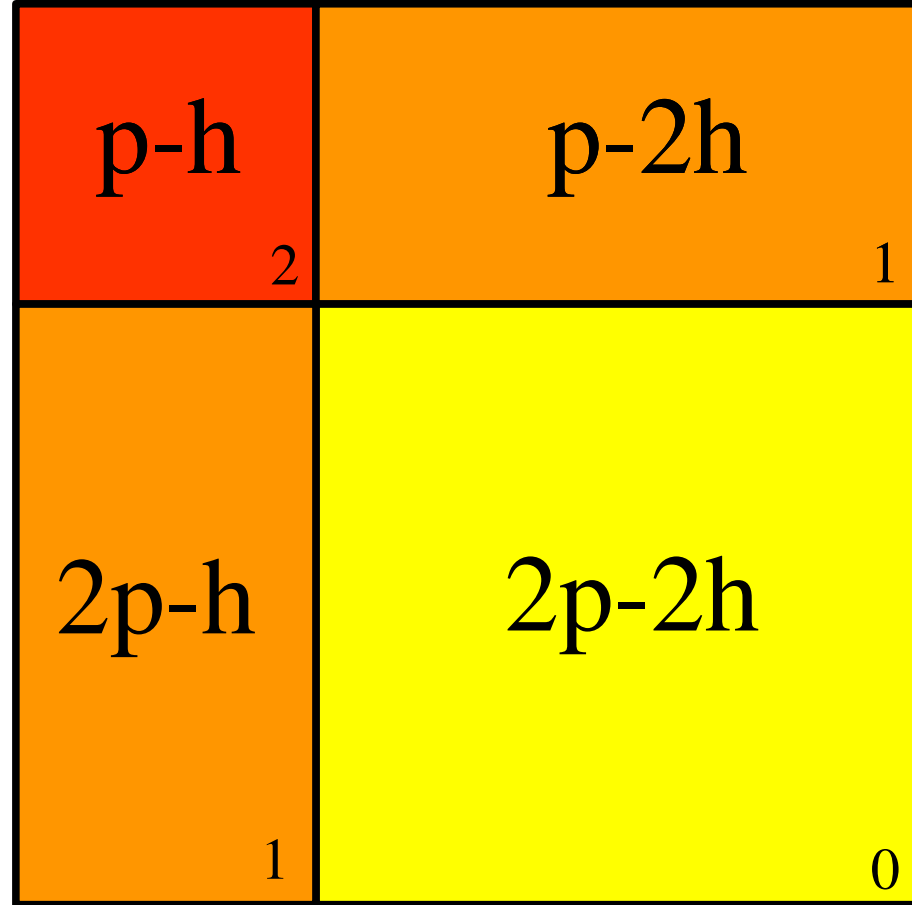


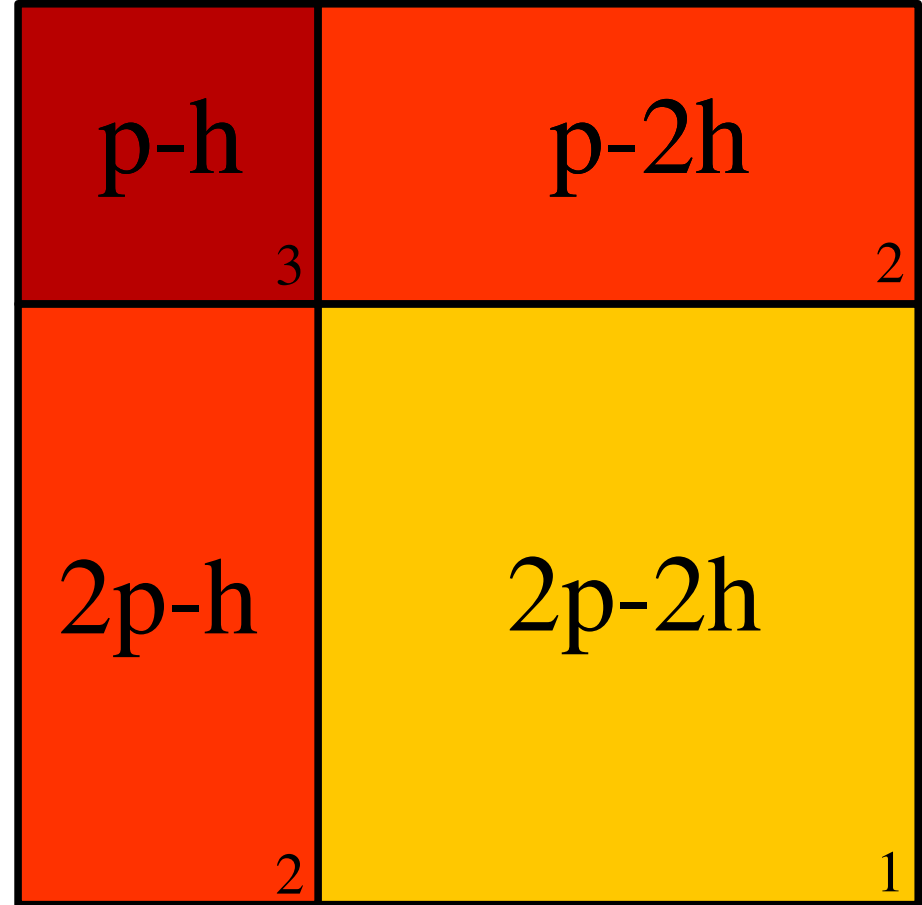
ADC(0)



ADC(1)



ADC(2)



ADC(3)