Amelia Cook

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FDUCATION

Tufts University

B.S. Computer Science Expected Grad. May 2024 Cum. GPA: 3.94 Dean's List Honoree

SKILLS

Languages:

C/C++, System Verilog, Java, Bash, Groovy, JavaScript, Python

Software and Tools:

Git, Make, Valgrind, Scrum, Agile

COURSEWORK

Completed:

Machine Structure and Assembly Intro to Security Programming Languages Algorithms Data Structures

Fall 2023:

Digital Design Verification Software Engineering

ACTIVITIES

Clubs and Hobbies:

Crochet, Baking, Travel Women in Computer Science

Baha'i Activities:

Junior Youth animator Ruhi Institute tutor, participant Outreach

WORK EXPERIENCE

GD Mission Systems - Engineering Intern

Jun 2023 - Aug 2023

- Built and implemented custom verification tool for Atlassian using Java, Bash to automate tasking
- Translated tool to Groovy to integrate with Jira workflow
- Coordinated with client and investigated bugs to iterate tool versions

MITRE - Software Engineering Intern

May 2022 - Aug 2022

- Utilized and mastered over 10 new technologies and tools in a versatile project-based environment
- Customize and deploy open-source app, configure
 Kubernetes deployment using Helm charts, Docker, and kind

LEADERSHIP EXPERIENCE

Tufts Computer Science Data Strucutres – Teaching Fellow Sep 2021 - Present

- Debug errors in C++, guide program architecture, answer questions on data structures and algorithms
- Coordinate team of 40 TAs, write assignment rubrics and oversee grading decisions, present weekly labs

Tufts Emerging Scholars Program – Peer Leader Oct 2021 – Present

- Promote collaborative and inclusive environment, guide discussions and activities, coordinate topics and logistics
- Prepare weekly presentations to introduce advanced topics such as AI/ML, cybersecurity, and natural language processing

Tufts Women in Technology Conference – Conference Chair May 2022 - Oct 2022

- Oversee and organize four committees to plan a conference for almost 400 people, including over 20 sponsors
- Coordinate with sponsors and guest speakers to host workshops, talks, and panels, and to create opportunities

PROJECTS

FIFC

Simulated a simple FIFO module using System Verilog and created a test bench to verify model worked as expected.

Image Processing in C

Implemented lossy compression and decompression algorithm. Optimized image transformations for cache hits.