

Savitribai Phule Pune University

(formerly University of Pune)

Telephone: 020 - 25621376 / 77 Web: www.unipune.ac.in

TRANSCRIPT

Ganeshkhind, PUNE-411007.

e-mail: transcript@unipune.ac.in

This is to certify that MR./MS. BHILEGAONKAR AMEY SADANAND Mother's Name: SMITA (SEAT NO. B150053038) has appeared for the BACHELOR OF ENGINEERING (ELECTRONICS & TELECOMMU.) (71) (2015 PAT.) examination of this University held in APR/MAY 2019 and is declared to have passed the said examination in FIRST CLASS WITH DISTINCTION with CGPA: 8.78.

	He/She has obtained following marks at the said examination. SUBJECT			(P.R.NO.71600885J) CREDIT				
CODE		JERSIT SAVI	TRIBAL PROTE PUNE UNI	TOT.	EARN.	GRD	GP.	
	F.E. SEAT NO. F150050149 MAY 2016	WERST SAV	TRIBAL PHULE PUNE UN	CRD 05	CRD 05	0	50	
107001	ENGINEERING MATHEMATICS I	ERSTLYSA		05	05	A+	45	
107009	ENGINEERING CHEMISTRY		WITH BIBAI PROJET PUNEU	02	02	A	16	
110003	FUNDAMENTALS OF PROG. LANG.I			04	04	0	40	
103004	BASIC ELECTRICAL ENGG.			04	04	A	32	
101005	BASIC CIVIL AND ENV. ENGG.		SA TRIBA BLULE LINE	04	04	A+	36	
102006	ENGINEERING GRAPHICS I			01	01	0	10	
111007	WORKSHOP PRACTICES			04	04	A+	36	
107008	ENGINEERING MATHEMATICS II			05	05	A+	45	
107002	ENGINEERING PHYSICS		PIE PHULF FUT	02	02	B+	14	
110010	FUNDAMENTALS OF PROG. LANG.II			05	05	0	50	
101011	ENGINEERING MECHANICS			04	04	0	40	
104012	BASIC ELECTRONICS ENGG.			04	04	A	32	
102013	BASIC MECHANICAL ENGG.			01	01	0	10	
102014	ENGINEERING GRAPHICS II		TOTAL CREDITS I			DIN TRI		
	E PUNE UNIVERSITY ST SGPA	: 9.12	TOTAL CREDITS I	ARNE	ERSI			
RIBAL PHI	S.E. SEAT NO. S150053038 MAY 2017			тот.	EARN.	GRD	GP.	
RIDALPH	S.E. SEAT NO. 3130033030 MILT 2017			CRD	CRD	TAVAT	RIBALDE	
204181	SIGNALS & SYSTEMS	The same of the sa	TH	03	03	A	24	
204181	SIGNALS & SYSTEMS		TW	01	01	A+	09	
204182	ELECTRONIC DEVICES & CIRCUITS		TH	04	04	A+	36	
204182	ELECTRONIC DEVICES & CIRCUITS		PR	01	01	В	06	
204183	ELECTRIC CIRCUITS & MACHIN		TH	03	03	B+	21	
204183	ELECTRIC CIRCUITS & MACHIN		TW	01	01	A+	09	
204184	DATA STRUCTURES & ALGORITHMS		TH	04	04	A+	36	
204184	DATA STRUCTURES & ALGORITHMS		OR	01	01	A+	09	
204185	DIGITAL ELECTRONICS		TH	04	04	В	24	
204185	DIGITAL ELECTRONICS		PR	01	01	A+	09	
204186	ELECTRO MEAS INSTRU & TOOLS		TW	02	PUNO2	A	16	
204187	INTEGRATED CIRCUITS		THE	04	PU 04	0	40	
204187	INTEGRATED CIRCUITS		TW+PR	ALTOI !	EPUOL U	1 V O	10	
204188	CONTROL SYSTEMS	BANDE BU	NE UNIVERSITY THINK	AL 03	EP-03	A+	27	
204189	ANALOG COMMUNICATION	BY PHULE PL	INE UNIVERSITY THAT	BAI 03	LEP 03 -	IN A+	27	
204189	ANALOG COMMUNICATION	IBAI PHULE PI	INE WIVERS PR	BAO1	HE FOI	UNIA	08	
204190	OBJECT ORIENTED PROGRAMMING	SICAL PHULL	UNE UNIVERSITY THEY	1B 03	ULE 03	UNAFR	ST 24	
204190	OBJECT ORIENTED PROGRAMMING		UNE UNIVERSITOR	218 02	ULE 02 NE	U B+	ST 14	
204191	EMPLOYABILITY SKILL DEVELOP		PUNE UNIVERSITE TWANT	RIB03	HULE 03	EUO E	30	
207005	ENGINEERING MATHEMATICS III	TRIBAL PHULLE	PUNE UNIVERSITH SAVI	104	HUL 04 UN	EUOVE	40	
207005	ENGINEERING MATHEMATICS III	- MALL WILL	PUNE UNIVERSITY SAV	01	HUL OI	A+	RS 09	



TOTAL CREDITS EARNED: 50



: 8.56



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		T.E. SEAT NO. T150053044 MAY 2018		TOT.	EARN.	GRD	GP.
				CRD	CRD		
	304181	DIGITAL COMMUNICATION	TH	03	03	A+	27
	304182	DIGITAL SIGNAL PROCESSING	TH	03	03	Α	24
	304183	ELECTROMAGNETICS	TH	04	04	B+	28
	304184	MICROCONTROLLERS	TH	03	03	A	24
	304185	MECHATRONICS	TH	03	03	O	30
-	304191	SIGN. PROC. & COMM. LAB	TW	01	01	A+	09
	304191	SIGN. PROC. & COMM. LAB	PR	01	01	0	10
	304192	MICROCON. & MECHATRONICS LAB	TW	01	01	0	10
	304192	MICROCON. & MECHATRONICS LAB	PR PR	01	01	A	08
	304193	ELECTRONICS SYSTEM DESIGN	OR	03	03	A+	27
	304186	POWER ELECTRONICS	THE	03	03	A+	27
	304187	INFO.THEORY CODING & COMM. N/W	THE THE	04	04	A+	36
	304188	BUSINESS MANAGEMENT	TH	03	03	0	30
3	304189	ADVANCED PROCESSORS	TH	03	03	В	18
3	304190	SYSTEM PROG. & OPERATING SYS.	TH	03	03	A+	27
3	304194	POWER & ITCI LAB	TW	01	01	A+	09
3	304194	POWER & ITCI LAB	PR OU	01	01	0	10
3	304195	ADV. PRCSR. & SYS. PROG. LAB	TW N	01	01	A+	09
3	304195	ADV. PRCSR. & SYS. PROG. LAB	PR	01	01	A	08
3	304196	EMPLOY. SKILLS & MINI PROJ.	OR	03	03	0	30
		SGPA : 8.72	TOTAL CREDITS	DITS EARNED : 46			
		B.E. SEAT NO. B150053038 APR/MAY 2019		тот.	EARN.	GRD	GP.
				CRD	CRD	A STATE	AL PHANK
4	404181	VLSI DESIGN & TECHNOLOGY	TH	03	03	A	24
4	404182	COMPUTER NETWORKS & SECURITY	TH	04	04	A	32
4	404183	RADIATION & MICROWAVE TECHNI.	TH	03	03	A+	27
1	404184A	DIGITAL IMAGE & VIDEO PROCE.	TH	03	03	A+	27
4	404185D	ARTIFICIAL INTELLIGENCE	TH	03	03	A	24
17	404186	LAB PRACTICE -I (CNS+ RMT)	TW	01	01	- A	08
- 4	404186	LAB PRACTICE -I (CNS+ RMT)	OR	01	01	B+	07
4	404187	LAB PRACTII (VLSI + ELE. I)	TW	01	01	0	10
4	404187	LAB PRACTII (VLSI + ELE. I)	PR	01	01	0	10
4	404188	PROJECT STAGE I	OR	02	02	0	20
4	404189	MOBILE COMMUNICATION	TH	03	03	A+	27
4	404190	BROADBAND COMM, SYSTEMS	TH	04	04	A+	36
4	404191A	MACHINE LEARNING	TH	03	03	A	24
4	404192A	ROBOTICS	TH	03	03	B+	21
4	404193	LAB PRACTICE-III (MC+BCS)	TW	01	01	A+	09
4	404193	LAB PRACTICE-III (MC+BCS)	PR	01	01	A	08
	404194	LAB PRACIV (ELECTIVE III)	OR	01	01	A+	09

FE SGPA: 9.12 SE SGPA: 8.56 TE SGPA: 8.72
TOT GP/TOT CREDITS: 1668/190 CGPA: 8.78 FIRST CLASS WITH DISTINCTION
AWARD OF DEGREE: ON THE BASIS OF PERFORMANCE FROM SEMESTER I TO VIII
DURATION OF COURSE: 04 YEARS AFTER 10+2 (HSC)
ENTRY POINT: 10+2(HSC) GRADUATION: 16 YEARS

SGPA

(PP-PAPER, PR PRACTICAL, TW-TERM WORK, OR-ORAL) (MEDIUM OF INSTRUCTION: ENGLISH)

PROJECT STAGE II

PROJECT STAGE II

REF.NO:EXAM./TRANS./BEN211013051 DATE:13/10/2021

404195

404195

DIRECTOR

TW

OR

TOTAL CREDITS EARNED: 44

BOARD OF EXAMINATIONS & EVALUATION.

04

02

0

40

20



: 8.70