

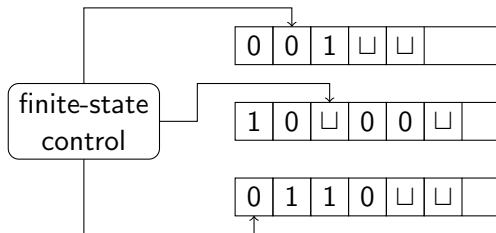
CIS 770: Formal Language Theory

Pavithra Prabhakar

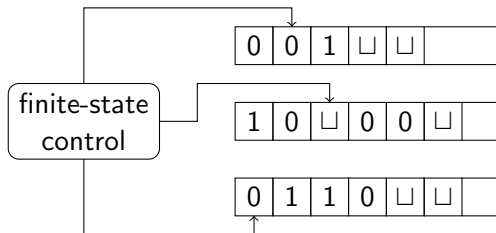
Kansas State University

Spring 2016

Multi-Tape Turing Machine

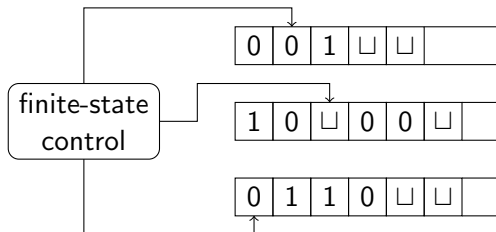


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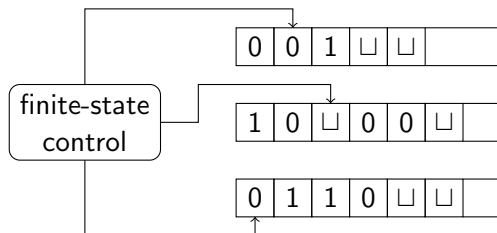
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- In one step: Read symbols under each of the k -heads, and depending on the current control state, write new symbols on the tapes, move the each tape head (possibly in different directions), and change state.

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Formal Definition

A k -tape Turing Machine is $M = (Q, \Sigma, \Gamma, \delta, q_0, q_{\text{acc}}, q_{\text{rej}})$ where

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- $\delta : (Q \setminus \{q_{\text{acc}}, q_{\text{rej}}\}) \times \Gamma^k \rightarrow Q \times (\Gamma \times \{L, R\})^k$ is the transition function.

Computation, Acceptance and Language

- A configuration of a multi-tape TM must describe the state, contents of all k -tapes, and positions of all k -heads. Thus, $C \in Q \times (\Gamma^*\{*\}\Gamma\Gamma^*)^k$, where $*$ denotes the head position.

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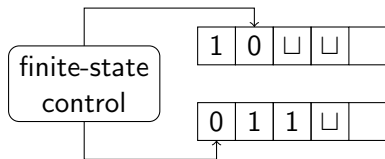
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- How do we simulate the movement of k independent heads?

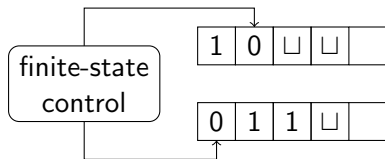
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Multi-tape TM M

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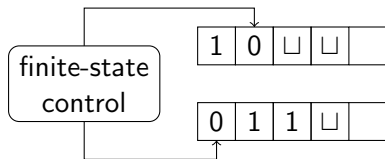
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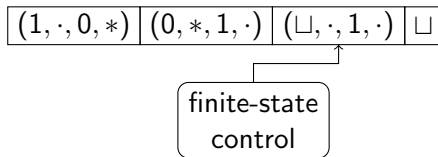
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1-tape equivalent single(M)

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- Once again, scan the tape, change all relevant contents, “move” heads (i.e., move $*$ s), and change state.

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Formal construction in notes.

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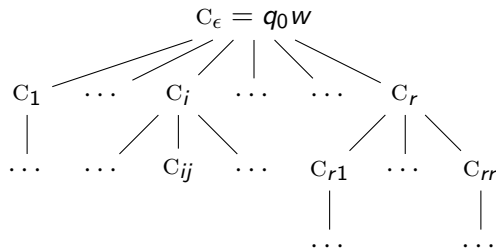
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- **Idea 1:** $\text{det}(M)$ tries to keep track of all possible “configurations” that M could possibly be after each step. Works for DFA simulation of NFA but not convenient here.
- **Idea 2:** $\text{det}(M)$ will simulate M on each possible sequence of computation steps that M may try in each step.

Nondeterministic Computation

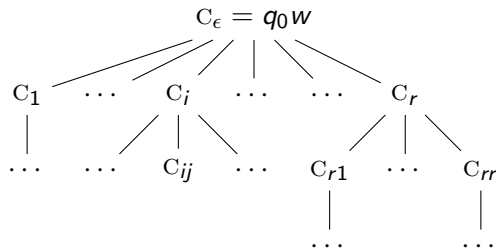
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- Input w is accepted iff \exists accepting configuration in tree.

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Observe that $\text{det}(M)$ may not terminate if w is not accepted.

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- Tape 3, called **choice tape**, will store the current sequence of nondeterministic choices

Execution of $\text{det}(M)$

- ① Initially: Input tape contains w , simulation tape and choice tape are blank
- ② Copy contents of input tape onto simulation tape
- ③ Simulate M using simulation tape as its (only) tape
 - ① At the next step of M , if state is q , simulation tape head reads X , and choice tape head reads i , then simulate the i th possibility in $\delta(q, X)$; if i is not a valid choice, then goto step 4
 - ② After changing state, simulation tape contents, and head position on simulation tape, move choice tape's head to the right. If Tape 3 is now scanning \sqcup , then goto step 4
 - ③ If M accepts then accept and halt, else goto step 3(1) to simulate the next step of M .
- ④ Write the lexicographically next choice sequence on choice tape, erase everything on simulation tape and goto step 2.

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- If M does not accept w then no sequence of choices leads to acceptance. $\text{det}(M)$ will therefore never halt!

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- Initially, the program instructions are stored in a contiguous block of memory locations starting at location 1. All registers and memory locations, other than those storing the program, are set to 0.

Instruction Set

- `add X, Y`: Add the contents of registers X and Y and store the result in X .
- `loadc X, I`: Place the constant I in register X .
- `load X, M`: Load the contents of memory location M into register X .
- `loadI X, M`: Load the contents of the location “pointed to” by the contents of M into register X .
- `store X, M`: store the contents of register X in memory location M .
- `jmp M`: The next instruction to be executed is in location M .
- `jmz X, M`: If register X is 0, then jump to instruction M .
- `halt`: Halt execution.

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Proof sketch in the notes.



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- Enhanced Turing Machine models: TM with 2-way infinite tape, multi-tape TM, nondeterministic TM, probabilistic Turing Machines, quantum Turing Machines ...
- Restricted Turing Machine models: queue machines, 2-stack machines, 2-counter machines, ...

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- In the course, we will use an informal pseudo-code to argue that a problem/language can be solved on Turing machines
- To show that something can be solved on Turing machines, you can use any programming language of choice, *unless the problem specifically asks you to design a Turing Machine*