

Project Name : Ameba Pro 2
Project PCB :
PCB VERSION : V01B
Project Phase : DVT

Page Description

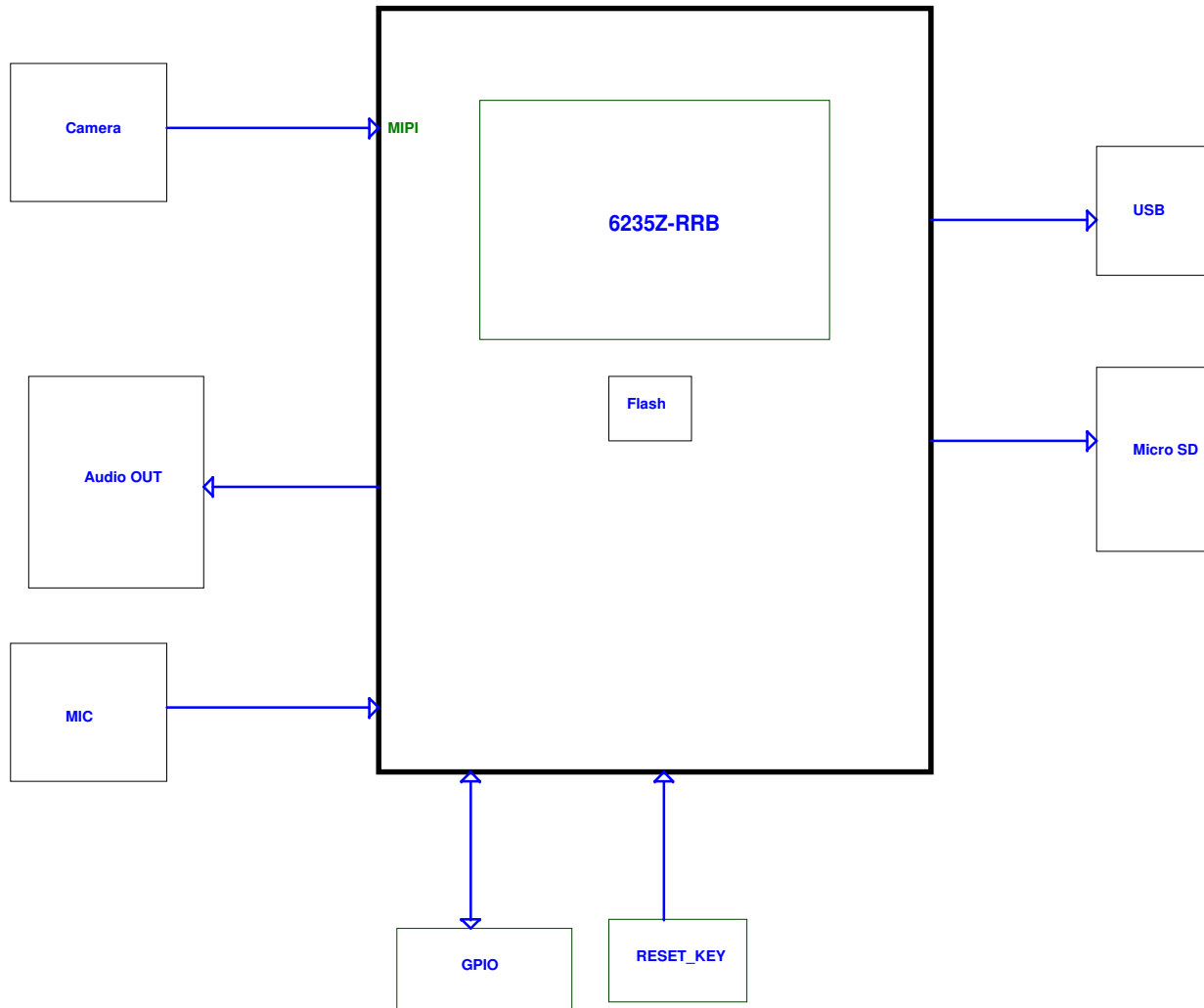
- 01 COVER PAGE
- 02 SYSTEM BLOCK
- 03 POWER BLOCK
- 04 MODULE
- 05 CAMERA
- 06 Audio
- 07 MicroSD and IO
- 08 POWER
- 09 EE-PIR-V01

Revision History

REV. No.	DATE	PAGE	CONTENTS	FILE NAME	DEP.	RELEASED BY
01	2022-9-20	1-9	new design	Ameba_PCBV01_20220920.DSN	WINSTEC-RD-HW	

Ameba MAIN BOARD

- SYSTEM BLOCK DIAGRAM -

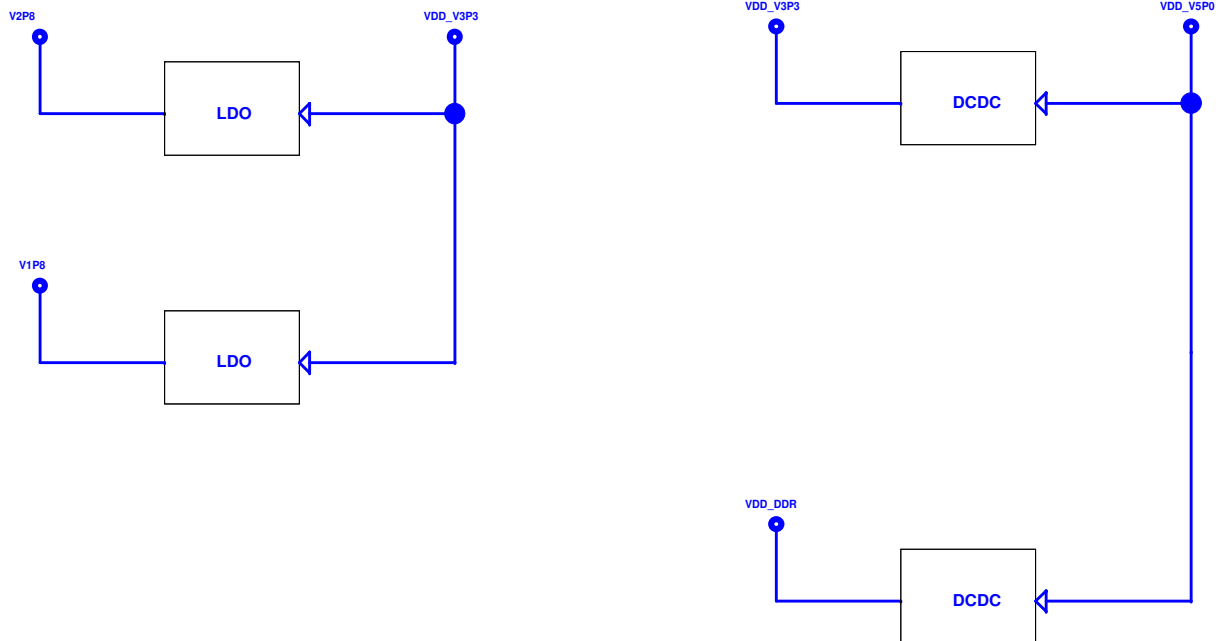


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Ameba MAIN BOARD		
Size A2	Document Number SYSTEM BLOCK	Rev 01B
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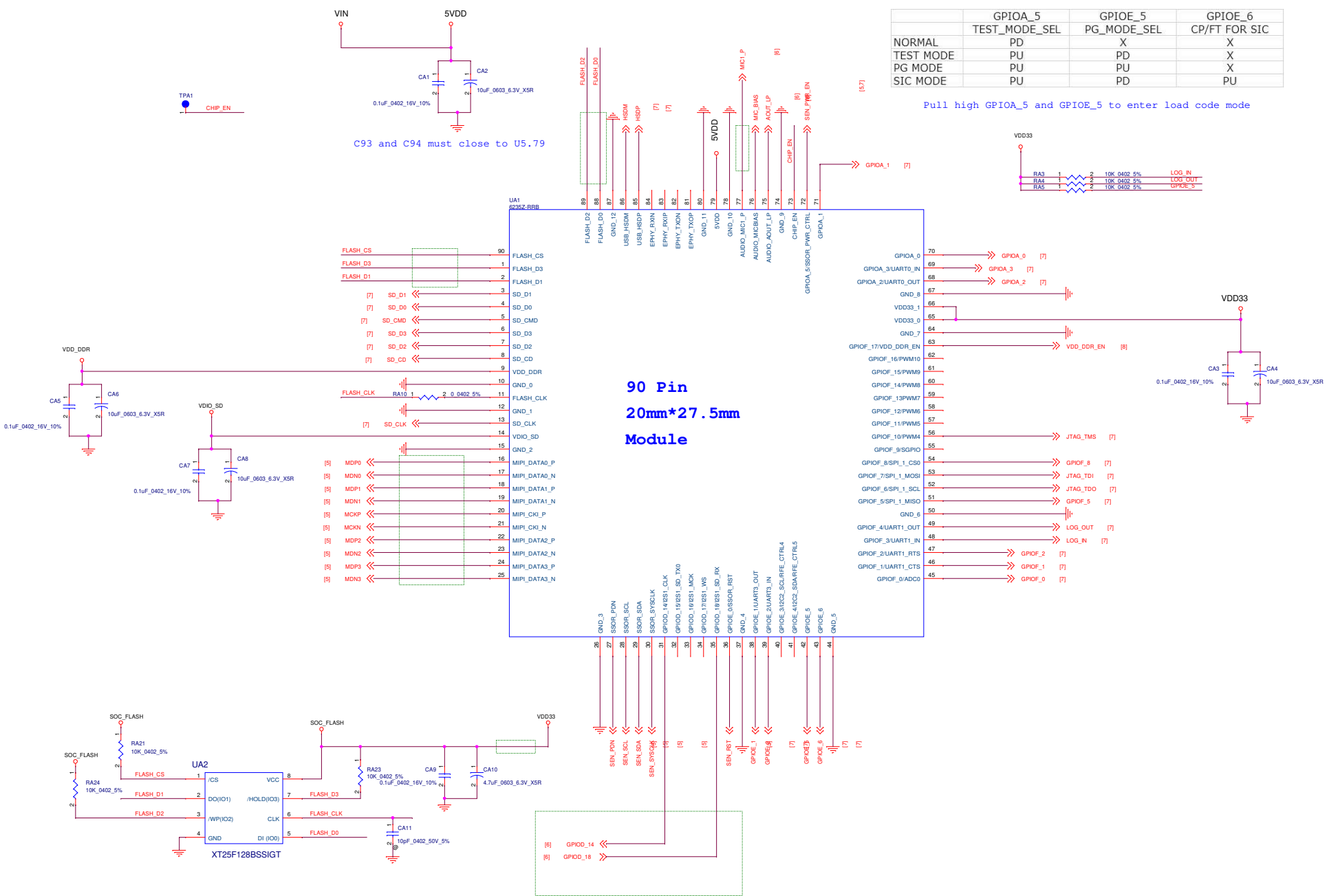
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- POWER BLOCK -



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C	POWER BLOCK	01B	
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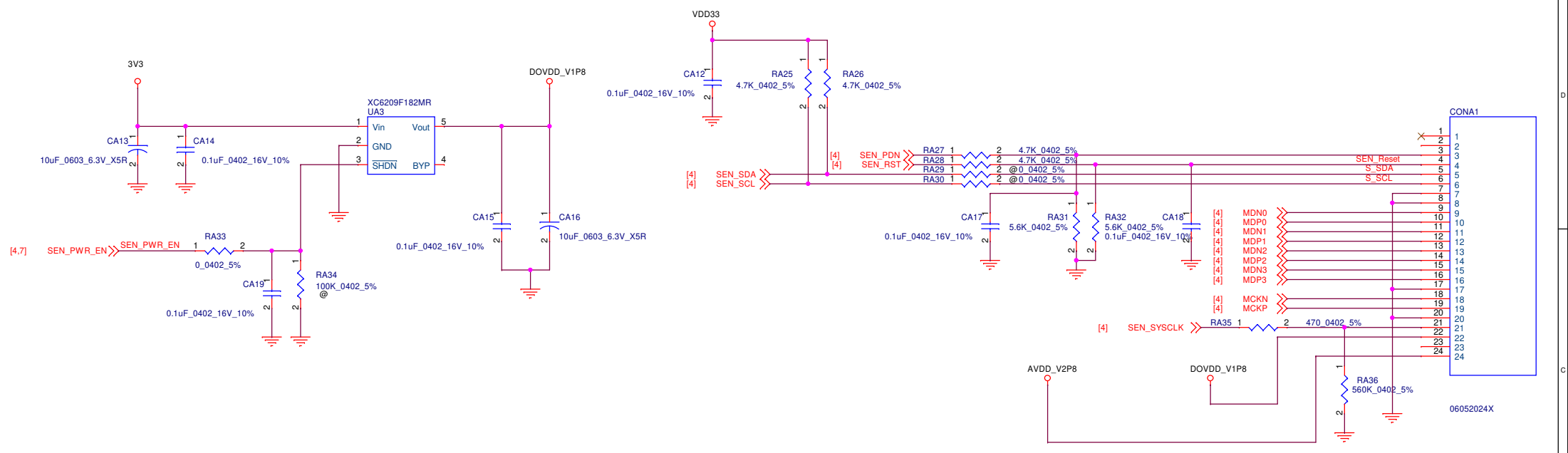


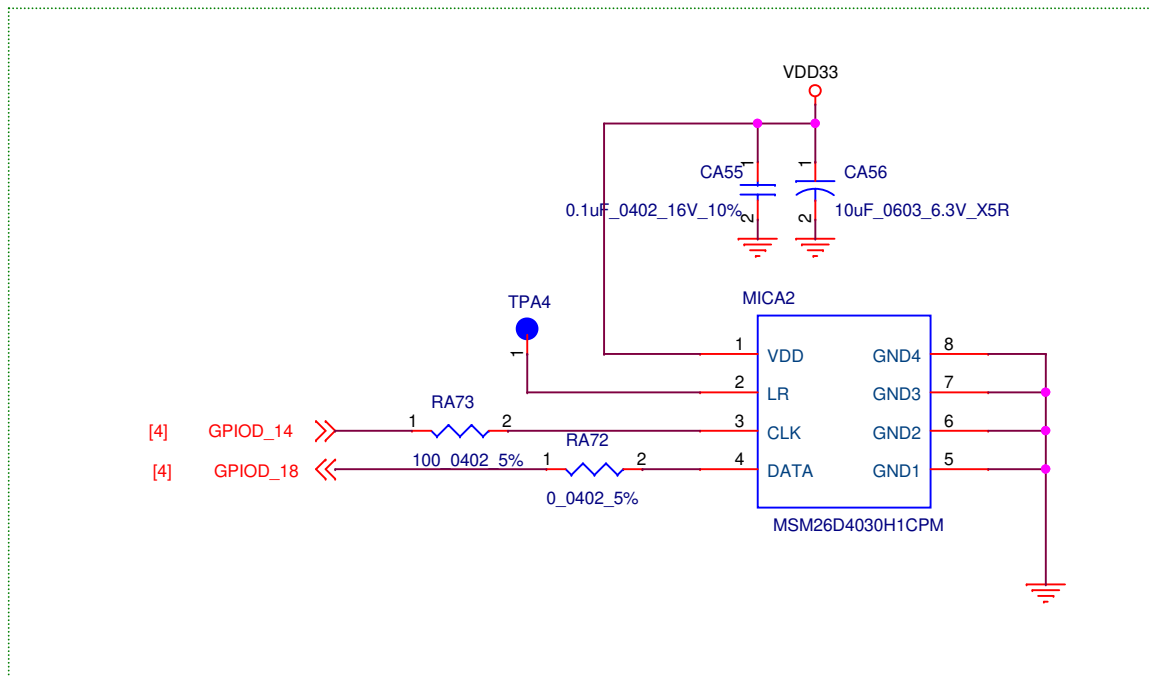
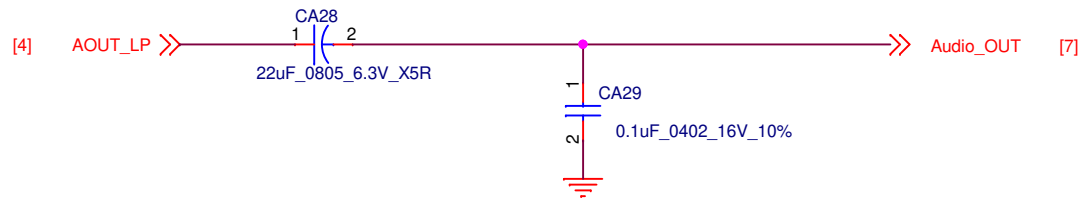
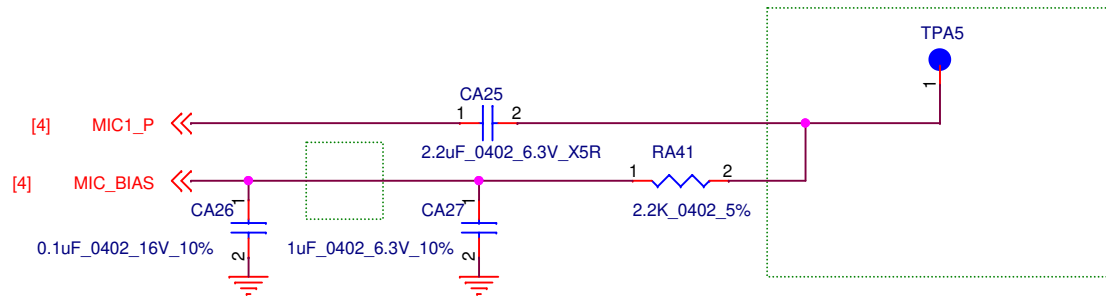
	GPIOA_5	GPIOE_5	GPIOE_6
	TEST_MODE_SEL	PG_MODE_SEL	CP/FT FOR SIC
NORMAL	PD	X	X
TEST MODE	PU	PD	X
PG MODE	PU	PU	X
SIC MODE	PU	PD	PU

Pull high GPIOA_5 and GPIOE_5 to enter load code mode

90 Pin
20mm*27.5mm
Module

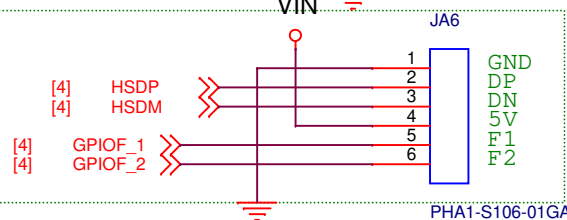
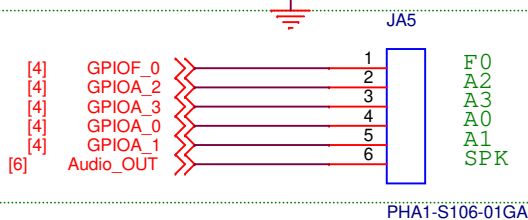
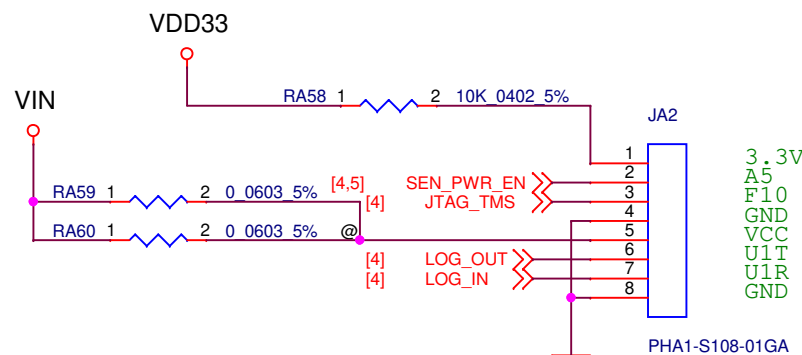
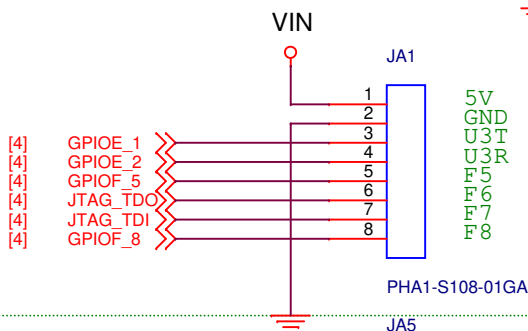
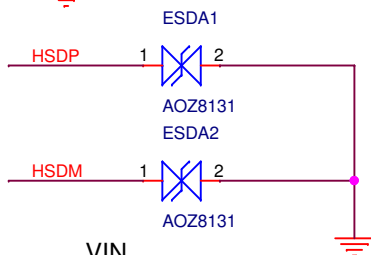
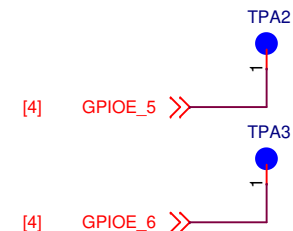
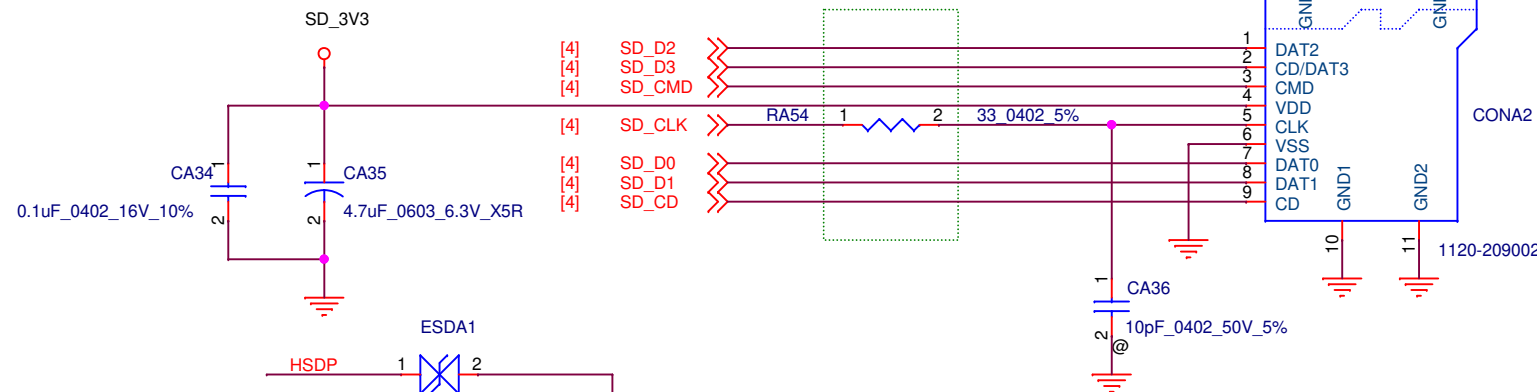
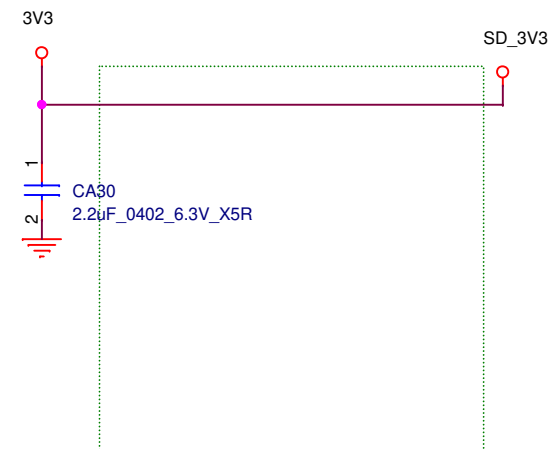
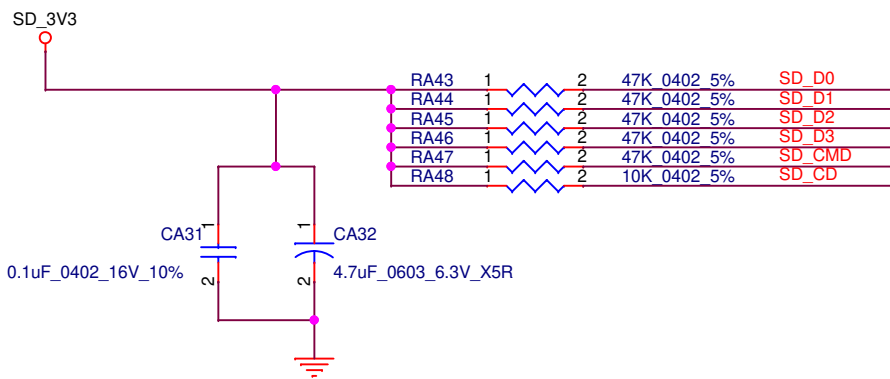
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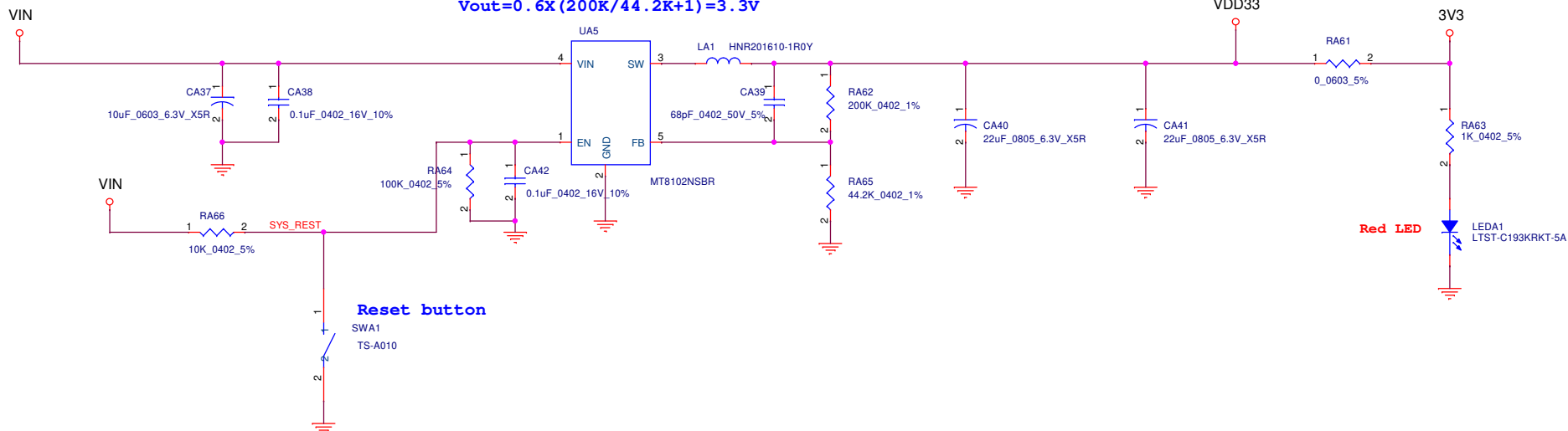


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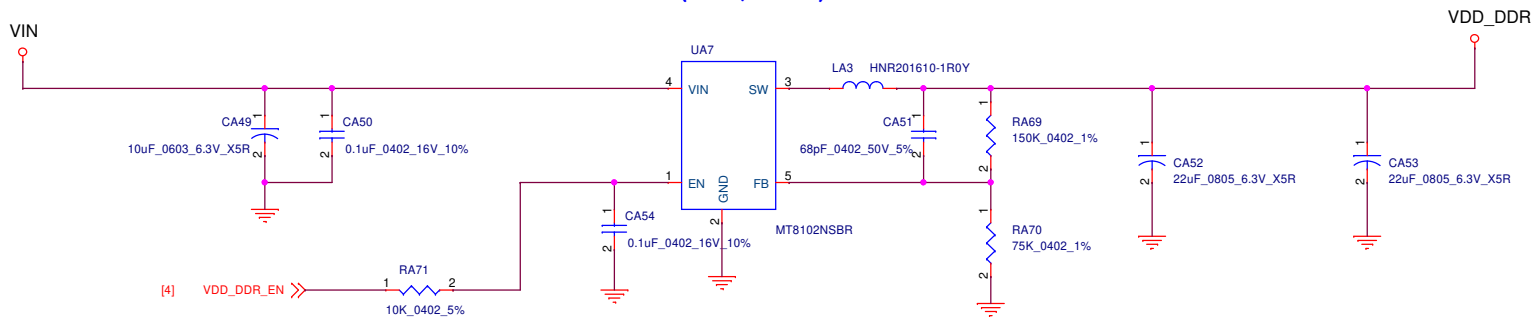
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5V --> 3.3V for low cost application
 $V_{out} = 0.6 \times (200K / 44.2K + 1) = 3.3V$



5V --> 1.8V for DDR2

$V_{out} = 0.6 \times (150K / 75K + 1) = 1.8V$



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Ameba PIR of EE

SCHEMATIC-REV	PCB-REV	DATE	SHEET	CONTENTS	REASON	REMARK
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