Mem_DMA_Mem

DMA transfers 8 bytes of data stored in FLASH memory to SRAM on CPU request

DMA

DMA
nrq

isr

LCD

Character LCD

Timer

Timer
tc
interrupt

BUS_CLK

24 MH   0

clock
reset

24-bit (UDB)
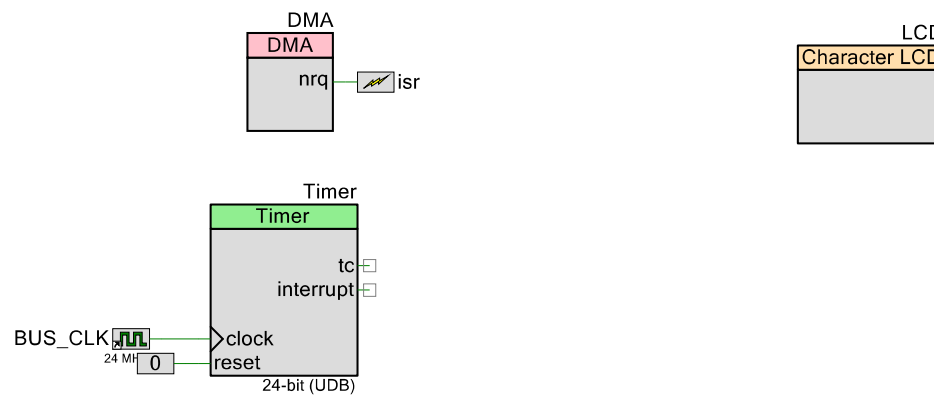
LCD to display the data received on the
SRAM. A sample display is as shown below

```
0 0 0 1 0 2 0 3 0 4 0 5 0 6 0 7
T R A N S F E R R E D
```