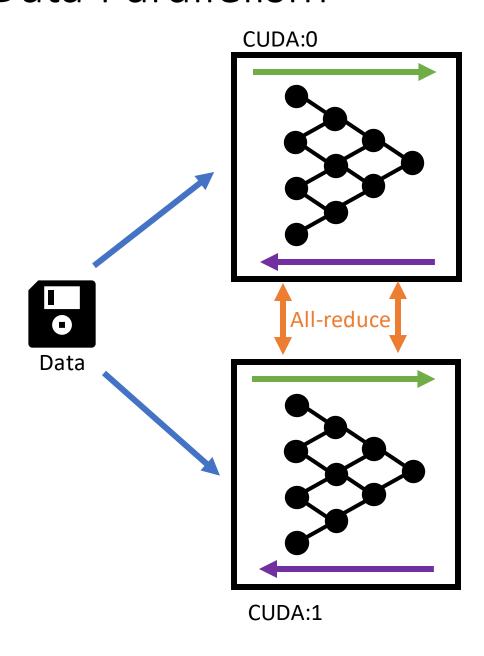




Data Parallelism

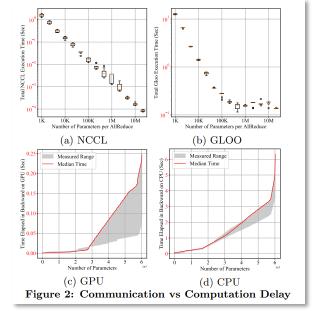


Models in sync

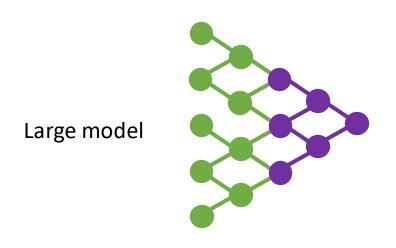
- 1. Partition data
- 2. Forward pass
- 3. Backward pass
- 4. All-reduce
- 5. Update gradients

```
import torch
import torch.nn as nn
import torch.nn.parallel as par
import torch.optim as optim
# initialize torch.distributed properly
# with init_process_group
# setup model and optimizer
net = nn.Linear(10, 10)
net = par.DistributedDataParallel(net)
opt = optim.SGD(net.parameters(), lr=0.01)
# run forward pass
inp = torch.randn(20, 10)
exp = torch.randn(20, 10)
out = net(inp)
# run backward pass
nn.MSELoss()(out, exp).backward()
# update parameters
opt.step()
```

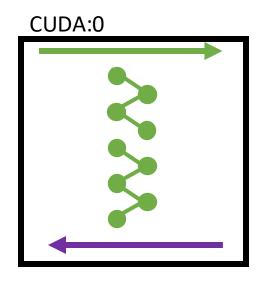
Gradient bucketing

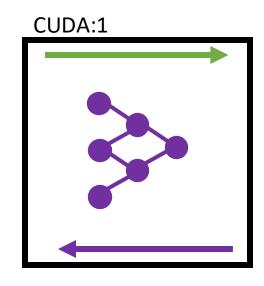


Model Parallelism









Naïve implementation very simple...

```
import torch.nn as nn
import torch.optim as optim

class ToyModel(nn.Module):
    def __init__(self):
        super(ToyModel, self).__init__()
        self.net1 = torch.nn.Linear(10, 10).to('cuda:0')
        self.relu = torch.nn.ReLU()
        self.net2 = torch.nn.Linear(10, 5).to('cuda:1')

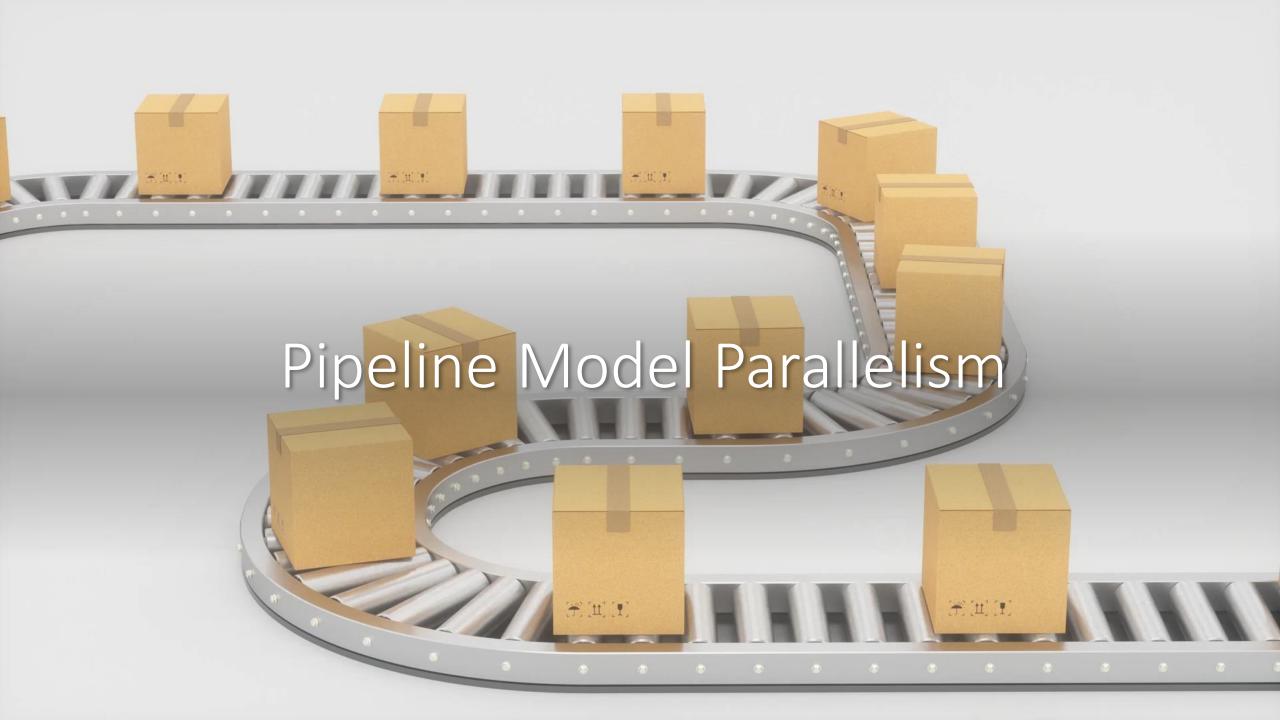
def forward(self, x):
        x = self.relu(self.net1(x.to('cuda:0')))
        return self.net2(x.to('cuda:1'))
```

labels = torch.randn(20, 5).to('cuda:1')

https://pytorch.org/tutorials/intermediate/model_parallel_tutorial.html

...very inefficient

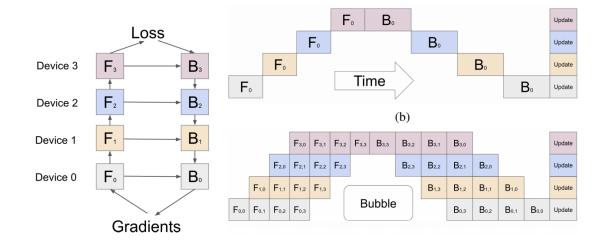




Pipeline Model Parallelism

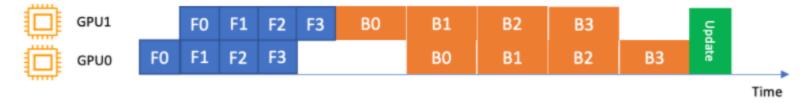
Pipeline model parallelism:

- Model has k partitions $\mu_1, ..., \mu_k$
- Break mini-batch D into m micro-batches D_i
- Let $F_{i,j}$ denote forward pass of D_j through μ_i
- Let $B_{i,j}$ denote the backward pass similarly Key upshot:
- $F_{*,j+1}$ can proceed **immediately** after $F_{*,j}$
- $B_{*,j-1}$ can proceed **immediately** after $B_{*,j}$

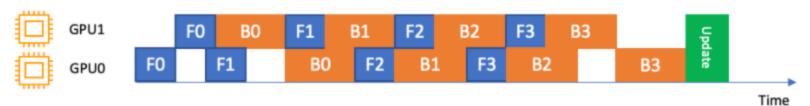


[] - GPipe: Easy Scaling with Micro-Batch Pipeline Parallelism – Huang et. al

Simple



Interleaved



Partitioning your model

TensorFlow

- Static computation graph
- tf.Operation
- Model == DAG of tf.Operations

PyTorch

- Dynamic computation graph
- nn.Module
- Model = Sequence of submodules, OR
- <u>Trace model</u> -> build graph of nn.Modules

Optimize for:

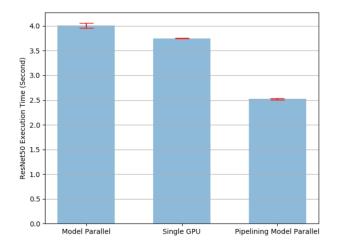
- Speed: min(Var(compute time))
- Memory: min(Var(num parameters))

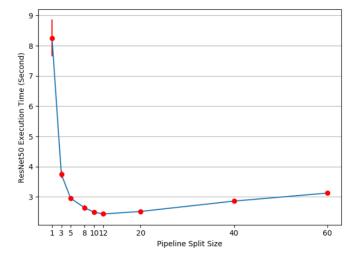
```
DIY
```

```
class ModelParallelResNet50(ResNet):
   def __init__(self, *args, **kwargs):
       super(ModelParallelResNet50, self). init (
           Bottleneck, [3, 4, 6, 3], num_classes=num_classes, *args, **kwargs)
       self.seg1 = nn.Seguential(
            self.conv1,
           self.bn1,
           self.relu,
           self.maxpool,
           self.layer1,
           self.layer2
       ).to('cuda:0')
       self.seq2 = nn.Sequential(
           self.layer3,
           self.layer4,
           self.avgpool,
       ).to('cuda:1')
       self.fc.to('cuda:1')
   def forward(self, x):
       x = self.seq2(self.seq1(x).to('cuda:1'))
       return self.fc(x.view(x.size(0), -1))
```

```
class PipelineParallelResNet50(ModelParallelResNet50):
    def __init__(self, split_size=20, *args, **kwargs):
        super(PipelineParallelResNet50, self).__init__(*args, **kwargs)
        self.split_size = split_size
    def forward(self, x):
        splits = iter(x.split(self.split_size, dim=0))
        s next = next(splits)
       s_prev = self.seq1(s_next).to('cuda:1')
        ret = []
        for s_next in splits:
            # A. s prev runs on cuda:1
            s_prev = self.seq2(s_prev)
            ret.append(self.fc(s prev.view(s prev.size(0), -1)))
            # B. s next runs on cuda:0, which can run concurrently with A
            s prev = self.seq1(s next).to('cuda:1')
        s prev = self.seq2(s prev)
       ret.append(self.fc(s_prev.view(s_prev.size(0), -1)))
        return torch.cat(ret)
```

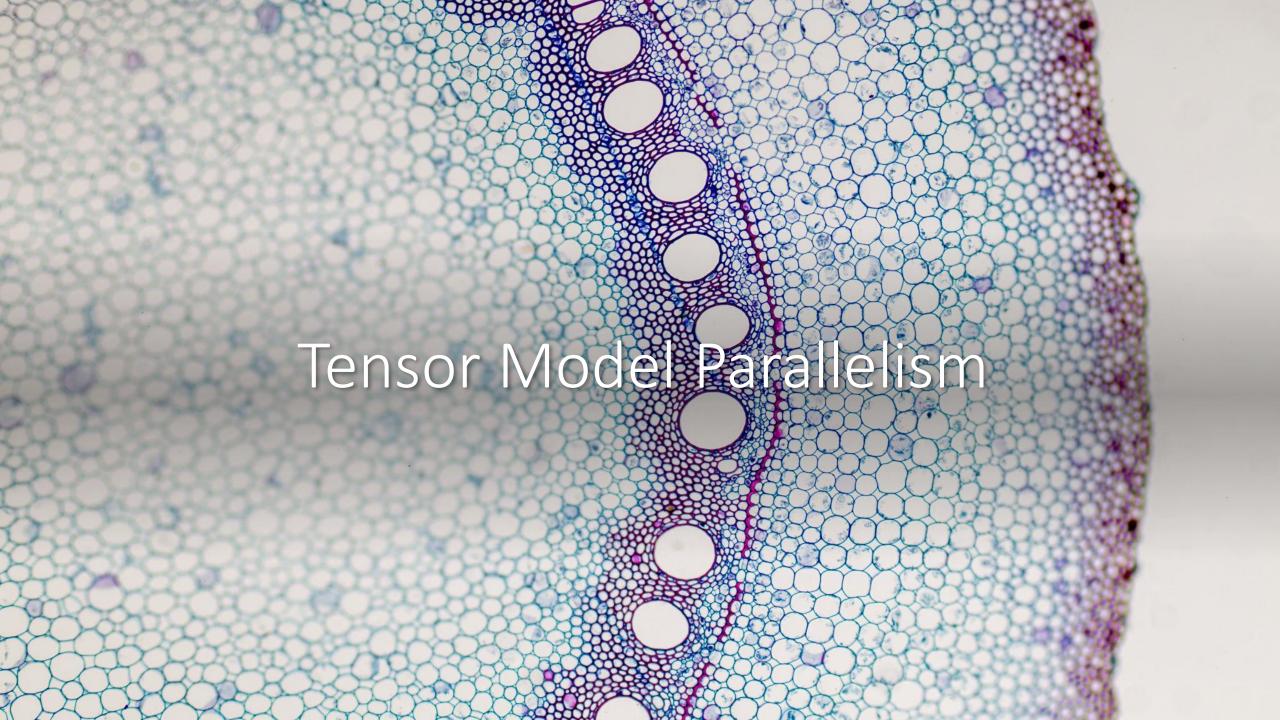
- Probably don't do this
- Use a framework /library





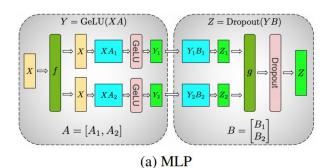
Frameworks

- <u>Lingvo</u>
 - TensorFlow only
 - Implements Gpipe
- Fairscale
 - PyTorch
 - Automatic splitting
 - Requires nn. Sequential
- SageMaker
 - TensorFlow and PyTorch
 - Automatic partitioning
 - Optimize for speed or memory
 - Simple/interleaved pipelines
 - Available in SageMaker Python SDK
 - Integrated with DDP
- Deepspeed
 - PyTorch
 - 3D-parallelism



Tensor Model Parallelism

- Another paradigm in model parallelism
- Split tensor between devices
- Architecture specific and nuanced
- Can reduce the effective batch size to be less than 1 per GPU
- E.g. Megatron-LM from NVIDIA []



Y = Self-Attention(X) Z = Dropout(YB) $X \Rightarrow V_1$ $X \Rightarrow V_1$ $X \Rightarrow V_1$ $X \Rightarrow V_2$ $Y_2B_2 \Rightarrow Z_2 \Rightarrow$

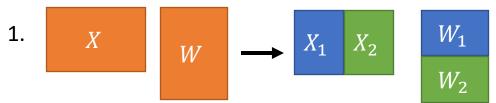
(b) Self-Attention

Example

Consider the operation $Z = f(X \cdot W)$ where:

- $X \in \mathbb{R}^{n \times m}$, $W \in \mathbb{R}^{m \times l}$
- f non-linearity e.g. ReLU

Consider two ways to parallelize:



Split *X* along columns, *W* along rows:

$$X = [X_1, X_2], W = \begin{bmatrix} W_1 \\ W_2 \end{bmatrix}$$

Then

$$Z = f(X_1W_1 + X_2W_2) \neq f(X_1W_1) + f(X_2W_2)$$

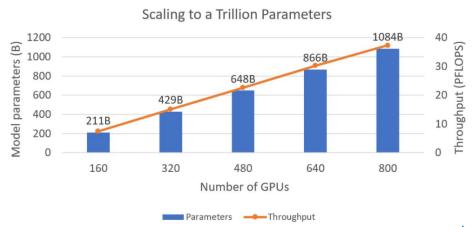
Split *W* along its columns

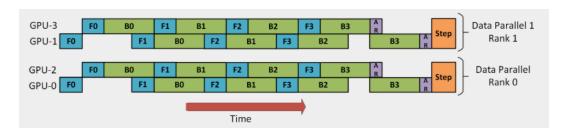
$$[Z_1, Z_2] = [f(XW_1), f(XW_2)]$$

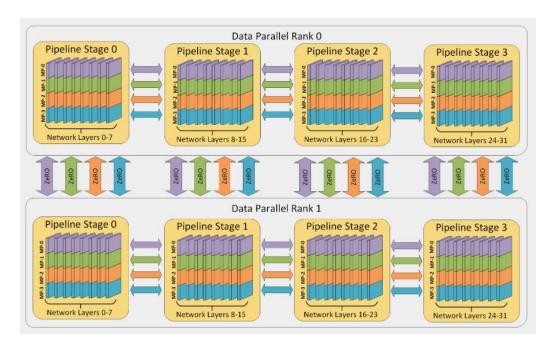
 \Rightarrow f can be applied on each GPU



- Hybrid data and model parallelism
- 3D-parallelism:
 - Data parallel
 - Pipeline model parallel
 - Tensor model parallel
- ZeRO:
 - Memory optimization techniques
- Train models with > 1 trillion parameters







https://www.deepspeed.ai/tutorials/pipeline/

https://www.microsoft.com/en-us/research/blog/deepspeed-extreme-scale-model-training-for-everyone/

Announcements

- Azureml-examples: deepspeed + transformers
 - Let me know if you're interested!
- ERL2 library → deepspeed trainer w/ Han
- More goodness:
 - 1-bit adam
 - Sparse attention
 - FP16
- Resources:
 - https://www.deepspeed.ai/
 - https://github.com/microsoft/DeepSpeed
 - <u>azureml-examples/deepspeed</u>
 - Example on azureml