

# Future of GPUs in Energy Efficient High Performance Computing

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**Abstract**—Data processing and computation requirements are getting bigger every day. Along with CPUs and FPGAs, GPUs have come to the play in the recent years. GPUs are usually able to produce high throughput with the option of being energy efficient. Unlike CPUs, GPUs are needed to be more efficient in terms of power consumption to be considered as an accelerator in HPC systems. **MODIFICATION NEEDED!!**

**Keywords**—High Performance Computing. GPUs in HPC, Energy Efficiency, HPC.

## I. INTRODUCTION

Since computation has entered into the exascale era, the main concern for now is how to minimize the power consumption of the components used. Numerous hardware and software based methodologies have been used or currently are being researched in this regard. GPUs are comparatively newer than the other accelerators but making them energy efficient is one of the main goals for the researchers. Because of having high computational power they are being used extensively in HPC systems. In fact many of the top most supercomputers are using GPUs along with CPUs to achieve more power in processing and computation. Machines built with CPU-GPUs consume a lot of power but it needs to be mitigated because of green computing. Lighten up the power consumption of GPUs has not reached it's peak yet unlike the CPUs. The terms energy and power are very much related as energy is the product of power and time. Power consumption of a system depends on the center architecture and system goals and this is applicable for all the accelerators. At the same time power wastage should be minimalized.

This paper — gives insights about the contribution of GPUs in HPC along with the current trends. This also highlights some comparisons among the accelerators and some related research works. Potential techniques to achieve energy efficiency for GPUs and possible future works in this regard.

## II. MOTIVATION AND BACKGROUND

### A. Unterkapitel

blabla mit drei Quellenangaben [1]–[3]

Tabelle I

BEISPIELTABELLE

Spalte1	Spalte2
0	1

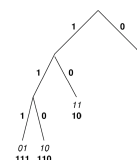


Abbildung 1. Baum

## III. ZUSAMMENFASSUNG UND AUSBLICK

blabla

### LITERATUR

- [1] B. Claise, "IPFIX protocol specifications," Internet-Draft, draft-ietf-ipfix-protocol-07, December 2004.
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