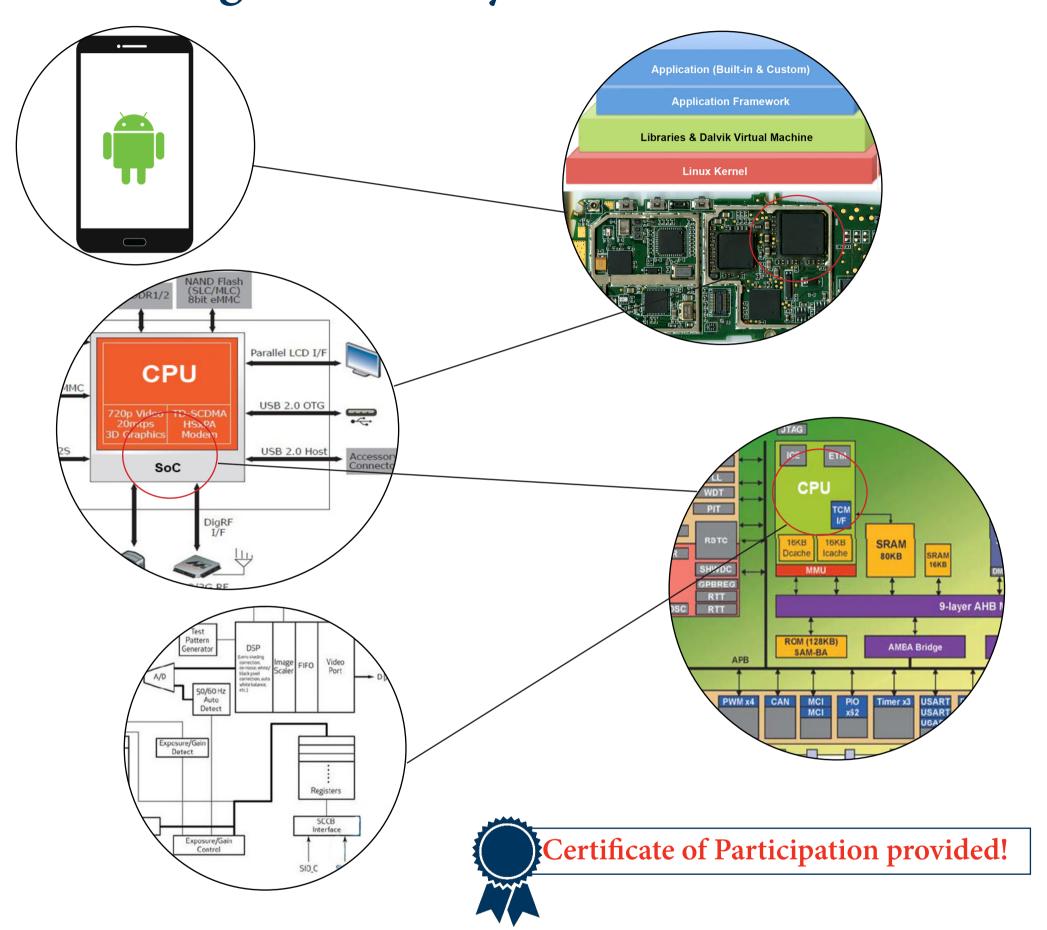


Hardware Verification Summer Course



I Challenge You Verify These!



Course Content

- Hardware Verification Process
- Functional Verification Metrics
- Verification Environment Design
- Stimuli and Scenario Design
- Hands-On Labs

Requirements

- Passion for technology
- Object Oriented Programming
- Digital Circuits Design (aka CID)
- Verilog / VHDL Knowledge