

























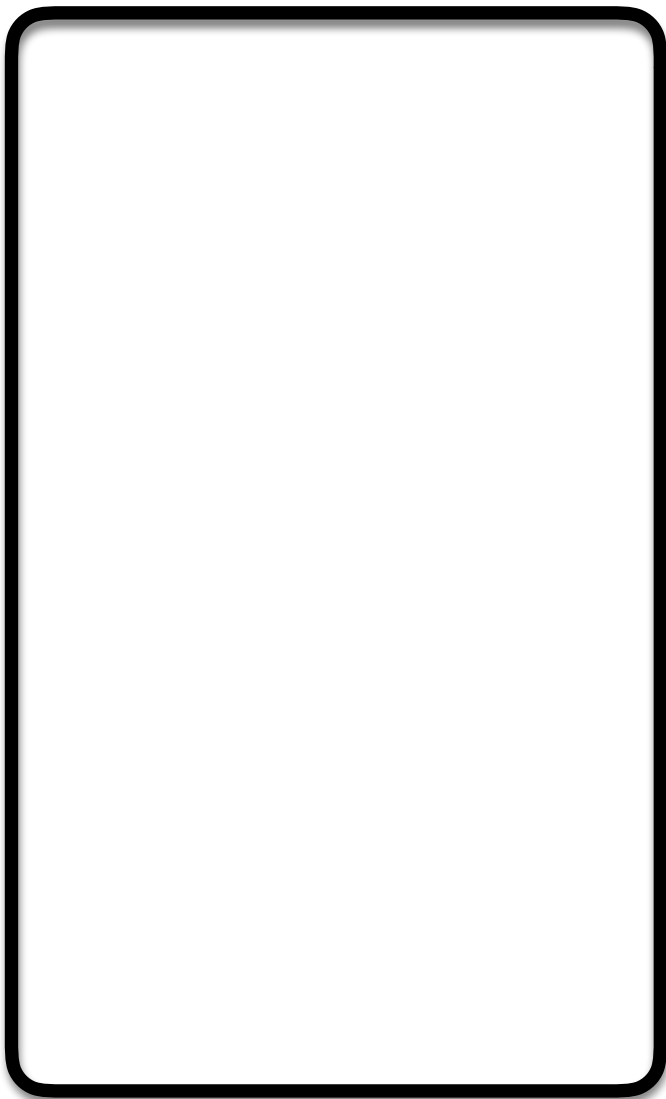
...

cnv

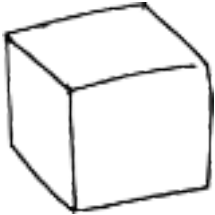
Botch **Norm**

CONV

BatchNorm



















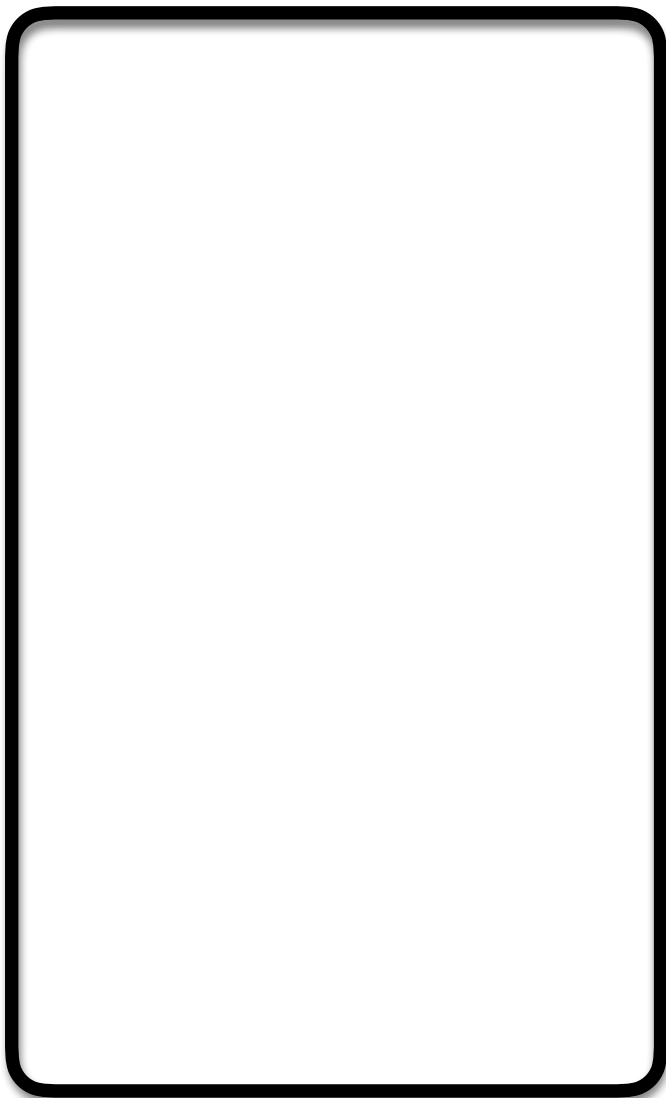








ReLU

























...

ReLU

Input

output

Input

output

Input

output

Feedback module lengths

S







































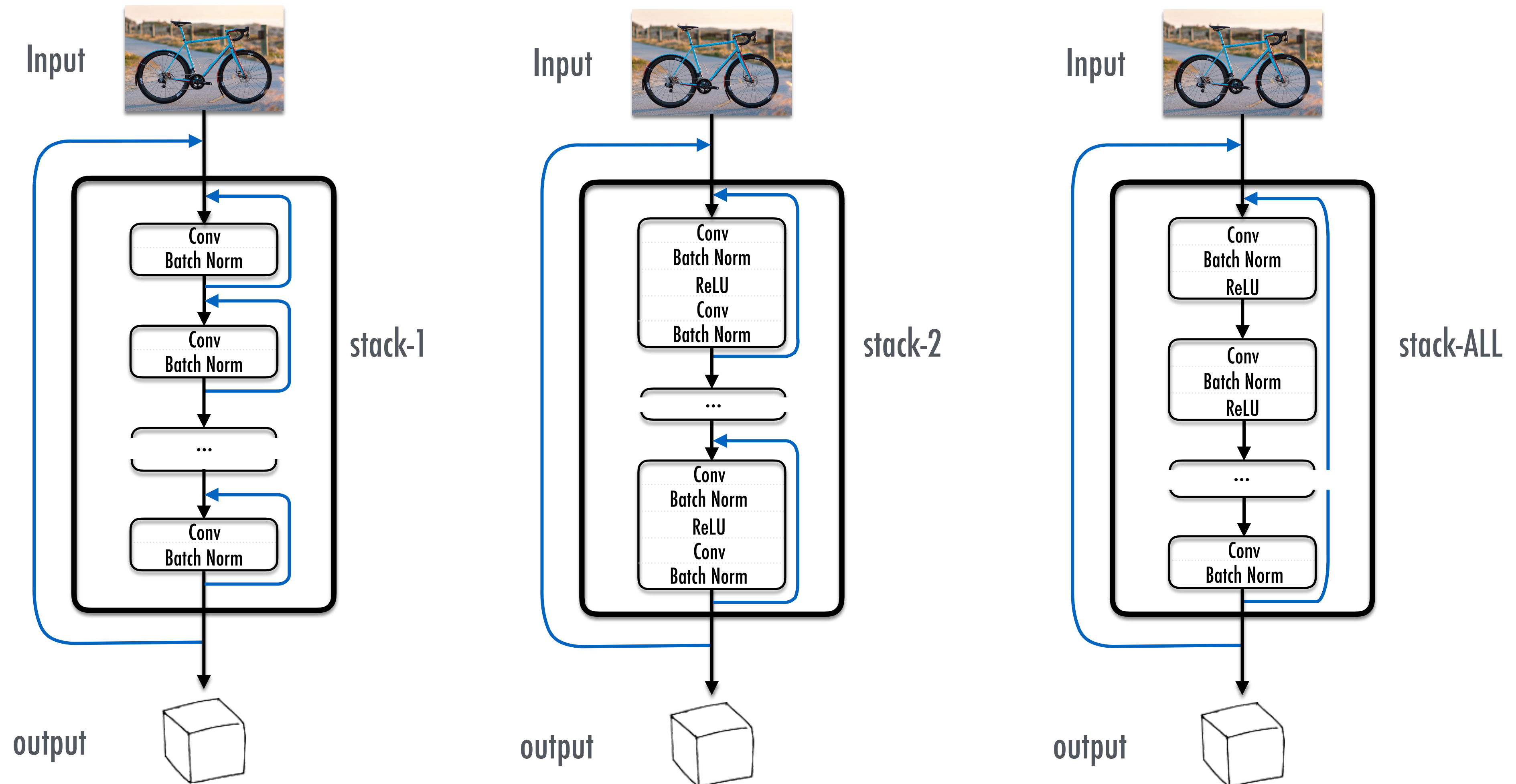


stack-1

stack-2

stock-A-ALL

Some additional aspects



Feedback module lengths

Some additional aspects