

Input

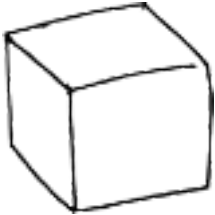






output





output

R





U















d

U





d















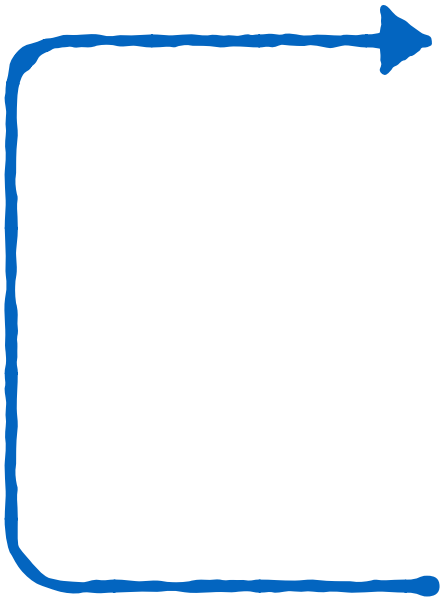














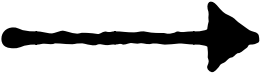




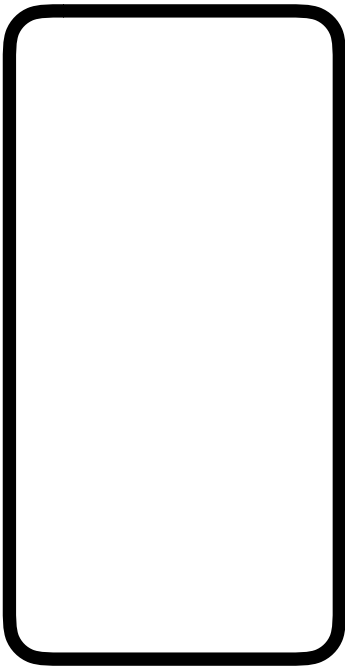


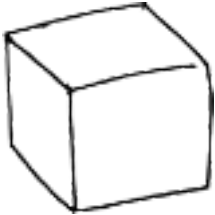












Recurrent module design choices

Input













output





