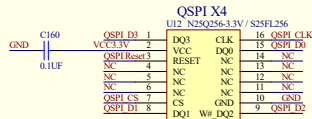
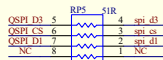
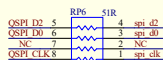
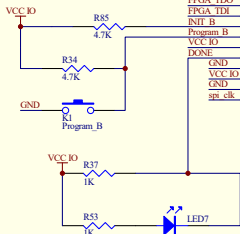


JTAG



configer  
BANK0 CONFIG

GND	R11	DXN_0
1.8V	MI12	VCCADC_0
GND	MI1	GNDADC_0
GND	R12	DXP_0
GND	N11	VREFN_0
GND	P12	VREFP_0
GND	N12	VP_0
GND	P11	VN_0
1.8V	E8	VCCBATT_0
FPGA TCK	L8	TCK_0
FPGA TMS	N8	TMS_0
FPGA TDO	R7	TDO_0
FPGA TDI	R6	TDI_0
INIT_B	G7	INIT_B_0
Program_B	P6	PROGRAM_B_0
VCC_IO	P7	PROGRAM_B_0
DONE	J7	CFGBVS_0
GND	P5	DONE_0
VCC_IO	Y5	M2_0
GND	T2	M0_0
spi_clk	C8	M1_0
		CCLK_0



Title		
Size A4	Number	Revision
Date: 28-Jun-2021	Sheet of	
File: F:\adK325 676V2XC\7K3251原理图\XC7K3251B原理图.ddb	Revision	

U1

KC/K32516/6 BANK16

B-TXD2	H9	IO L1P T0	IO L13P T2 MRCC	C12	A-RX CLK
B-MDX	H8	IO L1N T0	IO L13N T2 MRCC	C11	A-IXD6
B-TXD6	G10	IO L2N T0	IO L14P T2 SRCC	E11	A-IXD1
B-MDIO	G9	IO L2P T0	IO L14N T2 SRCC	D11	A-RESET
A-RXD2	J13	IO L3P T0 DQS	IO L15P T2 DQS	F14	A-RXER
A-RXD3	H13	IO L3N T0 DQS	IO L15N T2 DQ	F13	A-GTX CLK
B-TXD1	J11	IO L4P T0	IO L16P T2	G12	A-IXD0
B-TXD3	J10	IO L4N T0	IO L16N T2	F12	A-IX EN
A-RXD0	H14	IO L5P T0	IO L17P T2	D14	A-IXD4
A-RX DV	H14	IO L5N T0	IO L17N T2	D13	A-IXD7
B-TXD4	G12	IO L6P T0	IO L18P T2	E13	A-IXER
B-TXD0	H11	IO L6N T0 VREF	IO L18N T2	C14	A-IXD3
B-TXD7	F9	IO L7P T1	IO L19P T3	C13	A-IXD5
B-TX EN	F8	IO L7N T1	IO L19N T3 VREF	E12	A-IX CLK
B-TXER	D9	IO L8P T1	IO L20P T3	B12	B-RXD1
B-GTX CLK	D8	IO L8N T1	IO L20N T3	B11	B-RXD2
A-RXD5	A9	IO L9P T1 DQS	IO L21P T3 DQS	B14	A-RXD5
B-RXD7	A8	IO L9N T1 DQS	IO L21N T3 DQS	A14	A-RXD7
B-TX CLK	C5	IO L10P T1	IO L22P T3	B10	B-RXD4
B-RXD6	B9	IO L10N T1	IO L22N T3	A10	B-RXD3
A-TXD2	G11	IO L11P T1 SRCC	IO L23P T3	B15	A-RXD4
B-TXD5	F10	IO L11N T1 SRCC	IO L23N T3	A15	A-RXD5
B-RX CLK	E10	IO L12P T1 MRCC	IO L24P T3	A13	B-RXD0
B-RXER	D10	IO L12N T1 MRCC	IO L24N T3	A12	B-RX DV
B-RESET	J8	IO 0_16	IO 25_16	J14	A-RXD1

spi\_d0 B24  
spi\_d1 A25  
spi\_d3 A22  
PIN B25 B25  
PIN B26 B26  
PIN A23 A23  
PIN A24 A24  
PIN D26 D26  
PIN C26 C26  
spi\_cs C23  
key3 C24  
PIN D21 D21  
PIN C22 C22  
PIN B20 B20  
PIN A20 A20  
PIN E21 E21  
PIN E22 E22  
PIN C21 C21  
PIN B21 B21  
PIN D23 D23  
PIN D24 D24  
PIN E22 E22  
PIN E23 E23  
FPGA HDMI\_SCL K21

U1

KC/K32516/6 BANK14

IO L1P T0 DQ0 MOSI					
IO L1N T0 DQ0 D0N					
IO L2P T0 DQ2	IO L13P T2 MRCC	G22	PIN G22		
IO L2N T0 DQ3	IO L13N T2 MRCC	P23	PIN P23		
IO L3P T0 DQS PUDC_B	IO L14P T2 SRCC	G24	PIN G24		
IO L3N T0 DQS EMCCLK	IO L14N T2 SRCC	F24	PIN F24		
IO L4P T0 DQ4	IO L15P T2 DQS RDWR_B	E25	PIN E25		
IO L4N T0 DQ5	IO L15N T2 DQS DOUT CS0_B	D25	PIN D25		
IO L5P T0 DQ6	IO L16P T2 CSI_B	G25	PIN G25		
IO L5N T0 DQ7	IO L16N T2 A15 D31	F25	PIN F25		
IO L6P T0 FCS_B	IO L17P T2 A14 D30	H26	PIN H26		
IO L6N T0 DQ8 VREF	IO L17N T2 A13 D29	E26	PIN E26		
IO L7P T1 DQ9	IO L18P T2 A12 D28	J26	PIN J26		
IO L7N T1 D10	IO L18N T2 A11 D27	H21	PIN H21		
IO L8P T1 D11	IO L19P T3 A10 D26	G21	PIN G21		
IO L8N T1 D12	IO L19N T3 A09 D25 VREF	J21	PIN J21		
IO L9P T1 DQ8	IO L20P T3 A08 D24	H23	PIN H23		
IO L9N T1 DQ8 D13	IO L20N T3 A07 D23	H24	PIN H24		
IO L10P T1 D14	IO L21P T3 DQS	J21	PIN J21		
IO L10N T1 D15	IO L21N T3 DQS A06 D22	H22	PIN H22		
IO L11P T1 SRCC	IO L22P T3 A05 D21	J24	PIN J24		
IO L11N T1 SRCC	IO L22N T3 A04 D20	J25	PIN J25		
IO L12P T1 MRCC	IO L23P T3 A03 D19	K22	PIN K22		
IO L12N T1 MRCC	IO L23N T3 A02 D18	K23	PIN K23		
IO 0_14	IO L24P T3 A01 D17	J23	PIN J23		
	IO L24N T3 A00 D16	L23	PIN L23		
	IO 25_14				

U1

KC/K32516/6 BANK13

PIN C16	C16	IO L1P T0 AD0P	IO L13P T2 MRCC	E18	PIN E18
PIN B16	B16	IO L1N T0 AD0N	IO L13N T2 MRCC	D18	PIN D18
PIN A18	A18	IO L2P T0 AD8P	IO L14P T2 SRCC	H17	PIN H17
PIN A19	A19	IO L2N T0 AD8N	IO L14N T2 SRCC	H18	PIN H18
PIN B17	B17	IO L3P T0 DQS AD1P	IO L15P T2 DQS	D20	PIN D20
PIN A17	A17	IO L3N T0 DQS AD1N	IO L15N T2 DQS ADV_B	G19	PIN G19
PIN C19	C19	IO L4P T0 AD0P	IO L16P T2 A26	F20	PIN F20
PIN B19	B19	IO L4N T0 AD0N	IO L16N T2 A27	F19	PIN F19
PIN C17	C17	IO L5P T0 AD2P	IO L17P T2 A26	E20	PIN E20
PIN C18	C18	IO L5N T0 AD2N	IO L17N T2 A25	H19	PIN H19
PIN D15	D15	IO L6P T0	IO L18P T2 A24	G20	PIN G20
PIN D16	D16	IO L6N T0 VREF	IO L18N T2 A23	K20	PIN K20
PIN H16	H16	IO L7P T1 AD10P	IO L19P T3	J20	PIN J20
PIN G16	G16	IO L7N T1 AD10N	IO L19N T3 A21 VREF	J18	PIN J18
PIN G15	G15	IO L8P T1 AD3P	IO L20P T3 A20	J19	PIN J19
PIN F15	F15	IO L8N T1 AD3N	IO L20N T3 A19	L19	PIN L19
PIN FJ15	J15	IO L9P T1 DQS AD11P	IO L21P T3 DQS	L20	PIN L20
PIN FJ16	J16	IO L9N T1 DQS AD11N	IO L21N T3 DQS A18	K17	PIN K17
PIN E16	E16	IO L10P T1 AD4P	IO L22P T3 A17	M17	PIN M17
PIN G17	G17	IO L10N T1 AD4N	IO L22N T3 A16	L18	PIN L18
PIN F18	F18	IO L11P T1 SRCC AD12P	IO L23P T3 FWE_B	L17	PIN L17
CLK100M	F17	IO L11N T1 SRCC AD12N	IO L23N T3 FWE_B	K18	PIN K18
PIN E17	E17	IO L12P T1 MRCC AD5P	IO L24P T3 RS1	M16	A-MDC
A-MDIO	K15	IO L12N T1 MRCC AD5N	IO L24N T3 RS0		
		IO 0_15	IO 25_15		

Title			
Size	Number		Revision
A4			
Date:	28-Jun-2021		Sheet of
File:	F:\andK325 6/6V2XC/K3251原理图\图		10
	F:\andK325 6/6V2XC/K3251原理图\图		10





		2		3		4

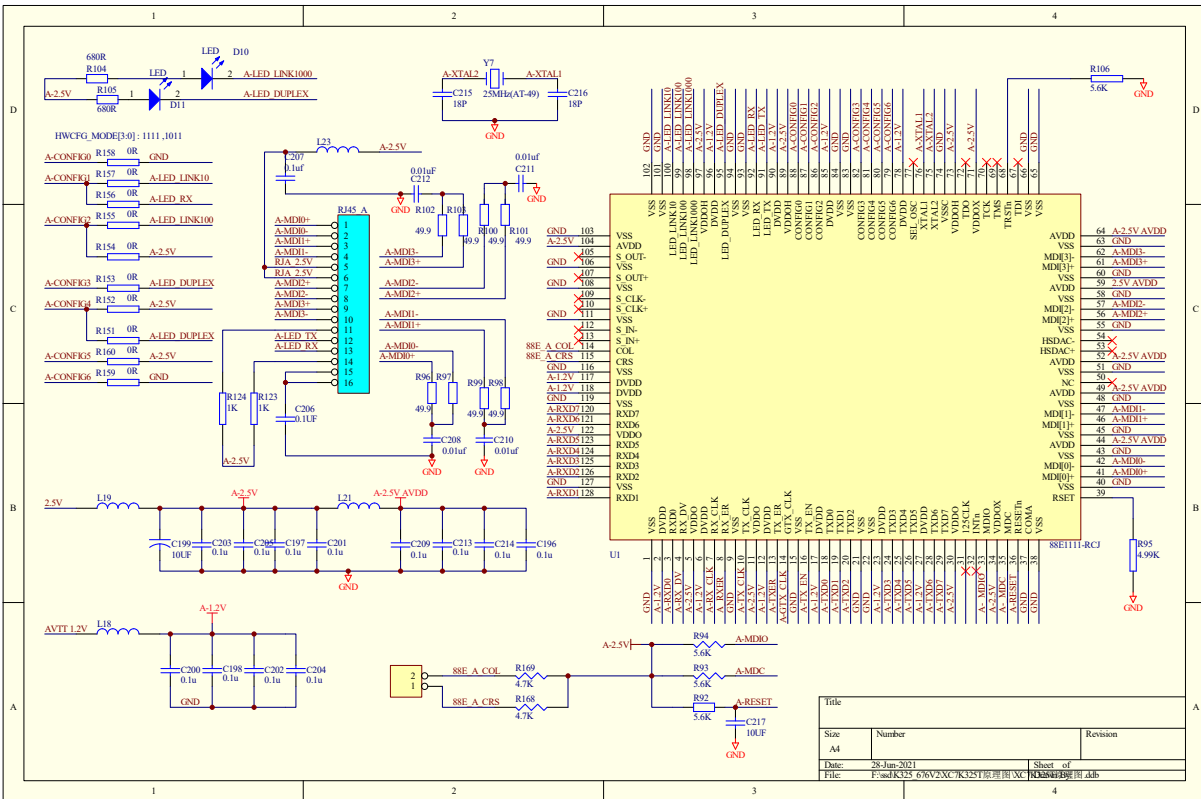
Title		
Size A4	Number	Revision
Date: 28-Jun-2021	Sheet of	
File: F:\sdK325 676V2\XC7K325T原理图\XC7K325T原理图.adb	10/24/2021	

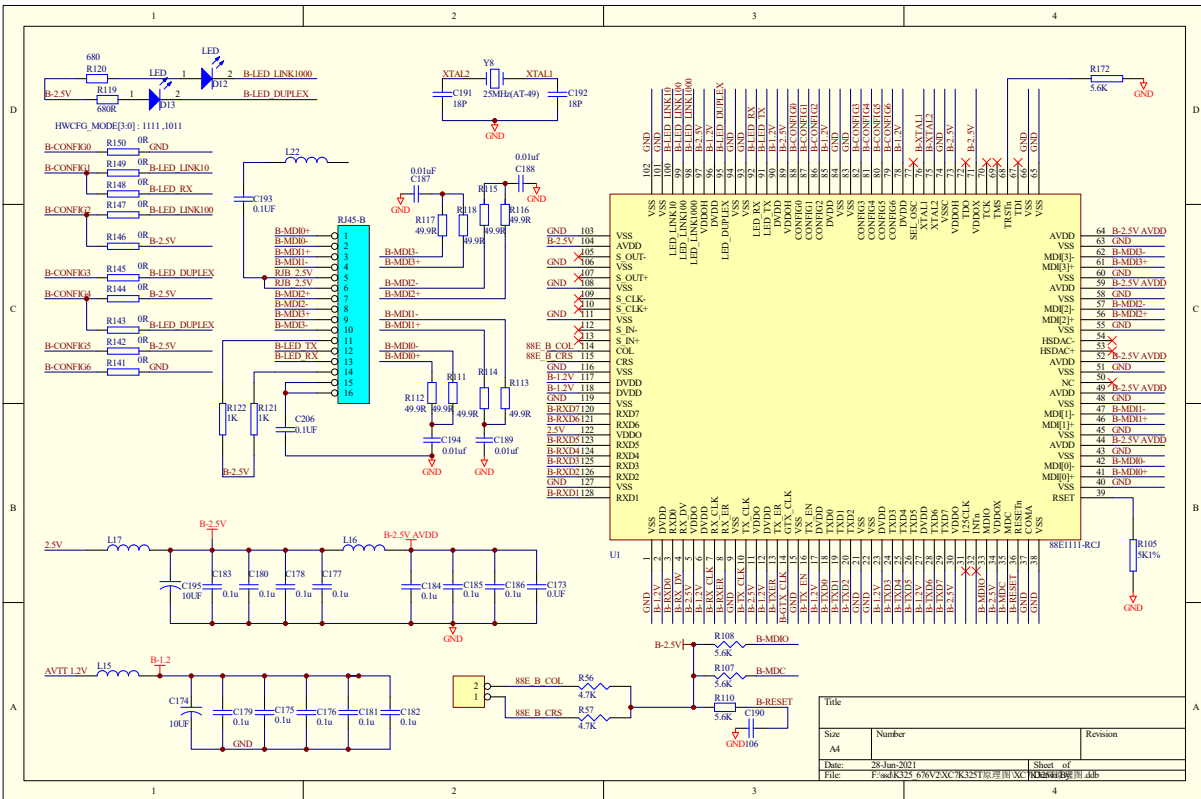
U1		
XC7K325T676 MGT BANK115		
slp_A tx_P	H2	MGTXTXP3_115
slp_A tx_n	H1	MGTXTXN3_115
slp_A rx_P	J4	MGTXRXN3_115
slp_A rx_n	J3	MGTXRXN3_115
slp_B tx_P	K2	MGTXRXN3_115
slp_B tx_n	K1	MGTXTXN2_115
slp_B rx_P	L4	MGTXRXN2_115
slp_B rx_n	L3	MGTXRXN2_115
B-SATA TX_P	M2	MGTXTXP1_115
B-SATA TX_N	M1	MGTXTXN1_115
B-SATA RX_P	N4	MGTXRXN1_115
B-SATA RX_N	N3	MGTXRXN1_115
A-SATA TX_P	P2	MGTXTXP0_115
A-SATA TX_N	P1	MGTXTXN0_115
A-SATA RX_P	R4	MGTXRXN0_115
A-SATA RX_N	R3	MGTXRXN0_115
CLK_N	K6	MGTRFCLKIP_115
CLK_P	K5	MGTRFCLKIN_115
pcie ck+	H6	MGTRFCLKOP_115
pcie ck-	H5	MGTRFCLKON_115

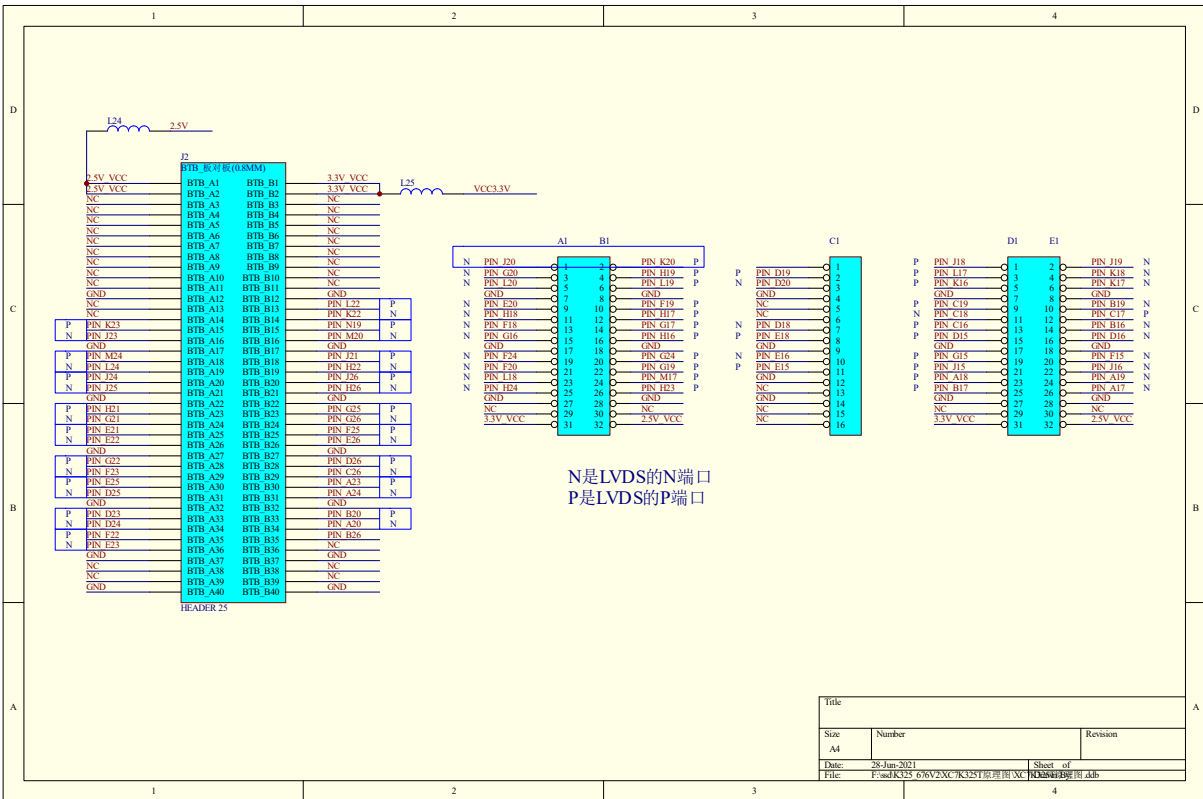
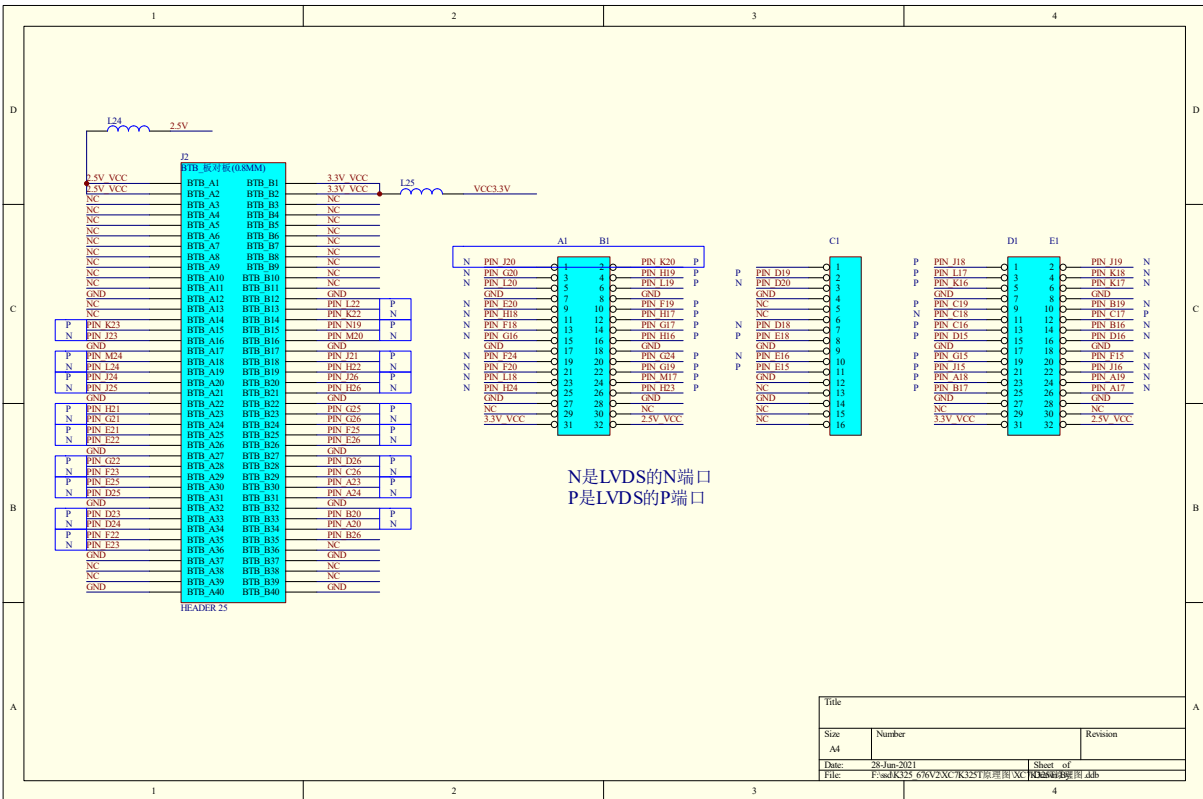
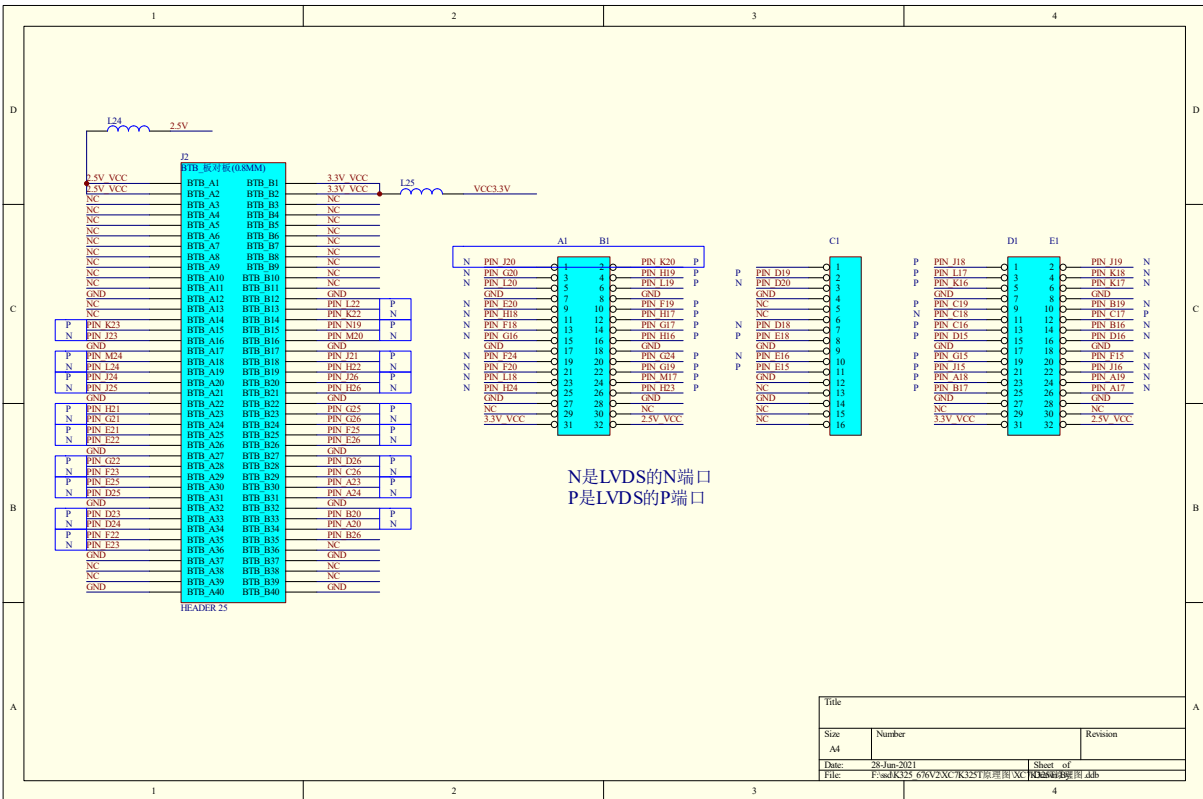
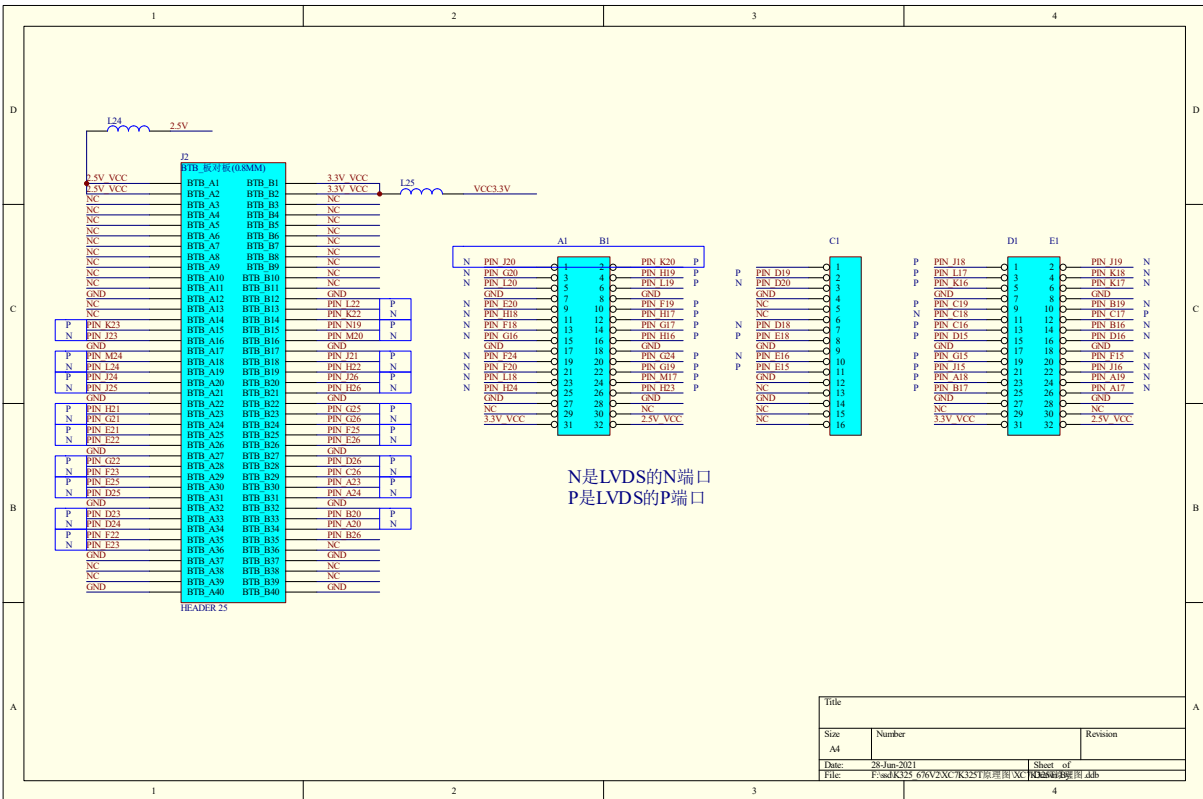
这一路时钟默认是没有输入的。

U1		
XC7K325T676 MGT BANK116		
pcie tx_p0	A4	MGTXTXP3_116
pcie tx_n0	A3	MGTXTXN3_116
pcie rx_p0	B6	MGTXRXN3_116
pcie rx_n0	B5	MGTXRXN3_116
pcie tx_p1	B2	MGTXTXP2_116
pcie tx_n1	B1	MGTXTXN2_116
pcie rx_p1	C4	MGTXRXN2_116
pcie rx_n1	C3	MGTXRXN2_116
pcie tx_p2	D2	MGTXTXP1_116
pcie tx_n2	D1	MGTXTXN1_116
pcie rx_p2	E4	MGTXRXN1_116
pcie rx_n2	E3	MGTXRXN1_116
pcie tx_p3	F2	MGTXTXP0_116
pcie tx_n3	F1	MGTXTXN0_116
pcie rx_p3	G4	MGTXRXN0_116
pcie rx_n3	G3	MGTXRXN0_116
CLK150_P	F6	MGTRFCLKIP_116
CLK150_N	F5	MGTRFCLKIN_116
CLK156.25_P	D6	MGTRFCLKOP_116
CLK156.25_N	D5	MGTRFCLKON_116

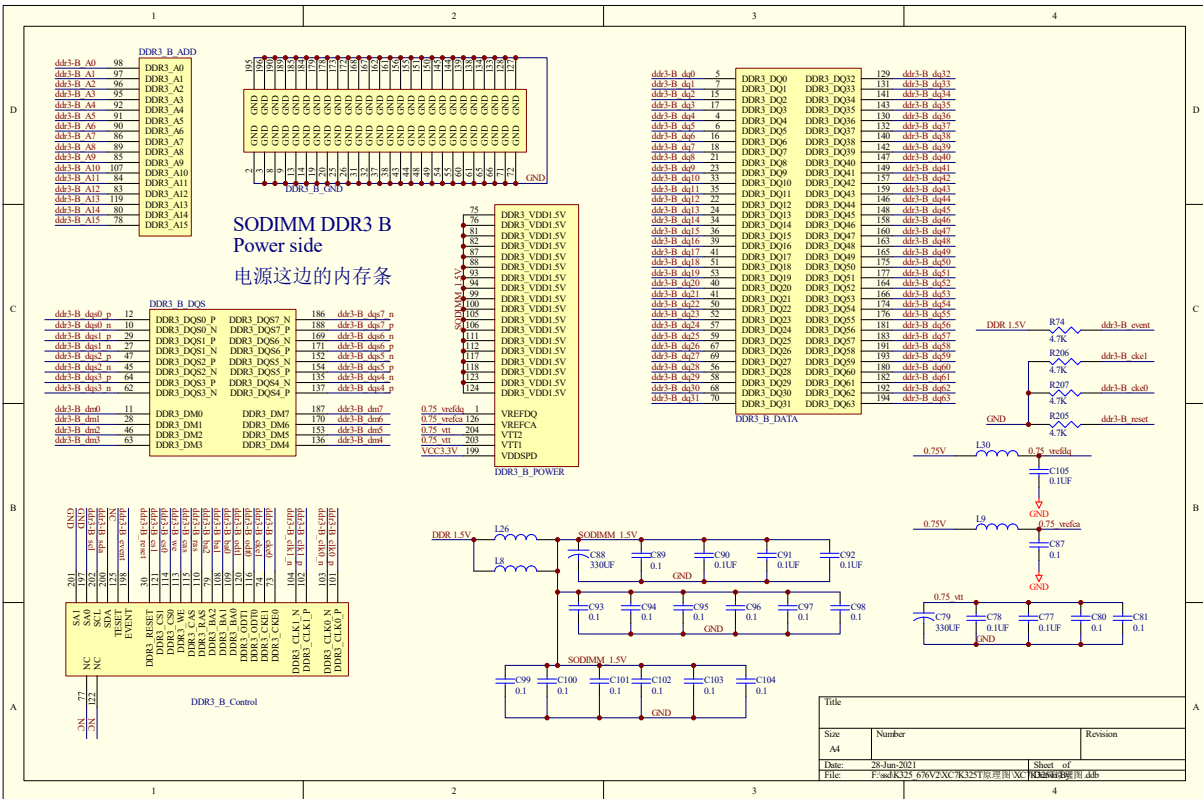
Title		
Size	Number	Revision
A4		
Date:	28-Jun-2021	Sheet of
File:	F:\adK325 676V2\XC7K325T原理图\XC7K325T676原理图.adb	











1

2

3

4

H  
FMC\_LPC\_H

H1	VREF_A_M2C
H2	PRSTNT_M2C_L
H3	GND
H4	GND
H5	CLK0_M2C_P
H6	CLK0_M2C_N
H7	GND
H8	LA00_P
H9	LA02_N
H10	LA04_P
H11	LA04_N
H12	LA07_P
H13	LA07_N
H14	LA07_N
H15	GND
H16	LA11_P
H17	LA11_N
H18	GND
H19	LA15_P
H20	LA15_N
H21	GND
H22	LA19_P
H23	LA19_N
H24	GND
H25	LA21_P
H26	LA21_N
H27	GND
H28	LA24_P
H29	LA24_N
H30	GND
H31	LA29_P
H32	LA29_N
H33	GND
H34	LA30_P
H35	LA30_N
H36	GND
H37	LA33_P
H38	LA33_N
H39	GND
H40	VADJ

G  
FMC\_LPC\_G

G1	GND
G2	PIN_Y23
G3	PIN_AA24
G4	GND
G5	GND
G6	PIN_AA25
G7	PIN_AB25
G8	GND
G9	PIN_U24
G10	PIN_U25
G11	GND
G12	PIN_T24
G13	PIN_T25
G14	GND
G15	PIN_R22
G16	PIN_R23
G17	GND
G18	PIN_R25
G19	PIN_P25
G20	GND
G21	PIN_P24
G22	PIN_N24
G23	GND
G24	PIN_P23
G25	PIN_N23
G26	GND
G27	PIN_V21
G28	PIN_W21
G29	GND
G30	PIN_R16
G31	PIN_R17
G32	GND
G33	PIN_P16
G34	PIN_N17
G35	GND
G36	PIN_T22
G37	PIN_T23
G38	GND
G39	VADJ
G40	GND

D  
FMC\_LPC\_D

D1	PG_C2M
D2	GND
D3	GND
D4	GTCLK0_M2C_P
D5	GTCLK0_M2C_N
D6	GND
D7	GND
D8	LA01_P_CC
D9	LA01_N_CC
D10	GND
D11	LA05_P
D12	LA05_N
D13	GND
D14	LA09_P
D15	LA09_N
D16	GND
D17	LA13_P
D18	LA13_N
D19	GND
D20	LA17_P_CC
D21	LA17_N_CC
D22	GND
D23	LA23_P
D24	LA23_N
D25	GND
D26	LA26_P
D27	LA26_N
D28	GND
D29	TCK
D30	TDI
D31	TDO
D32	3.3V_AUX
D33	TMS
D34	TRST_L
D35	GND
D36	3.3V
D37	GND
D38	3.3V
D39	GND
D40	3.3V

C  
FMC\_LPC\_C

C1	GND
C2	GND
C3	DP0_C2M_P
C4	DP0_C2M_N
C5	GND
C6	DP0_M2C_P
C7	DP0_M2C_N
C8	GND
C9	GND
C10	LA06_P
C11	LA06_N
C12	GND
C13	GND
C14	LA10_P
C15	LA10_N
C16	GND
C17	GND
C18	LA14_P
C19	LA14_N
C20	GND
C21	GND
C22	LA18_P_CC
C23	LA18_N_CC
C24	GND
C25	GND
C26	LA27_P
C27	LA27_N
C28	GND
C29	GND
C30	SCL
C31	SDA
C32	GND
C33	GND
C34	GA0
C35	12V
C36	GND
C37	12V
C38	GND
C39	3.3V
C40	3.3V

PIN\_U19  
PIN\_U20

C228  
22UF

GND

C229  
22UF

Title

Size

A4

Number

Revision

Date:

28-Jun-2021

Sheet of

File: F:\adK325\_6\6V2\XC7K325T原理图.ddb

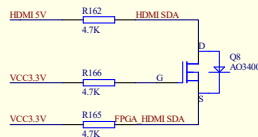
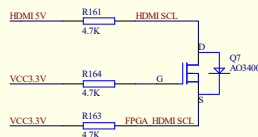
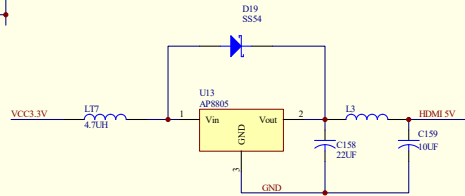
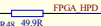
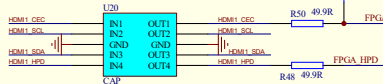
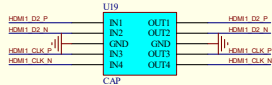
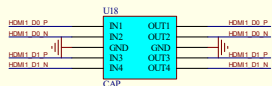
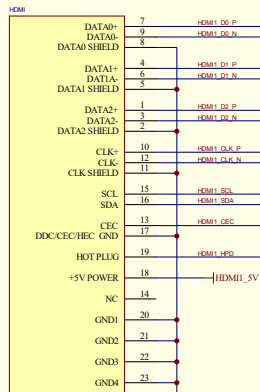
1

2

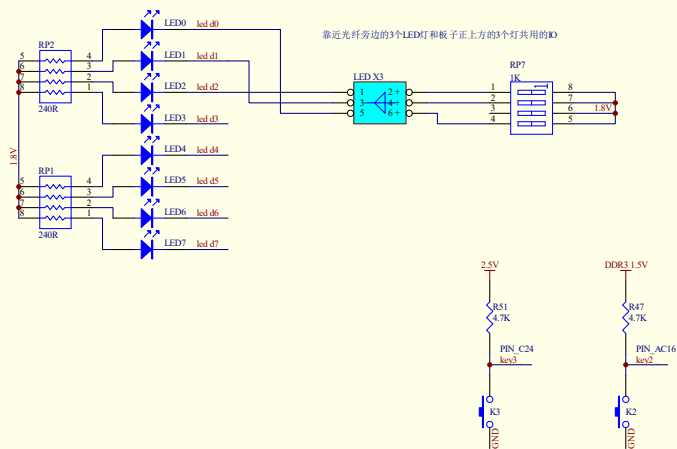
3

4

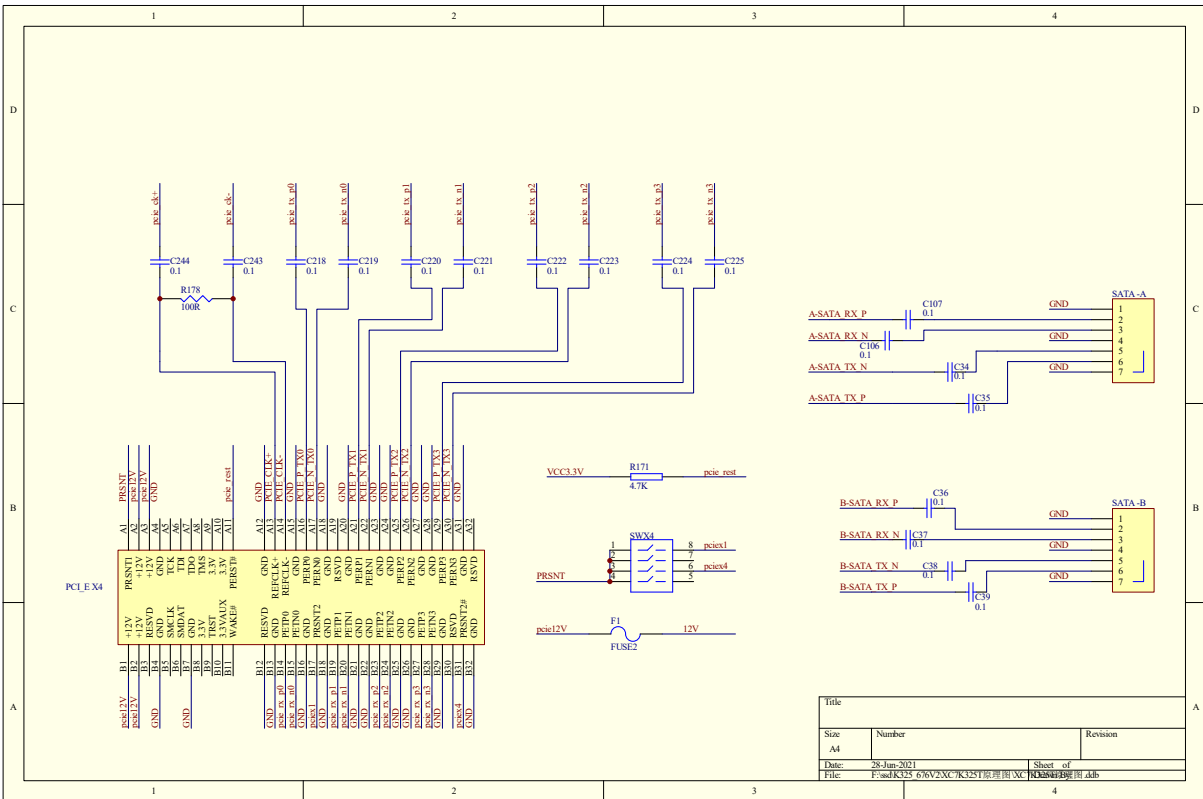


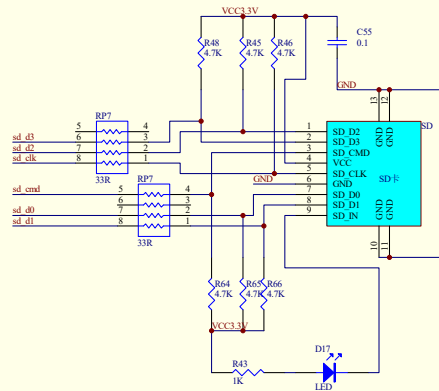
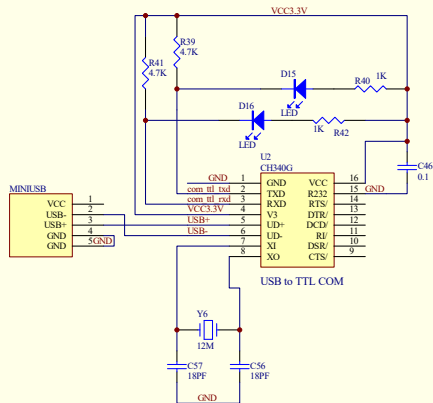


Title		
Size A4	Number	Revision
Date: 28-Jun-2021	Sheet of	
File: F:\andK325 676V2\XC7K325T原理图\XC7K325T原理图.ddb		

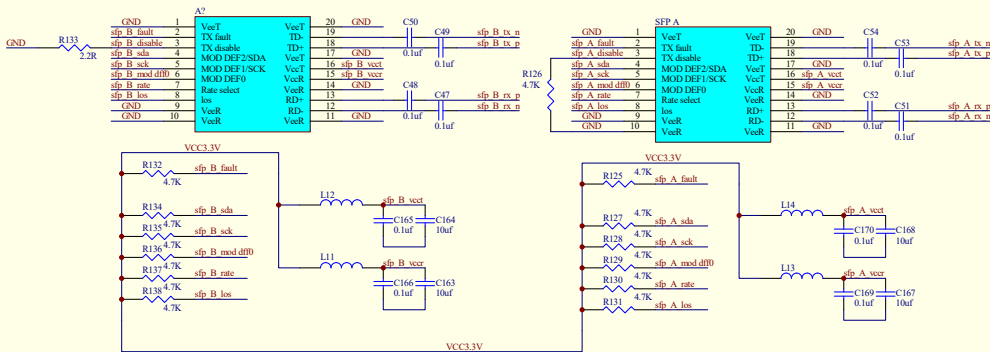


Title		
Size	Number	Revision
A4		
Date:	28-Jun-2021	Sheet of
File:	F:\andK325 6'6V2XC'7K325Y原理图XC	PCB图 zdb





Title		
Size A4	Number	Revision
Date: 28-Jun-2021	Sheet of	
File: F:\sd\K325 6/6V2\XC7K325T原理图\XC7K325T原理图.ddb		



Title		
Size	Number	Revision
A4		
Date:	28-Jun-2021	Sheet of
File:	F:\adK325 676V2XC7K325T原理图XC7K325T原理图.ddb	