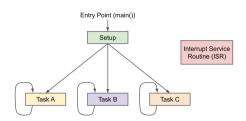
TASK SCHEDULING

· Blow disgram of a multi-task program:

What our code looks like



each tesus run concurrently in its own while loop.

· can also set up ISRs (intempt service rentines)

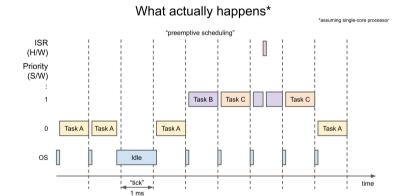
that can presempt any of the tasks, to execute

some lode:

#Se -> . handles hardware timer overflow

- · pin state changes
- · New communication on a sus

TASK SCHEDULING IN SINGLE CORE SYSTEMS



→ CPU divides the torus into time slices

Stasks appear to run concurrently

- -> schedulers determine which thou to run in each time slice
- time slice = 1 ms = 1 tick

Shardware times creates interrupt every 1 ms S ISD for times runs the schooler

S chooses fre tape to run next

- out each tick intermet -> thou w/ highest priority is chacen to run
 - -> trake w/ the same highest priority -> uses round-nobin execution
 - if a test w/ higher priority then running took is available immediately my

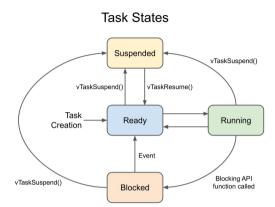
doesn't work for next tick

-> hardware intempt > minning software

S: hardware ISR on intempt any task

-> can assign that pronty using vTask Privity Set ()

TASK STATES



Ready state - task enters this state when it is created

y this means the task is ready to run

during each tith - 1 task in ready state 0 chosen by schedular to run

Running state > when terms are running > can go to receip state again by the scheduler

Blowled Stake -> When function cause the task to wait eg. vTask Delay ()

-> occurs when tasks conit for an event to occur eg. timer on vTask Delay () to expire

-> allows other task to run instead of the Blowed task

Suspended state > like patting task to succe eg. use vTasu suspend ()

-> any task con put any other tack in this mode (including itself)

-> task go back to Redy state by calling vTaskResume()