PARTIL: PRIORITY INVERSION

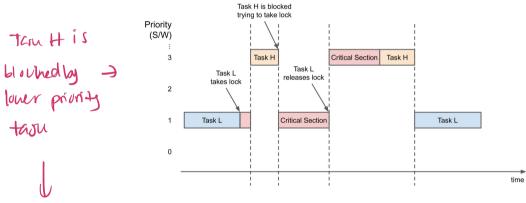
-> a bug when a high priority task is indirectly preempted by a low priority trak

Loeg- when a low priority tosu holds a mutex that a high priority tosu must unit for when executing

abounded priority inversion

Is the length of time of inversion is bounded by how long the priority took is in the critical section (holding the love)

Bounded Priority Inversion



priority of toou H is directly invested

· Unbounded priority inversion

Ly predium priority task interrupts high priority task while it holds the love

Tan M can blow task L for ANY own own of time Unbounded Priority Inversion

Task H is blocked trying to take lock

Task H is prevented from doing work in critical section

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because task L holds the lock

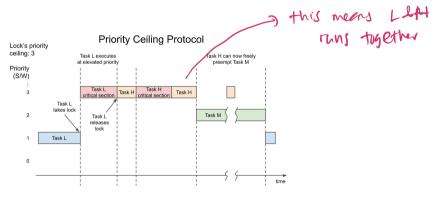
Ly hence it also blown task H

SOLUTION

1) PEIDRITY CETLING PROTICOL

Lower a tarn takes a low, it's priority cevel is automatically brossed to that of the priority ceiling

any trans that needs to use the resum or lock

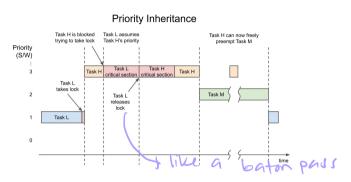


- · Porunity ceiling is 3
- · Tark L's priority is now the same as Task H's

 Liprevents Task M from running until the are done

@ PRIJEITY INHERITANLE

bousting priority of a task holding a low to that of the task trying to take the lock



· Task L's priority is only boosted when L tries to take the lock

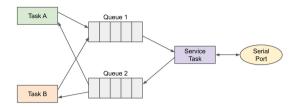
Li. Task M cennot interrupt it

priority of table drips once it releases the love
This only solves unbanded priority inversion

Bandled priority inversion -> solved through good programming practise
-> very critical section shop

- -> avoid using critical section or lowing mechanism that on blown a high primity task
- -) use one task to control a shared resource eg-using guenes to receive messages from a task that handles the serial port

Using a Service Task



othis awids using critical section for the serial port