

Adr Src = 0

IRWrite = 1

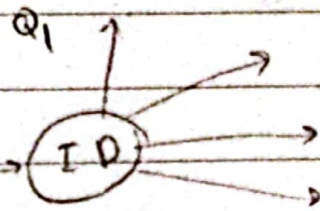
ALUSrcA = 00

ALUSrcB = 10

ALU Op = 000(+)

ResultSrc = 10

PCWrite = 1

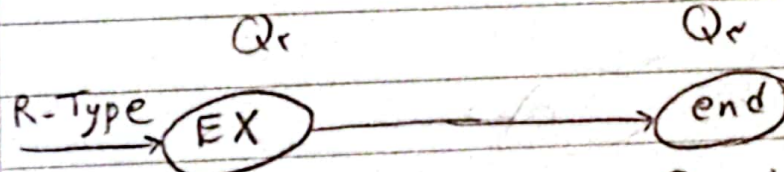


ALUSrcA = 01

ALUSrcB = 01

ALU Op = 000(+)

Imm Src = 011 (B-Type)



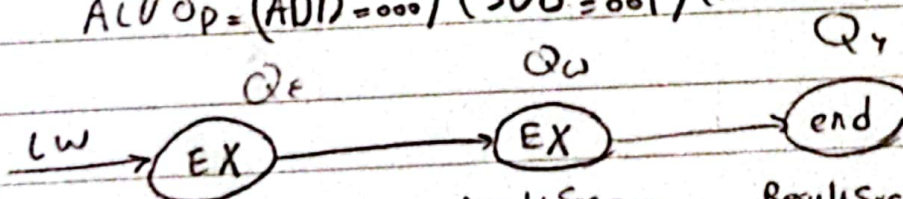
ALUSrcA = 10

ALUSrcB = 00

ALU Op = (ADD = 000) (SUB = 001) (AND = 010) (OR = 011) (SLT = 101)

ResultSrc = 00

Reg write = 1



ALUSrcA = 10

ALUSrcB = 01

ALU Op = 000(+)

Imm Src = 000 (I-Type)

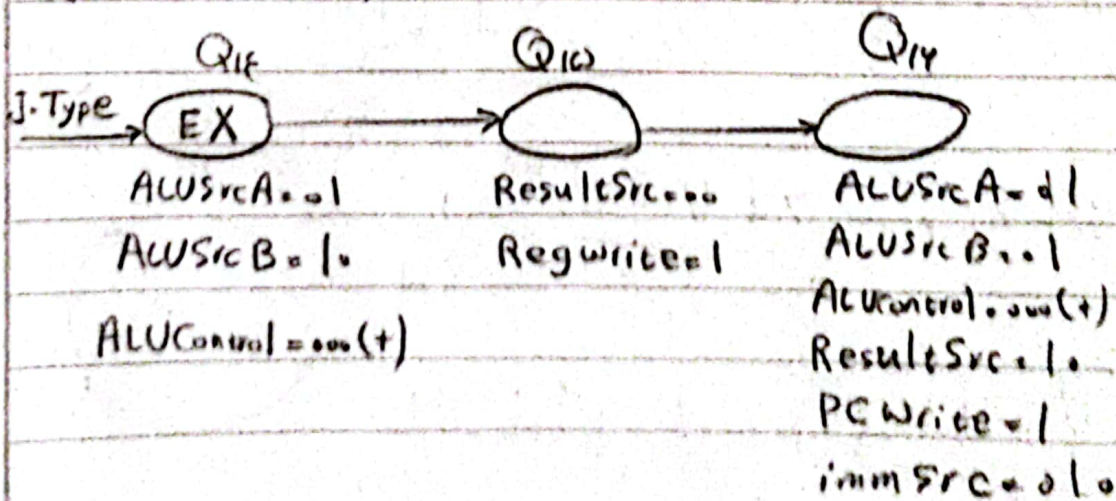
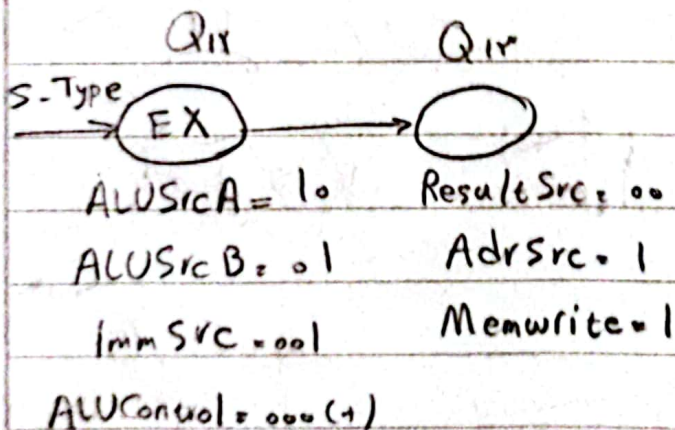
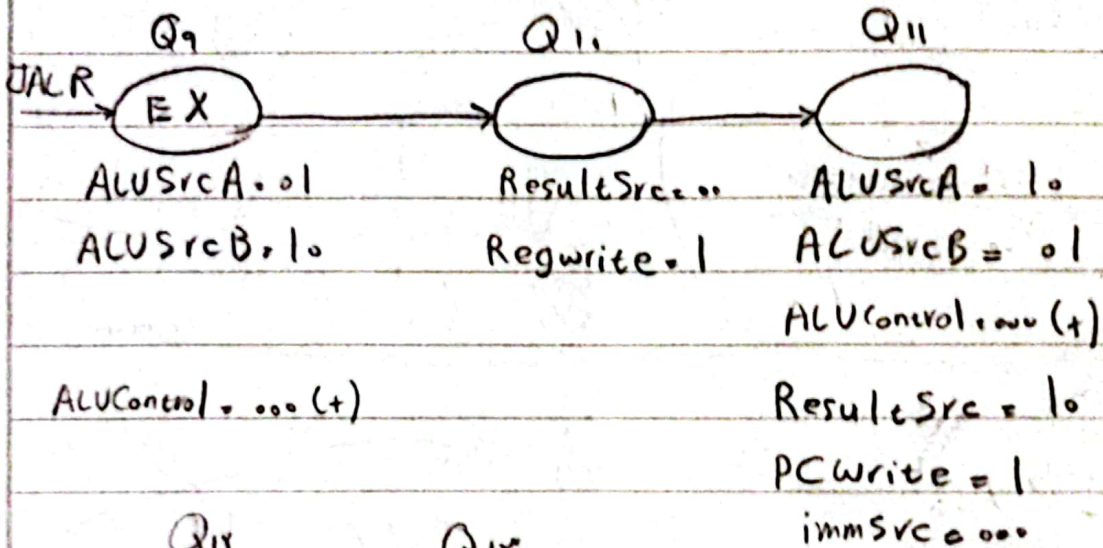
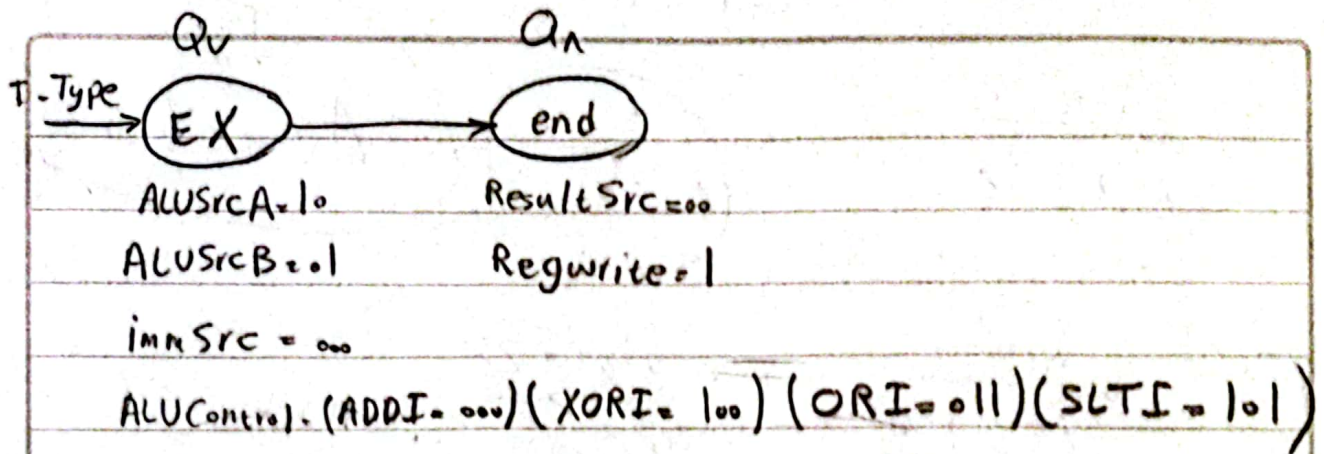
(I-Type)

ResultSrc = 00

Adr Src = 1

ResultSrc = 01

Reg write = 1



Q₁₄
B-Type → EX

ALUSrcA = 1.

ALUSrcB = ..

ALUControl = (BEQ || BNE = 001) (BLT || BGE = 101)

ResultSrc = 00

PCWrite = (BEQ: zero? 1: 0) (BNE: zero? 0: 1)
(BLT: LSB? 1: 0) (BGE: LSB? 0: 1)

Q₁₅

U-Type →

immSrc = 100

ResultSrc = 11

Regwrite = 1