Paddigo Size formula: (k-1) here is zero Output Sire formula: InputSire - Kernel Sire + 2x Padding Sire + 1

Stride Number of multiply-add operations in CNL: Output Size x Output Channels X (kernel Size x input channels +1) Number of parameters in & Output channels X (kernelsine x inputehannels +1) Fully connected layers number Output Neuronsx (input neurons + 1) Number of parameters Output Neurons x (input Neurons +1) # multiply in FCL input size out put size of parameters Cayer Jetails 104 576 . 43x43x32 47x47x3 CNL: 5x5x32 - Stride 1 194208 9248 21x21x32 43x43x32 CNL: 3×3×32-82 175 712 9248 19x19x32 CNL: 3x3x32-SI 21x21x32 83232 9248 9x9x32 CNL: 3x3x32-S2 19x19x32 64736 9248 CNL: 3x3x32-SI 7x7x32 9×9×32 flattering 1568 0 7x7x32 200832 200832 FC-128 1568 128 1290 1290 FC-128 10 824586 241546

overall.