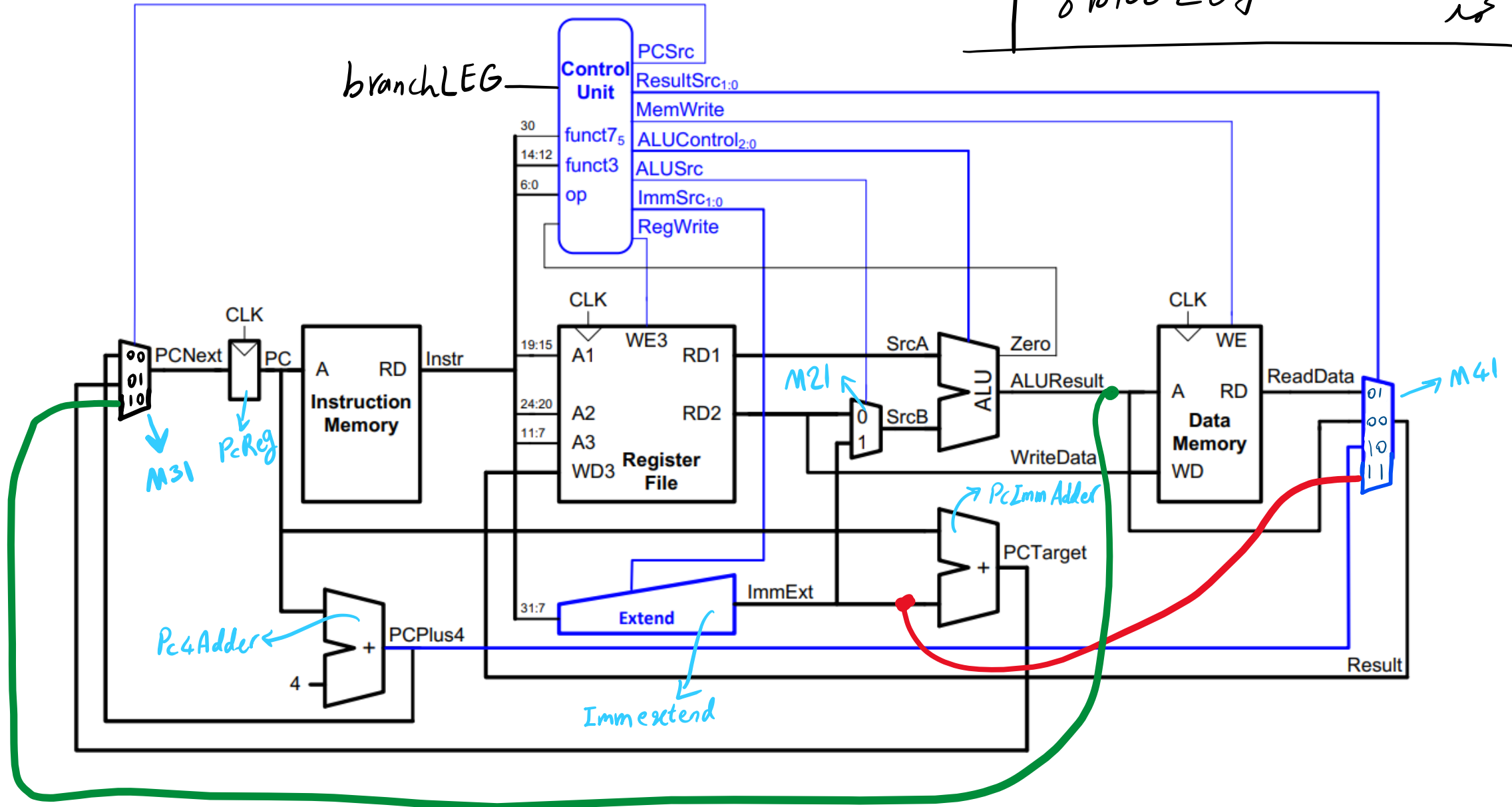


امیرحسین جعفری اراد
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		PCSrc	ResultSrc	MemWrite	ALUControl	ALUSrc	ImmSrc	RegWrite
R-Type	add	00	00	0	000	0	XXX	1
	sub	00	00	0	001	0	XXX	1
	and	00	00	0	010	0	XXX	1
	or	00	00	0	011	0	XXX	1
	slt	00	00	0	101	0	XXX	1
I-Type	lw	00	01	0	000	1	000	1
	addi	00	00	0	000	1	000	1
	xori	00	00	0	100	1	000	1
	ori	00	00	0	011	1	000	1
	slti	00	00	0	101	1	000	1
	jalr	10	10	0	000	1	000	1
S-Type	sw	00	XX	1	000	1	001	0
J-Type	jal	01	10	0	XXX	X	010	1
B-Type	beq	zero ? 01 : 00	XX	0	001	0	011	0
	bne	zero ? 00 : 01	XX	0	001	0	011	0
	blt	alureult[0] ? 01 : 00	XX	0	101	0	011	0
	bge	alureult[0] ? 00 : 01	XX	0	101	0	011	0
U-Type	lui	00	11	0	XXX	X	100	1