

Operating system Assignment - 2

Q. 1

Ans Address translation in modern system.

- Each process generates logical (virtual) address.
- MMU (Memory management unit) translates these into physical address.

Translation Steps :-

- a. CPU generates logical address.
- b. MMU checks page table for corresponding frame number.
- c. Concatenation frame + offset → physical address.

Q. 2

Ans Process A (100 kb of 120 kb block) / free 30 kb /
Process B (200 kb)

Internal → 20 kb wasted space inside allocated block.

External → 30 kb free, but too small for 40 kb

Mitigation : Paging (removes external), segmentation with buddy system, slab allocators, or dynamic allocation with coalescing free space.

Q. 3

Ans Memory is split into fixed size frames, process into pages, page table store mapping.

- Pros: No external fragmentation, simple allocation.
- Cons: Page tables use extra memory, TLB misses add delay, last page may waste some space.

Date _____ / _____ / _____

Q. 4.

Ans OS hardware interaction in Virtual memory.

- Page-Table in memory.
- MMU translates virtual
- TLB caches recent translations
- Protection bits

Q. 5.

Ans 16-bit virtual address, 11cb page size.

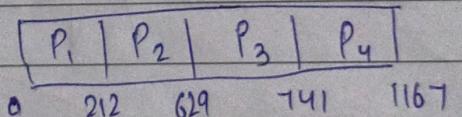
- Address space = 2^{16} = 65536 bytes
- Page size = 2^{10} = 1024 bytes
- Number of Pages = $\frac{65536}{1024} = 64$
- Page table size = 64×2 bytes = 128 bytes

Part B

Q. 6.Ans

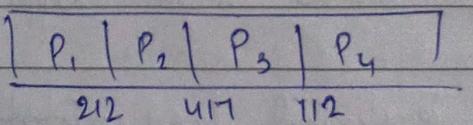
Process	Size (kb)
P ₁	212
P ₂	417
P ₃	112
P ₄	426

- First-fit



$$\text{Unused memory} = 259 \text{ kb}$$

- Best-fit



P₄ still can't fit

Unused = 259 kb

Date _____ / _____ / _____

- Worst-fit

P4 (426) can't fit, unused = 259 kb

All three methods leave 259 kb unused in this process.

Q7.

Aus Page replacement.

- FIFO = 10 faults
- Optimal = 7 faults
- LRU = 9 faults

Optimal is best, LRU is practical, FIFO may show Belady's anomaly.

Q8.

Aus Disk write = 10 ms

Memory write = 100 ms

Dirty pages = 30% of 1000 = 300

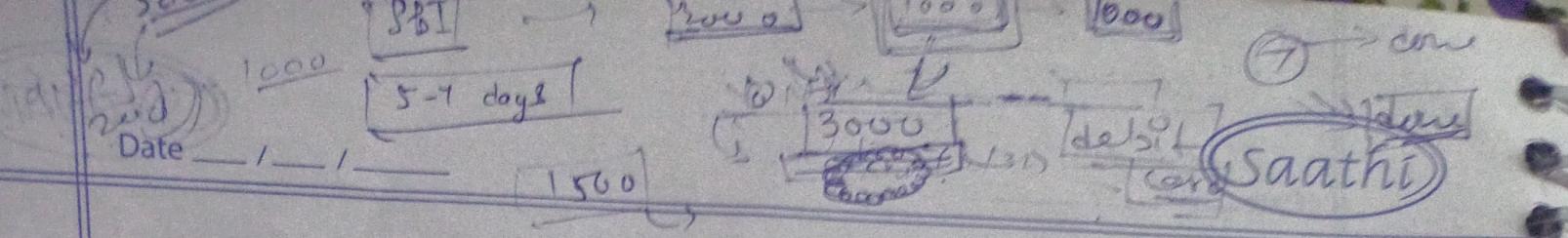
$$\text{Overhead} = 300 \times 10 \\ = 3000 \text{ ms} = 3 \text{ seconds.}$$

⑥ Optimization: ~~write-back~~ caching with dirty bit tracking or pre-cleaning (background flush) reduce blocking time.

Q9.

Aus Autonomous vehicle memory.

- Use working set model to allocate enough frames for real time (object detection) to prevent thrashing



- Replacement policy : LRU or WLock with priority.
- Strategy: Reserve fixed memory for real-time processes , share remaining dynamically . This ensures responsiveness and efficient utilization.

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