

Operating System Assignment - 3

1. Race Condition + Real-World Example + Mutual Exclusion
- Ans A race condition occurs when two or more entities try to perform actions simultaneously, and the final outcome depends on the unpredictable timing of their operations.

Real-world example (outside computing):

- Two people withdrawing money from the same ATM account at the same time.

How mutual exclusion solve this?

A lock at the ATM ensures only one person can access the account at a time.

2.

Ans

Aspect

Implementation complexity

Peterson's Satu

Simple logic but only works for 2 processes; difficult to extend

Semaphores

Very flexible; supports many processes; widely used in OS kernel

Hardware dependency

Pure software; requires strict hardware support

Needs Atomic

hardware instructions.

Conclusion: Semaphores are more practical for real OS implementation.

3.

- Ans Monitors automatically provide mutual exclusion, so only one thread can execute critical sections at a time.

Advantage:

In multi-core systems, monitors simplify synchronization by eliminating the need for manually controlling semaphores, reducing errors.

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Ans How starvation occurs:

If writers are repeatedly waiting but readers keep arriving, the writer may never get access (reader priority \rightarrow writer starvation).

Prevention Method:

Use writer priority: Once writer arrives, block new readers and allow current readers to finish \rightarrow writer executes next.

S.

Ans Eliminating Hold and Wait means a process must request all required resources at once before running.

Practical Drawback:

This leads to low resource utilization. Many processes unnecessarily hold resources they don't immediately need, causing delays.

Part B

(Q6)

Ans Given Total = (10, 5, 7)

Process	Allocation (A, B, C)	Max (A, B, C)
P ₀	0, 1, 0	7, 5, 3
P ₁	2, 0, 0	3, 2, 2
P ₂	3, 0, 2	9, 0, 2
P ₃	2, 1, 1	4, 2, 2
P ₄	0, 0, 2	5, 3, 3

(a) Need = Max - Allocation

Process	Need (A, B, C)
P ₀	7, 4, 3
P ₁	1, 2, 2
P ₂	6, 0, 0
P ₃	2, 1, 1
P ₄	5, 3, 1

Available = Total - Allocation Sum

Allocation Sum = (1, 2, 6) → Available = (3, 3, 1)

(b) Safe State check:

Work = (3, 3, 1)

- P₁ can run ($\text{Need} \leq \text{Work}$) → New Work = (5, 3, 1)
- P₃ can run → Work = (7, 4, 2)
- P₄ can run → Work = (7, 4, 4)
- P₀ can run → Work = (7, 5, 4)
- P₂ can run → Work = (10, 5, 6)

→ Safe sequence = P₁ → P₃ → P₄ → P₀ → P₂

(c) If P₁ requests (1, 0, 2)

New Need = (0, 2, 0) Request \leq Available $(1, 0, 2) \leq (3, 3, 1)$
cannot be granted immediately.

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7. Dining Philosophers:

→ Deadlock scenario:

each philosopher picks up their left chopstick first, all hold one chopstick, and none can pick the right one → deadlock.

Solution: Use semaphore array $\text{chopstick}[5] = 1$ and a mutex for limit

$\text{wait}(\text{mutex});$

$\text{wait}(\text{chopstick}[i]);$

$\text{wait}(\text{chopstick}[(i+1) \% 5]);$

$\text{eat}();$

$\text{signal}(\text{chopstick}[i]);$

$\text{signal}(\text{chopstick}[(i+1) \% 5]);$

$\text{signal}(\text{mutex});$

8. I/O System Analysis:

Given:

Interrupt time = 5 μs

Transfer Rate = 800 KB/s = $800 \times 1024 = 51200$ bytes/s

Block = 100 bytes

$$(a) \text{Interrupts per second} = 51200 / 5 = 10240$$

$$\text{CPU Time} = 10240 \times 5 \mu\text{s} = 25.6 \text{ ms per second}$$

(b) Improvement: Use Direct memory access (DMA) to transfer data directly without frequent CPU interrupts.

a.

Ans (a) Critical Sections:

- Updating shared radar data
- Flight Path computation database
- Communication logs

IPC mechanism

Use message queues or real time semaphores for synchronising ensuring quick switching and real-time safety.

(b) Deadlock detection & Recovery:

- Use resource allocation graph for detection.
- On ~~det~~ detection, prompt non-critical process (eg: temporarily pause flight path computation).

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