

JESSICA CLAIRE

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PROFESSIONAL SUMMARY

More than 20 years of experience Object-Oriented Programming ASM, C, C++, C#. Architecture, development and deliver high quality of diagnostics software products, applications and tools for test fixture PCA, system test, RMA service on aggressive schedules. Included full user interface (GUI) and test scripts to meet defined test requirements. Managed operations software and testing teams more than 30 people, provided factory diagnostics and test support locally as well as global support for worldwide factories, and led a team to develop WinPE software imaging. Included development, validation, release, and sustaining of manufacturing diagnostics as well as test equipment Sr. Architecture, Design, Diagnostic Test and Manufacture Processes for all HP Servers, Blades, Nodes, Storages products. Sr. Product Engineer. Experience in Digital and Analog Circuits design, analysis and troubleshooting. Outstanding management and leadership, provoking staff members/vendors to come up solutions for new technology and innovations. Outstanding instructor, mentor and capable of providing guidance and experience to inexperienced staff members.

SKILLS

- Code reviews
- Advanced critical thinking
- Agile
- Program testing software
- Software development lifecycle expert
- Engineering standards
- Python
- Operational analysis

WORK HISTORY

01/2005 to 08/2014

Sr. Architect & Diagnostics and Test Engineering Manager

Fis â€™ Elm Grove, WI,

- Designed, developed, and managed Diagnostics programs, manufacturing processes and other applications written in ASM, C, C++, C# for HP Servers, Blades, Nodes, Enclosures, Rack to ensure meeting product quality matrix and manufacturing requirements.
- Diagnostic supporting Windows Server 32/64 Bit, Linux and DOS.
- Included full user interface (GUI) and test scripts.
- Designed and developed test diagnostic for HP rack controller by handling multithreads with more than 40 ports with different protocols include RS232/485, RJ45.
- Designed and developed test diagnostic for Calxeda ARM servers in one rack (48 server nodes in a 2U chassis) products by using multithreads Telnet Protocol Client-Server technology.
- Designed, developed and supported tools with ability to integrate electronic components into system (VRD Processor, Flash EEPROM).
- Developed complete test diagnostics for more than 10 new HP products (e.g., DL380, DL560, ML350 Servers, BL460, BL660 Blades, SE2140, SE2160 Hyper-Scales, Storages, etc.) and supported more than 400 HP sustaining products as well as test equipment, business worth over \$2B revenue per year since 2010.
- Managed a team of 30 engineers.
- Created manufactures process from A-Z , provided factory and test support locally as well as global support for worldwide factories (China, Czech Republic, and Mexico).
- Created and implemented comprehensive test strategy and test plans across all products at all stages (ICT, FBT, Pre-Test, Run-In, Audit and Pack-Out based on hardware/software system specifications and reviewed with program managers and product engineers to meet all product requirements.
- Reported build coverage, status and issues found at each build stage.
- Faulted injections to ensure tests are performing as expected.
- Debugged root cause of any issue found during the manufacturing process.
- Inspected, audited and executed all test plans to make sure products being launched meet all the testing requirements and reported the status to provide feedback to the product development team.
- Formulated creative testing method using extensive knowledge in Embedded, Integration, and System Tests, which reduced testing time by 30% without sacrificing quality.
- Co-developed with hardware team, designed and implemented some device testing replacing expensive hardware (Blades, PCIe X8,16, etc.), reducing test equipment cost by 75% and saving \$2M for customer since 2007.
- Created and designed hot-plug concept instead of shutting down unit test system, reducing manufacturing test time of front panels DL380 by 70%.
- Worked with Operation team upfront to estimate the cost of test to support a new product.

2003 to 2005

Sr. Software Engineer

Fis â€™ Franksville, WI,

	<ul style="list-style-type: none"> Identified, troubleshot and resolved software defects for commercial/consumer HP routers firmware requiring C programming. Documented update for new software releases. Designed automated software installation kit in house, saving more than 50% outsources costs and \$10M per.
2001 to 2003	Sr. Software Engineer Fis â€™ Genesee Depot, WI, <ul style="list-style-type: none"> Designed, developed, validated and released commercial applications for ColoReal project with full user interface, supporting multiple languages and create test strategy, test plan and test scripts to meet all product requirements. Co-author with pending patent for Compaq Monitor/Printer products with revenue of over \$30M.
1999 to 2001	Sr. Software Engineer Fis â€™ Hales Corners, WI, <ul style="list-style-type: none"> Identified and resolved software defects, validation, release and sustaining Compaq/DEC Clustering Systems with full user interface supporting multiple languages and create test strategy, test plan and test scripts to meet all product requirements.
1995 to 1999	Sr. Software Engineer Bd (Becton, Dickinson And Company) â€™ Lubbock, TX, <ul style="list-style-type: none"> Designed, developed test diagnostic for Graphic Chips (i.e. Matrox, S3, ATI, 3DLab. for DOS and Windows requiring ASM, C and C++ programming, using MS DirectX and OpenGL APIs. Created test strategy, test plan and test scripts to meet all product requirements.
1984 to 1995	Staff Firmware Engineer IBM Corp â€™ City, STATE, <ul style="list-style-type: none"> R&D professional in conceptualization and development of microprocessor-based for lines printer products. Organize and maintain C/Assembly language source code for line printer products. Effective use tools such as Emulators/Simulators Signum 8051 emulator for Intel micro controller to design, implement and debug firmware.

EDUCATION

1984	Bachelor of Science: Electrical Engineer Virginia Polytechnic Institute University - , GPA: Electrical Engineering
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ACCOMPLISHMENTS

SKILLS

Agile, ASM, Assembly language, C, C programming, C++, C++ programming, CIM, Client-Server, Clustering, Compaq, hardware, concept, controller, Czech, DEC, product development, DOS, Flash, GUI, Graphic, HP, HP Servers, Intel, languages, Linux, C#, Excel, MS Office, Outlook, PowerPoint, Windows, MS Windows, Word, NIC, OOA, OpenGL, PCI, Printer, processes, protocols, PXE, quality, routers, Servers, scripts, software development, software installation, Visual SourceSafe, strategy, Telnet, test equipment, user interface, validation, MS Visual Studio, author, written, x86, XML