◆◆◆

LCD MODULE SPECIFICATION

MODEL NO.

BC1602A series

FOR MESSRS:

_____

ON DATE OF:

_____

APPROVED BY:

_____

Bolymin, Inc.

◆◆◆

C O N T E N T S

Bolymin, Inc.

◆◆◆

1. Numbering System

B	C	2004	A	G	P	L	E	B	xxx
0	1	2	3	4	5	6	7	8	9

0	Brand	Bolymin	
1	Module Type	C= character type G= graphic type P= TAB/TCP type	O= COG type F= COF type
2	Format	2002=20 characters, 4 lines 12232= 122 x 32 dots	
3	Version No.	A type	
4	LCD Color	G=STN/gray Y=STN/yellow-green C=color STN	B=STN/blue F=FSTN T=TN
5	LCD Type	R=positive/reflective P=positive/transflective	M=positive/transmissive N=negative/transmissive
6	Backlight type/color	L=LED array/ yellow-green H=LED edge/white R=LED array/red G=LED edge/yellow-green	D=LED edge/blue E=EL/white B=EL/blue C=CCFL/white
7	CGRAM Font	J=English/Japanese Font E=English/European Font	C=English/Cyrillic Font H=English/Hebrew Font
8	View Angle/ Operating Temperature	B=Bottom/Normal Temperature H=Bottom/Wide Temperature U=Bottom/Ultra wide Temperature	T=Top/Normal Temperature W=Top/Wide Temperature C=9H/Normal Temperature
9	Special Code	3=3 volt logic power supply n=negative voltage for LCD c=cable/connector xxx=to be assigned on data sheet	t=temperature compensation for LCD p=touch panel

2. Precaution in use of LCD Module

(1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3) Don't disassemble the LCM.

(4) Don't operate it above the absolute maximum rating.

(5) Don't drop, bend or twist LCM.

(6) Soldering: only to the I/O terminals.

(7) Storage: please storage in anti-static electricity container and clean environment.

(8) Don't touch the elastmer connecter, especially insert a backlight panel (EL or CCFL)

3. General Specification

(1) Mechanical Dimension

Item	Dimension	Unit
Number of Characters	16characters x 2 Lines	—
Module dimension (L x W x H)	80.0 x 36.0 x 12.7(Max)- LED array B/L, LED edge B/L (white, blue) 80.0 x 36.0 x 8.9 (Max) – LED edge/blue B/L, EL or No B/L	mm
View area	66.0 x 16.0	mm
Active area	56.2 x 11.5	mm
Dot size	0.56 x 0.66	mm
Dot pitch	0.60 x 0.70	mm
Character size (L x W)	2.96 x 5.56	mm
Character pitch (L x W)	3.55 x 5.94	mm

(2) Controller IC: **KS0066 (or Equivalent) controller**

(3) Temperature Range

	Normal	Wide
Operating	0 ~+50℃	-20 ~+70℃
Storage	-10 ~+60℃	-30 ~+80℃

4. Absolute Maximum Ratings

4.1 Electrical Absolute Maximum Ratings

(Vss=0V, Ta=25℃)

Item	Symbol	Min	Max	Unit
Supply Voltage (Logic)	Vdd-Vss	-0.3	7	V
Supply Voltage (LCD driver)	Vdd-Vo	-0.3	13	V
Input Voltage	V_I	Vss	Vdd	V
Normal Type	T_{OP}	0	+50	℃
	T_{STG}	-10	+60	℃
Wide Temperature Type	Top	-20	+70	℃
	Tstg	-30	+80	℃

4.2 Environmental Absolute Maximum Ratings

Item	Operating		Storage		Comment
	(Min.)	(Max.)	(Min.)	(Max.)	
Humidity	Note (2)		Note (2)		Without condensation
Vibration	--	$4.9M/S^2$	--	$19.6M/S^2$	XYZ Direction
Shock	--	$29.4M/S^2$	--	$490M/S^2$	XYZ Direction

Note (1) Ta = 0℃： 50Hr Max.

Note (2) Ta ≦40℃ ：90% RH MAX

Ta > 40℃ ：Absolute humidity must be lower than the humidity of 90% at 40℃.

5. Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	Vdd-Vss	—	3.0	—	5.5	V
Supply Voltage For LCD ＊Wide Temp、Type	Vdd-Vo	＊Ta=-20℃	—	5.2	—	V
		Ta=0℃	—	—	—	V
		Ta=25℃	—	**4.0**	—	V
		Ta=50℃	—	—	—	V
		＊Ta=+70℃	—	3.6	—	V
Input High Volt.	V_{IH}	—	2.2	—	Vdd	V
Input Low Volt.	V_{IL}	—	—	—	0.6	V
Output High Volt.	V_{OH}	—	2.4	—	—	V
Output Low Volt.	V_{OL}	—	—	—	0.4	V
Supply Current	Idd	Vdd=5V	—	1.2	—	mA

6. Optical Characteristics

a. STN

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
View Angle	(V)θ	CR$\geq$2	10		45	deg
	(H)φ	CR$\geq$2	-30		30	deg
Contrast Ratio	CR	—		3		—
Response Time 25℃	T rise	—		100	150	ms
	T fall	—		150	200	ms

b. FSTN

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
View Angle	(V)θ	CR$\geq$3	10		60	deg
	(H)φ	CR$\geq$3	-45		45	deg
Contrast Ratio	CR	—		5		—
Response Time 25℃	T rise	—		100	150	ms
	T fall	—		150	200	ms

6.1   Definitions

■View Angles

■Contrast Ratio

Z   (Visual angle direction)

θ

LCD

X

φ

Y

(Best visual angle direction)

$$CR = \frac{\text{Brightness at selected state (BS)}}{\text{Brightness at non-selected state (Bns)}}$$

Brightness (%)

Selected state

Non-selected state

Bs

Bns

Operating voltage for LCD driving

■ Response Time

Nonselected Condition   Selected Condition   Nonselected Condition

Brightness

100 %

10 %

90 %

tr

Rise Time

td

Decay Time (fall time tf)

Bolymin, Inc.

◆◆◆
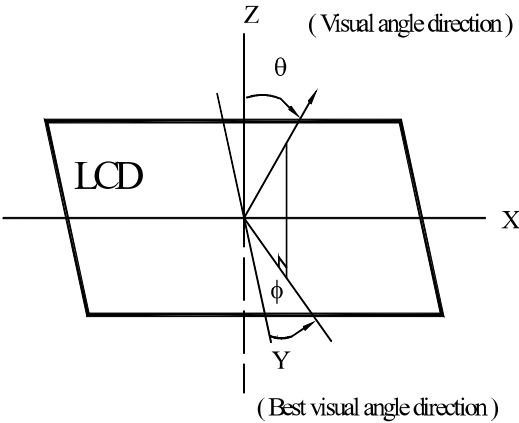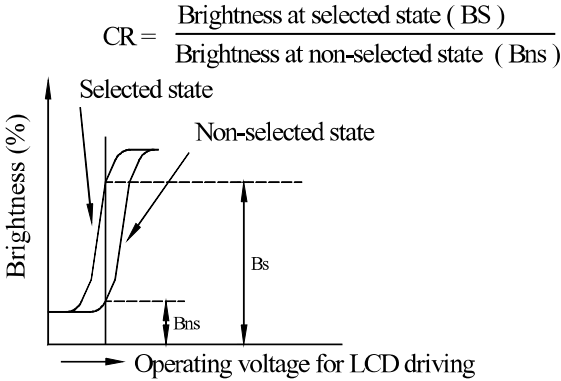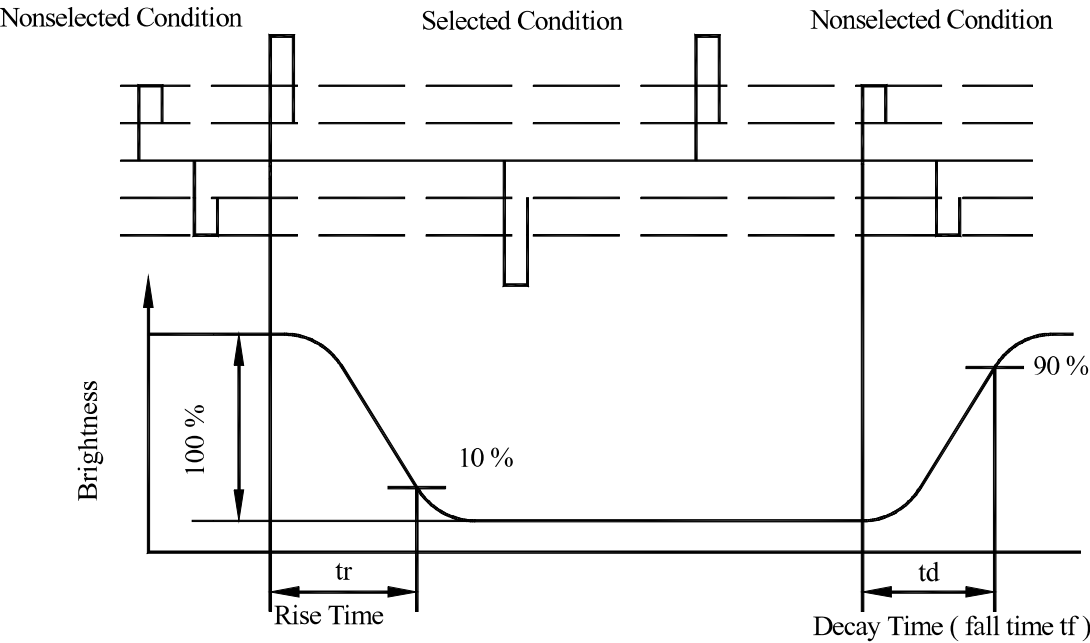
7. Interface Pin Function

| Pin No.	Symbol	Level	Description
1	Vss	0V	Ground
2	Vdd	5.0V	Supply Voltage for logic **(option +3V)**
3	Vo	(Variable)	Operating voltage for LCD
4	RS	H/L	H:DATA, L:Instruction code
5	R/W	H/L	H:Read(MPU→Module)L:Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	A/ Vee	—	Power supply for LED backlight (+) / Negative voltage output
16	K	—	Power supply for LED backlight (GND)

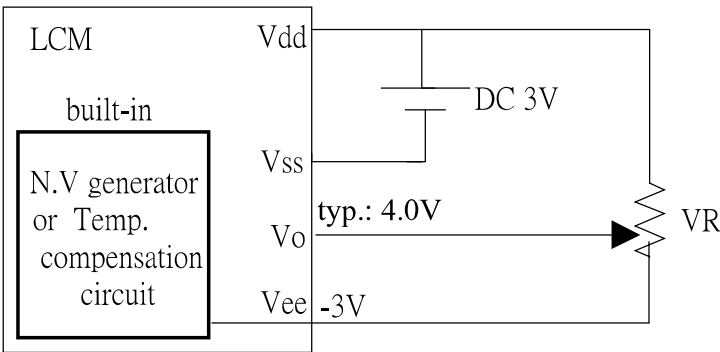**8. Power Supply for LCD Module and LCD Operating Voltage a Adjustment**

＊Standart Type
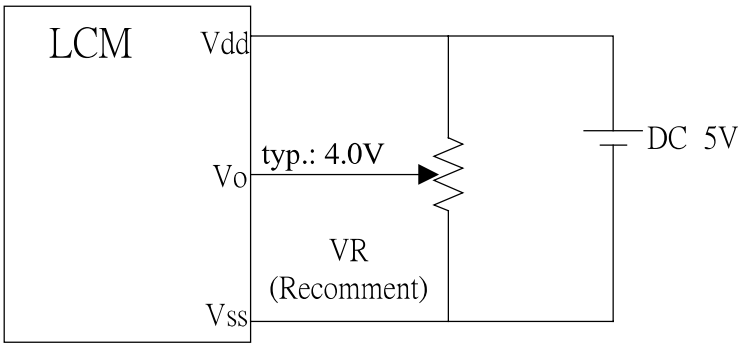
＊(Option)LCM operating on " DC 3V " input , with external negative
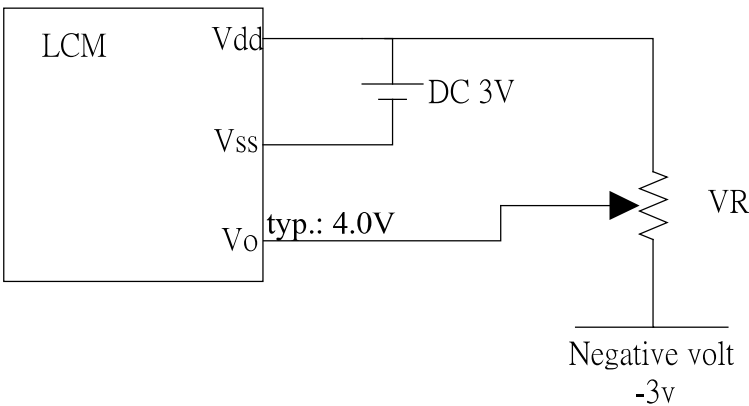
＊(Option) LCM operating on " DC 3V " input , with built-in negative Voltage

9.Backlight Information

9.1 Specification

(1) LED array / yellow-green

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	ILED	—	100		mA	V=4.2V
Supply Voltage	V	—	4.2	4.3	V	
Reverse Voltage	VR	—	—	8	V	
Luminous Intensity	IV	60	—	—	cd/m^2	ILED=100mA
Wave Length	λp		574		nm	ILED=100mA
Life Time		—	100000	—	Hr.	V≦4.2V
Color	Yellow Green					

(2) LED edge/white/blue

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	ILED	—	20	25	mA	V=3.4V
Supply Voltage	V	—	3.4	3.5	V	
Reverse Voltage	VR	—	—	8	V	
Luminous Intensity	IV	50	—	—	cd/m^2	ILED=20mA
Life Time		—	10000-white 50000-blue	—	Hr.	V≦3.4 V
Color	White/Blue					

(3) LED edge/ yellow-green

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Supply Current	ILED	—	20		mA	V=3.9V
Supply Voltage	V	—	3.9	4.1	V	
Reverse Voltage	VR	—	—	6	V	
Luminous Intensity	IV	—	—	—	cd/ m^2	ILED=20mA
Wave Length	λ p		573		nm	ILED=20mA
Life Time		—	100000	—	Hr.	V$\leqq$3.9V
Color	Yellow Green					

(4) EL / Blue

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Voltage	Vrms	--	110 (AC)		--	
Frequency	HZ	--	400		--	
Brightness*	cd/m^2	48	60		--	
CIE Chromaticity Diagram	X	--	0.330		--	110Vrms 400Hz
	Y	--	0.365		--	
Current Dissipation	mA/cm^2	--	1.33		--	
Power Dissipation	mW/cm^2	--	26.29		--	
Color	Blue					

9.2 Backlight driving methods

a. LED B/L drive from pin15 (LED+) pin16 (LED-)

a.1 array / yellow-green

LCM

a.2 edge (white/blue)

LCM
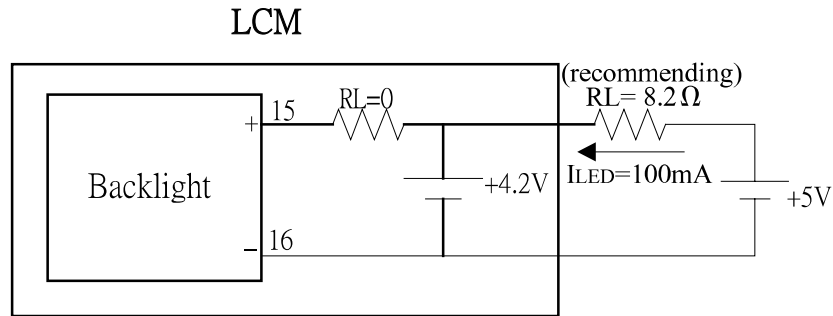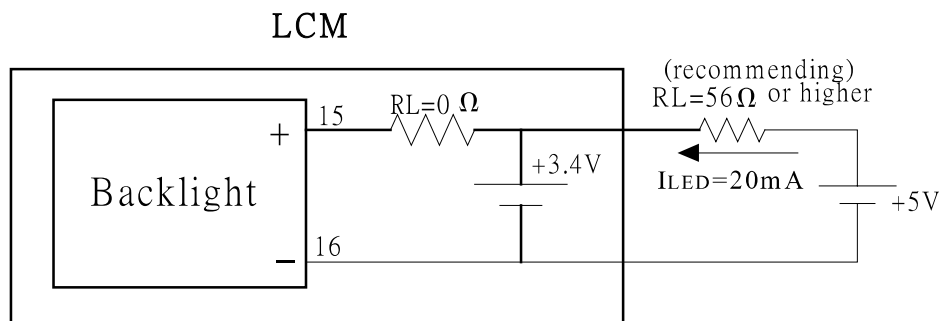
a.3 edge /yellow-green

LCM
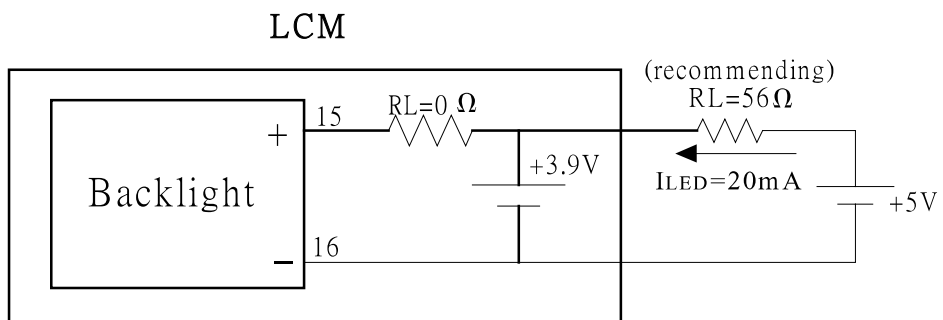
b. LED B/L drive from A. K directly

b.1 array / yellow-green

LCM

b.2 edge (white/blue)

LCM
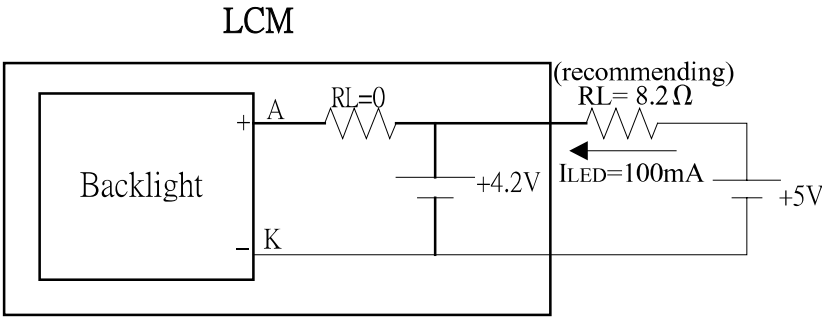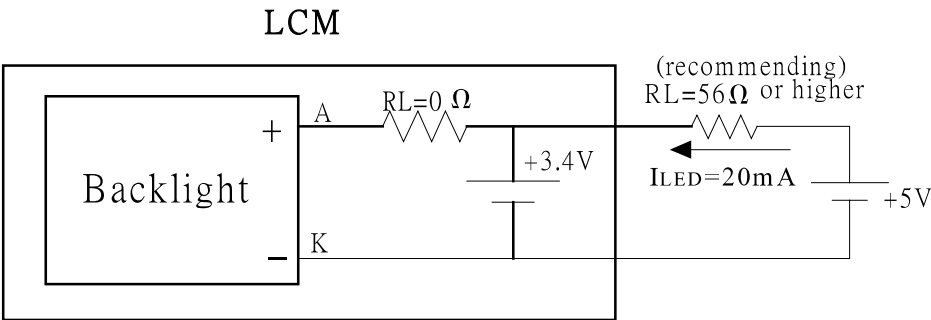
b.3 edge /yellow-green

LCM
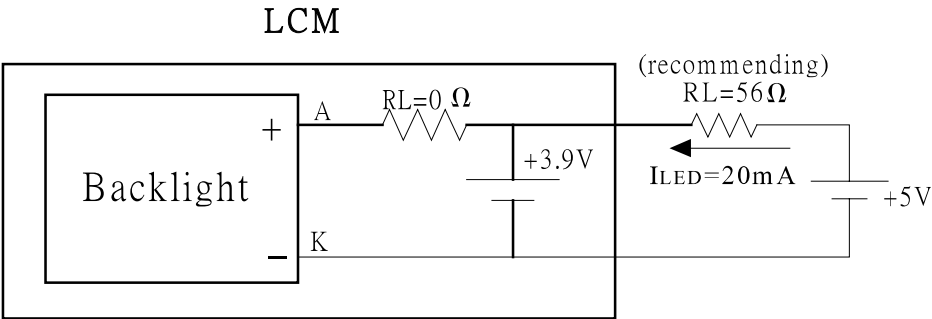
c.＊(Option) LED B/L drive from pin1 (Vss) pin2 (Vdd)

LCM
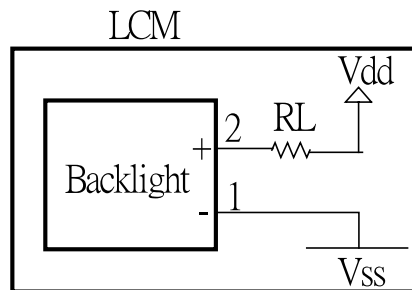
(1) Jump 1,2 Short

(2) Current Resistor required on RL

(3) Jump 15,16 open

(4)To be sure of enough current supply for both Vdd + LED B/L

d. EL B/L drive from A.K directly

LCM
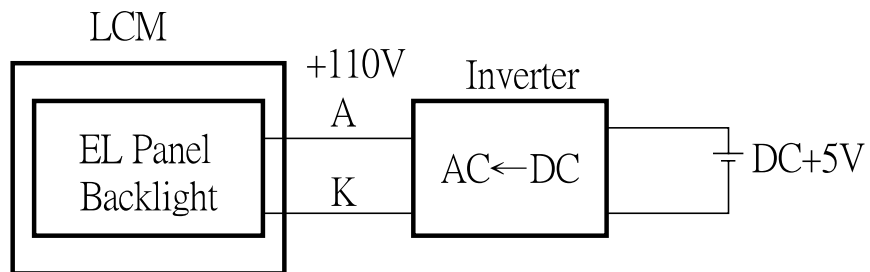
10. Quality Assurance

10.1 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.

45°
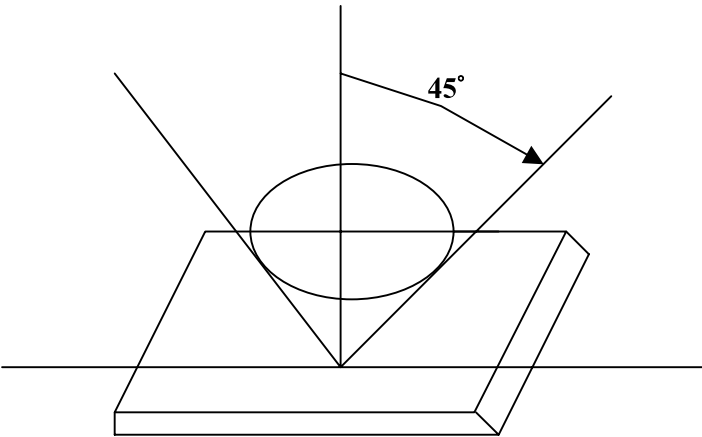
Definition of applicable Zones

B

A

L C D

B E Z E L

P C B
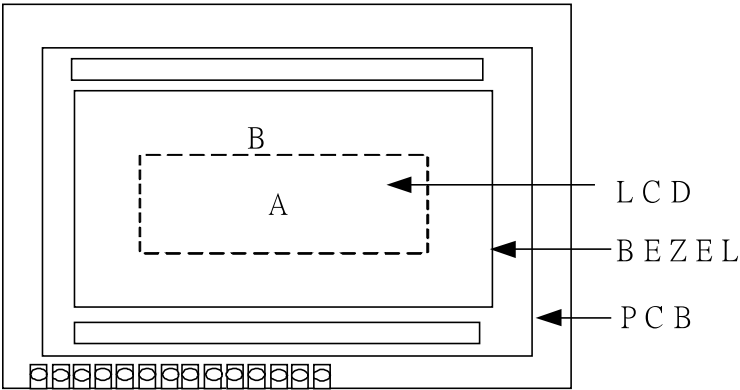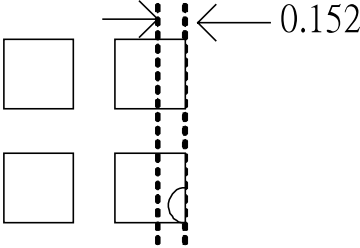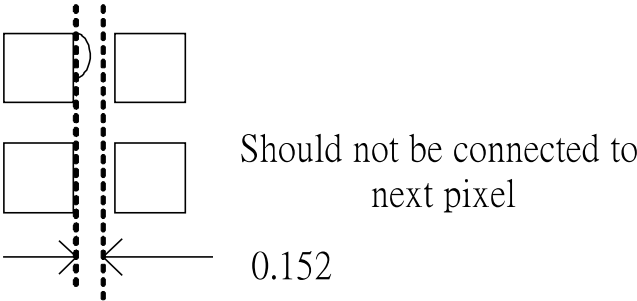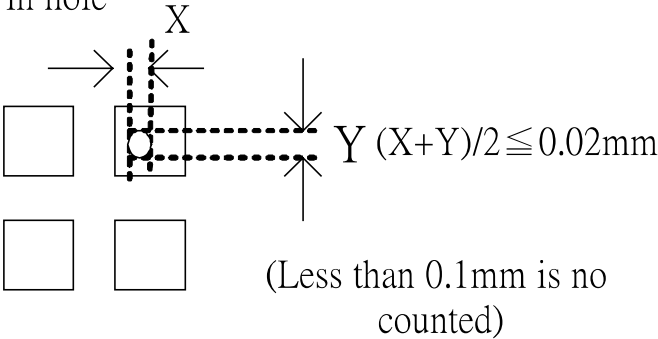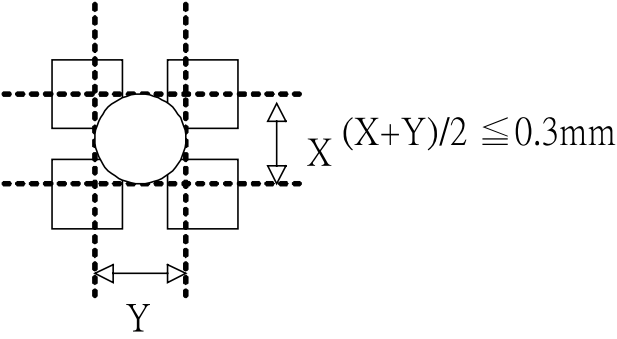
A : Display Area

B : Non-Display Area

10.2 Inspection Parameters

NO.	Parameter	Criteria
1	Black or White spots	
2	Scratch, Substances	
3	Air Bubbles (between glass & polarizer)	

Parameter 1: Black or White spots

Dimension \ Zone	Acceptable Number A	Acceptable Number B	Class Of Defects	Acceptable Level
D＜0.15	*	*	Minor	2.5
0.15≦D≦0.2	4	4		
0.2≦D≦0.25	2	2		
D≦0.3	0	1		

D=(Long + Short)/2   *: Disregard

Parameter 2: Scratch, Substances

X(mm)	Y(mm) Zone	Acceptable Number A	Acceptable Number B	Class Of Defects	Acceptable Level
*	0.04≧W	*	*	Minor	2.5
3.0≧L	0.06≧W	4	4		
2.0≧L	0.08≧W	2	3		
－	0.1＜W	0	1		

X: Length   Y : Width   * : Disregard
Total defects should not exceed 4/module

Parameter 3: Air Bubbles (between glass & polarizer)

Dimension \ Zone	Acceptable Number A	Acceptable Number B	Class Of Defects	Acceptable Level
D≦0.15	*	*	Minor	2.5
0.15＜D≦0.25	2	*		
0.25＜D	0	1		

*: Disregard
Total defects shall not excess 3/module.

| 4 | Uniformity | (1) Pixel shape   (with Dent)

←— 0.152

(2) Pixel shape   (With Projection)

Should not be connected to next pixel

0.152

(3) Pin hole

X

Y (X+Y)/2≦0.02mm

(Less than 0.1mm is no counted)

(4)Deformation

X (X+Y)/2 ≦0.3mm

Y

Total acceptable number ：1/pixel,5/cell |

11. Reliability

■Content of Reliability Test

No.	Test Item	Content of Test	Test Condition	Applicable Standard
Environmental Test				
1	High Temperature storage	Endurance test applying the high storage temperature for a long time.	60℃ 200hrs	____
2	Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-20℃ 200hrs	____
3	High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50℃ 200hrs	____
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	0℃ 200hrs	____
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60℃,90%RH 96hrs	____
6	High Temperature/ Humidity Operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	40℃,90%RH 96hrs	____
7	Temperature Cycle	Endurance test applying the low and high temperature cycle. -20℃ 25℃ 60℃ ←→ 30min 5min 30min 1 cycle	-20℃/60℃ 10 cycles	____
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	____
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msedc 3 times of each direction	____
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	____
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	____

***Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25℃

Bolymin, Inc.

◆◆◆

12. Appendix (Drawing , EL inverter data , KS0066 controller data)

12-1 Drawing

As shown on next page
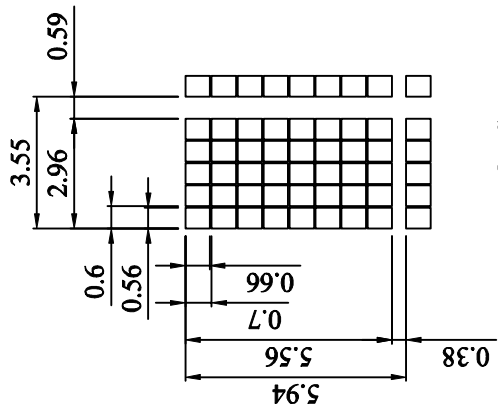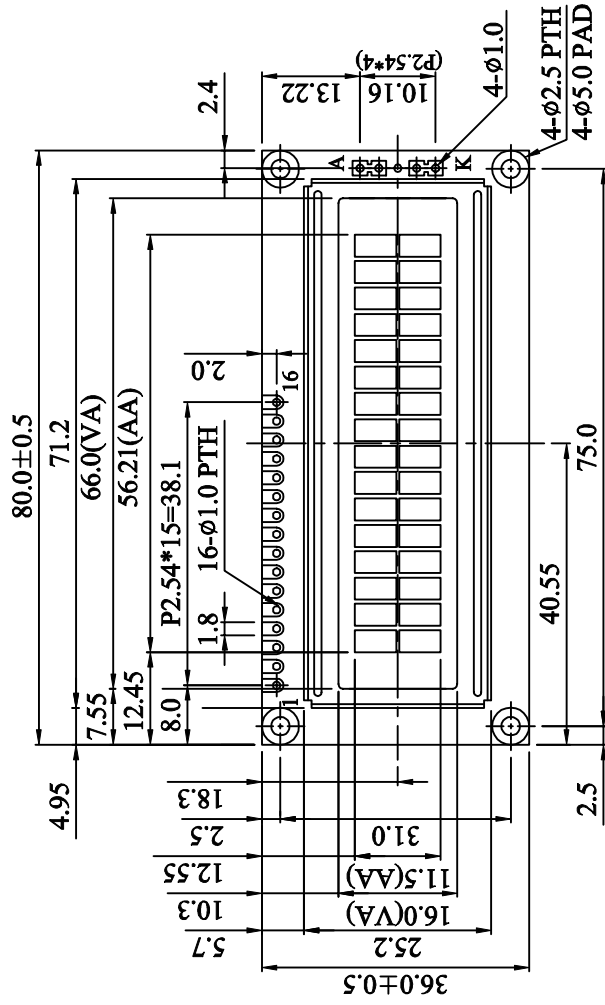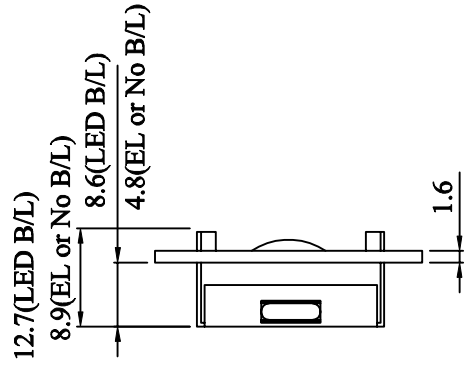
PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	RS
5	R/$\overline{W}$
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	A/Vee
16	K

12.7(LED B/L)
8.9(EL or No B/L)
8.6(LED B/L)
4.8(EL or No B/L)
1.6

2.4
13.22
10.16
(P2.54*4)
4-⌀1.0
4-⌀2.5 PTH
4-⌀5.0 PAD

80.0±0.5
71.2
66.0(VA)
56.21(AA)
P2.54*15=38.1
16-⌀1.0 PTH
2.0
75.0
40.55
1.8
16
7.55
12.45
8.0

4.95
18.3
2.5
12.55
10.3
5.7
11.5(AA)
16.0(VA)
25.2
31.0
2.5
36.0±0.5

0.59
3.55
2.96
0.6
0.56
0.66
0.7
5.56
5.94
0.38

DOT SIZE
SCALE 5/1

Bolymin, Inc.

◆◆◆

12-2 EL inverter data (P/N:IVEL-01)

As shown on next page

Bolymin, Inc.

EL Inverter Specification
P/N: IVEL-01

Customer			Date	99/12/03	Rev	A
Part No	IVEL-01	Item	DC/AC INVERTER	Dwg. No		

Specification						
Input(V/Dc)	Output(V/Ac)	Frequency(Hz)	Input(mA/Dc)	El range(c　)	Test Dummy Load=El(cm^2)	
5V/DC±10%	80V±15%	600Hz±20%	60mA TYP.	30~80 c	22nF//66.6KΩ	

Test Condtion:@25°C. Dc 5V & Standard Dummy Load.
Test equipment :
1. Millimeter : FLUKE 87S Millimeter.
2. Oscilloscope : Tektromic TDS210 Digital Oscilloscope.
3. Power supply : Gw GPC-3030D Dc Power Supply.
4. Load : EPI – LOAD – 01 Multi Range Load.

Operation Temperature : -10°C ~ +70°C

Storage Temperature : -30°C ~ +80°C

Note :
1. Warning : output do not open or short . Inverter may be burnout.
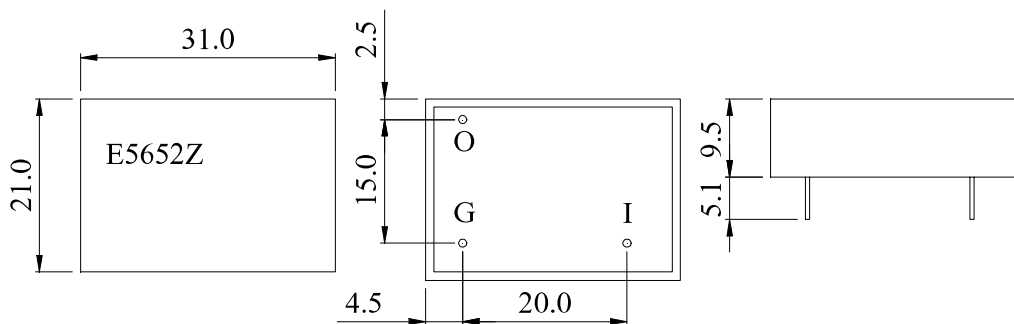
PIN	Description
I	Input DC Voltage.
G	DC/AC ground.
O	Output AC Voltage.

Tolerance:±0.5mm

Prepare:	Checked:	Approval:

12-3. KS0066 controller data

12-3.1 Function description

The LCD display Module is built in a LSI controller, the controller has two 8-bit registers, an instruction register (IR) and a data register (DR).

The IR stores instruction codes, such as display clear and cursor shift, and address information for display data RAM (DDRAM) and character generator (CGRAM). The IR can only be written from the MPU.   The DR temporarily stores data to be written or read from DDRAM or CGRAM. When address information is written into the IR, then data is stored into the DR from DDRAM or CGRAM.   By the register selector (RS) signal, these two registers can be selected.

RS	R/W	Operation
0	0	IR write as an internal operation (display clear, etc.)
0	1	Read busy flag (DB7) and address counter (DB0 to DB7)
1	0	Write data to DDRAM or CGRAM (DR to DDRAM or CGRAM)
1	1	Read data from DDRAM or CGRAM (DDRAM or CGRAM to DR)

Busy Flag (BF)

When the busy flag is 1, the controller LSI is in the internal operation mode, and the next instruction will not be accepted.   When RS=0 and R/W=1, the busy flag is output to DB7.   The next instruction must be written after ensuring that the busy flag is 0.

Address Counter (AC)

The address counter (AC) assigns addresses to both DDRAM and CGRAM

Display Data RAM (DDRAM)

This DDRAM is used to store the display data represented in 8-bit character codes.   Its extended capacity is 80×8 bits or 80 characters.   Below figure is the relationship between DDRAM addresses and positions on the liquid crystal display.

	High bits			Low bits				Example:DDRAM addresses 4E						
AC (hexadecimal)	AC6	AC5	AC4	AC3	AC2	AC1	AC0	1	0	0	1	1	1	0
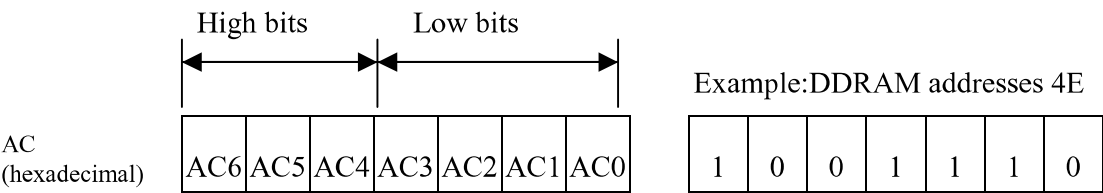
DDRAM Address

◆◆◆

Display position DDRAM address

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F

Example: 2-Line by 16-Character Display

Character Generator ROM (CGROM)

The CGROM generate 5×8 dot or 5×10 dot character patterns from 8-bit character codes. See Table 2.

Character Generator RAM (CGRAM)

In CGRAM, the user can rewrite character by program.   For 5×8 dots, eight character patterns can be written, and for 5×10 dots, four character patterns can be written.

Write into DDRAM the character code at the addresses shown as the left column of table 1. To show the character patterns stored in CGRAM.

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Character Patterns (CGRAM Data)

For 5 * 8 dot character patterns

Character Codes (DDRAM data)		CGRAM Address		Character Patterns (CGRAM data)		
7 6 5 4 3 2 1 0		5 4 3 2 1 0		7 6 5 4 3 2 1 0		
High Low		High Low		High Low		
0 0 0 0 * 0 0 0		0 0 0	0 0 0	* * *	▮▮ 0	Character pattern(1)
			0 0 1	* * *	0 0 0 ▮	
			0 1 0	* * *	0 0 0 ▮	
			0 1 1	* * *	▮ 0	
			1 0 0	* * *	▮ 0 0	
			1 0 1	* * *	0 0 ▮	
			1 1 0	* * *	0 0 0 ▮	
			1 1 1	* * *	0 0 0 0 0	Cursor pattern
0 0 0 0 * 0 0 1		0 0 1	0 0 0	* * *	▮ 0 0 0 ▮	Character pattern(2)
			0 0 1	* * *	0 ▮ 0 0	
			0 1 0	* * *		
			0 1 1	* * *	0 0 ▮ 0 0	
			1 0 0	* * *	▮	
			1 0 1	* * *	0 0 ▮ 0 0	
			1 1 0	* * *	0 0 ▮ 0 0	
			1 1 1	* * *	0 0 0 0 0	Cursor pattern
			0 0 0	* * *		
			0 0 1			
0 0 0 0 * 1 1 1		1 1 1	1 0 0			
			1 0 1			
			1 1 0			
			1 1 1	* * *		

For 5 * 10 dot character patterns

Character Codes (DDRAM data)		CGRAM Address		Character Patterns (CGRAM data)		
7 6 5 4 3 2 1 0		5 4 3 2 1 0		7 6 5 4 3 2 1 0		
High Low		High Low		High Low		
0 0 0 0 * 0 0 0		0 0	0 0 0 0	* * *	0 0 0 0 0	Character pattern
			0 0 0 1	* * *	0 0 0 0 0	
			0 0 1 0	* * *	▮ 0 ▮ 0	
			0 0 1 1	* * *	▮ 0 0 ▮	
			0 1 0 0	* * *	▮ 0 0 0 ▮	
			0 1 0 1	* * *	▮ 0 0 0 ▮	
			0 1 1 0	* * *	▮ 0	
			0 1 1 1	* * *	▮ 0 0 0	
			1 0 0 0	* * *	▮ 0 0 0	
			1 0 0 1	* * *	▮ 0 0 0	
			1 0 1 0	* * *	0 0 0 0 0	Cursor pattern
		1 1 1 1		* * *	* * * * *	

▮ : " High "

◆◆◆

13-3.2 C.G ROM table.table 2

Code J: English – Japanese Font

Upper 4 bit / Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)			@	P	`	p				―	タ	ミ	○	p
LLLH	(2)		!	1	A	Q	a	q			。	ア	チ	ム	á	q
LLHL	(3)		"	2	B	R	b	r			「	イ	ツ	メ	ß	θ
LLHH	(4)		#	3	C	S	c	s			」	ウ	テ	モ	ε	∞
LHLL	(5)		$	4	D	T	d	t			、	エ	ト	ヤ	μ	Ω
LHLH	(6)		%	5	E	U	e	u			・	オ	ナ	ユ	σ	ü
LHHL	(7)		&	6	F	V	f	v			ヲ	カ	ニ	ヨ	ρ	Σ
LHHH	(8)		'	7	G	W	g	w			ァ	キ	ヌ	ラ	g	π
HLLL	(1)		(	8	H	X	h	x			ィ	ク	ネ	リ	√	x̄
HLLH	(2)		)	9	I	Y	i	y			ゥ	ケ	ノ	ル	´	y
HLHL	(3)		*	:	J	Z	j	z			ェ	コ	ハ	レ	j	千
HLHH	(4)		+	;	K	[	k	{			ォ	サ	ヒ	ロ	ˣ	万
HHLL	(5)		,	<	L	¥	l					ャ	シ	フ	ワ	¢	円
HHLH	(6)		-	=	M	]	m	}			ュ	ス	ヘ	ン	£	÷
HHHL	(7)		.	>	N	^	n	→			ョ	セ	ホ	゛	ñ	
HHHH	(8)		/	?	O	_	o	←			ッ	ソ	マ	゜	ö	█

Bolymin, Inc.

◆◆◆

Code E: English - European Font

Upper 4 bit / Lower 4 bit		LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)																
LLLH	CG RAM (2)																
LLHL	CG RAM (3)																
LLHH	CG RAM (4)																
LHLL	CG RAM (5)																
LHLH	CG RAM (6)																
LHHL	CG RAM (7)																
LHHH	CG RAM (8)																
HLLL	CG RAM (1)																
HLLH	CG RAM (2)																
HLHL	CG RAM (3)																
HLHH	CG RAM (4)																
HHLL	CG RAM (5)																
HHLH	CG RAM (6)																
HHHL	CG RAM (7)																
HHHH	CG RAM (8)																

Bolymin, Inc.

◆◆◆

Code C: English - Cyrillic Font

Upper 4 bit / Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM (1)															
LLLH	CG RAM (2)															
LLHL	CG RAM (3)															
LLHH	CG RAM (4)															
LHLL	CG RAM (5)															
LHLH	CG RAM (6)															
LHHL	CG RAM (7)															
LHHH	CG RAM (8)															
HLLL	CG RAM (1)															
HLLH	CG RAM (2)															
HLHL	CG RAM (3)															
HLHH	CG RAM (4)															
HHLL	CG RAM (5)															
HHLH	CG RAM (6)															
HHHL	CG RAM (7)															
HHHH	CG RAM (8)															

13-3.3 Instruction table

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	Execution time (fosc=270K hz)
					Instruction Code							
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "00H" to DDRAM and set DDRAM address to "00H" from AC	1.53ms
Return Home	0	0	0	0	0	0	0	0	1	—	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 μ s
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Set display (D), cursor (C), and blinking of cursor (B) on/off control bit.	39 μ s
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	—	—	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 μ s
Function Set	0	0	0	0	1	DL	N	F	—	—	Set interface data length (DL:8-bit/4-bit), numbers of display line (N:2-line/1-line)and, display font type (F:5×11 dots/5×8 dots)	39 μ s
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 μ s
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 μ s
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μ s
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43 μ s
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43 μ s

＊ "—" ：don't care

13-3.4 Timing characteristics

13-3.4.1 Write Operation
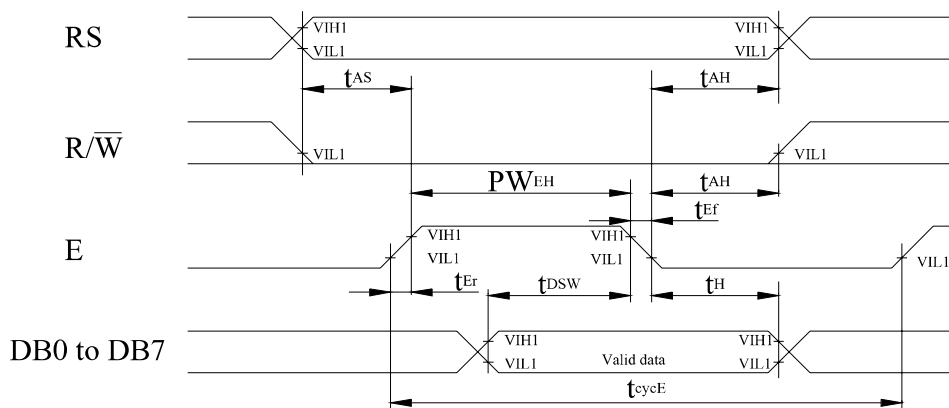
Ta=25℃ , Vdd=5.0±0.5V

Item	Symbol	Min	Typ	Max	Unit
Enable cycle time	t_{cycE}	500	—	—	ns
Enable pulse width (high level)	PW_{EH}	230	—	—	ns
Enable rise/fall time	t_{Er}, t_{Ef}	—	—	20	ns
Address set-up time (RS, R/W to E)	t_{AS}	40	—	—	ns
Address hold time	t_{AH}	10	—	—	ns
Data set-up time	t_{DSW}	80	—	—	ns
Data hold time	t_{H}	10	—	—	ns

13-3.4.2   Read Operation
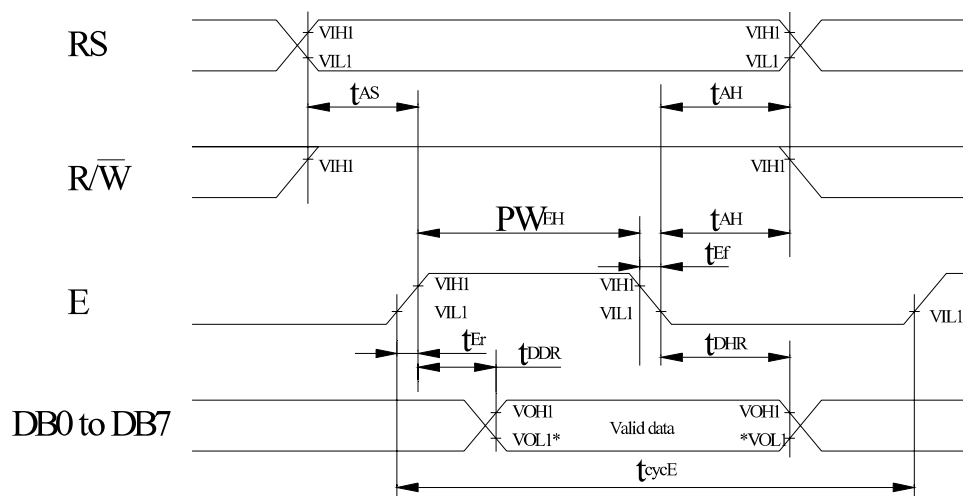
NOTE: *VOL1 is assumed to be 0.8V at 2 MHZ operation.

Ta=25℃,Vdd=5.0±0.5V

Item	Symbol	Min	Typ	Max	Unit
Enable cycle time	t_{cycE}	500	—	—	ns
Enable pulse width (high level)	PW_{EH}	230	—	—	ns
Enable rise/fall time	t_{Er}, t_{Ef}	—	—	20	ns
Address set-up time (RS, R/W to E)	t_{AS}	40	—	—	ns
Address hold time	t_{AH}	10	—	—	ns
Data delay time	t_{DDR}	—	—	100	ns
Data hold time	t_{DHR}	5	—	—	ns

13-3.5 Initializing soft ware of LCM

13-3.5.1 8-bit interface

```
            ┌──────────────┐
            │   Power on    │
            └──────────────┘
                   │
                   ▼
  ┌────────────────────────────────────────────┐
  │ Wait for more than 15 ms after Vcc rises to 4.5 V │
  └────────────────────────────────────────────┘
                   │
                   ▼
```

											BF can not be checked before this instruction.

RS	R/W̄	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	*	*	*	*

Function set (Interface is 8 bits long.)

```
                   │
                   ▼
  ┌────────────────────────────┐
  │   Wait for more than 4.1 ms │
  └────────────────────────────┘
                   │
                   ▼
```

BF can not be checked before this instruction.

RS	R/W̄	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	*	*	*	*

Function set (Interface is 8 bits long.)

```
                   │
                   ▼
  ┌────────────────────────────┐
  │  Wait for more than 100 μs  │
  └────────────────────────────┘
                   │
                   ▼
```

BF can not be checked before this instruction.

RS	R/W̄	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	*	*	*	*

Function set (Interface is 8 bits long.)

```
                   │
                   ▼
```

BF can be checked after the following instructions. When BF is not checked , the waiting time between instructions is longer than execution instruction time.

RS	R/W̄	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	N	F	*	*
0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	1	I/D	S

- Function set (Interface is 8 bits long. Specify the number of display lines and font.)
- The number of display lines and character font can not be changed after this point.
- Display off
- Display clear
- Entry mode set

```
                   │
                   ▼
  ┌────────────────────────────┐
  │     Initialization ends     │
  └────────────────────────────┘
```

8-Bit Ineterface

13-3.5.2 4-bit interface

```
              ┌─────────────────────┐
              │     Power on        │
              └─────────────────────┘
                        │
                        ▼
   ┌────────────────────────────────────────────────┐
   │ Wait for more than 15 ms after Vcc rises to 4.5 V │
   └────────────────────────────────────────────────┘
                        │
                        ▼
```

RS	R/W̄	DB7	DB6	DB5	DB4
0	0	0	0	1	1

BF can not be checked before this instruction.

Function set (Interface is 8 bits long.)

```
                        │
                        ▼
   ┌────────────────────────────────────────────────┐
   │            Wait for more than 4.1 ms             │
   └────────────────────────────────────────────────┘
                        │
                        ▼
```

RS	R/W̄	DB7	DB6	DB5	DB4
0	0	0	0	1	1

BF can not be checked before this instruction.

Function set (Interface is 8 bits long.)

```
                        │
                        ▼
   ┌────────────────────────────────────────────────┐
   │            Wait for more than 100 μs             │
   └────────────────────────────────────────────────┘
                        │
                        ▼
```

RS	R/W̄	DB7	DB6	DB5	DB4
0	0	0	0	1	1

BF can not be checked before this instruction.

Function set (Interface is 8 bits long.)

RS	R/W̄	DB7	DB6	DB5	DB4
0	0	0	0	1	0
0	0	0	0	1	0
0	0	N	F	*	*
0	0	0	0	0	0
0	0	1	0	0	0
0	0	0	0	0	0
0	0	0	0	0	1
0	0	0	0	0	0
0	0	0	0	I/D	S

BF can be checked after the following instructions. When BF is not checked , the waiting time between instructions is longer than execution instruction time.

► Function set (Set interface to be 4 bits long.) Interface is 8 bits in length.

► Function set (Interface is 4 bits long. Specify the number of display lines and character font.) The number of display lines and character font can not be changed after this point.

► Display off
► Display clear
► Entry mode set

```
                        │
                        ▼
   ┌────────────────────────────────────────────────┐
   │              Initialization ends                │
   └────────────────────────────────────────────────┘
```

4-Bit Ineterface