

29. Register Summary

| SSF (SSF) | Address | Name | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | Page |
|--|---|-------|---------------|---------------------------------------|------------------|---------|---------|---------|--------|---------|--------------|
| SEC SEC SPC | \$3F (\$5F) | SREG | 1 | T | Н | S | V | N | Z | С | 8 |
| SOLICION OCRUMENT NEW ONLY | \$3E (\$5E) | SPH | _ | - | _ | _ | SP11 | SP10 | SP9 | SP8 | 11 |
| SAR (589) GICR | \$3D (\$5D) | SPL | SP7 | SP6 | SP5 | SP4 | SP3 | SP2 | SP1 | SP0 | 11 |
| SAI ASA GIFR NIFE NIFE NIFE OCETA COETA | \$3C (\$5C) | OCR0 | Timer/Counter | 0 Output Compar | re Register | | | _ | | | 86 |
| SW 1909 TIMBR | \$3B (\$5B) | GICR | INT1 | INT0 | INT2 | - | _ | - | IVSEL | IVCE | 48, 71 |
| Sept 6969 | \$3A (\$5A) | GIFR | INTF1 | INTF0 | INTF2 | - | - | - | - | - | 71 |
| SYT 677 SPMCK SPMCK SPMCK NOWSB NO | \$39 (\$59) | TIMSK | OCIE2 | TOIE2 | TICIE1 | OCIE1A | OCIE1B | TOIE1 | OCIE0 | TOIE0 | 87, 117, 136 |
| S90 (S90 TVCR | \$38 (\$58) | TIFR | OCF2 | TOV2 | ICF1 | OCF1A | OCF1B | TOV1 | OCF0 | TOV0 | 87, 117, 136 |
| \$55 \$55 MCUCK \$5E \$842 \$841 \$840 \$840 \$850 \$1501 \$1501 \$1500 \$36,89 \$31 \$348 \$480 \$4000 \$1500 \$1500 \$36,89 \$31 \$33 \$383 \$10000 \$10000 \$10000 \$10000 \$10000 \$10000 \$10000 \$10000 \$10000 \$10000 \$100 | \$37 (\$57) | SPMCR | SPMIE | RWWSB | - | RWWSRE | BLBSET | PGWRT | PGERS | SPMEN | 264 |
| SM (SM) MOUCSR JTD SG2 | · · · · | | | | | | | | | | |
| SSS SSS COCRD | ` ' | | ł | | SM1 | | | | 1 | | |
| \$32 (\$32) TONTO TimerCounterO (\$18) \$32 (\$32) \$32 (\$35) \$31 (\$35) | ` ' | | | | 1 | | | | 1 | | |
| \$32 \$30 \$65 70 \$00 | | | | | COM01 | COM00 | WGM01 | CS02 | CS01 | CS00 | |
| S30 (S50) SFIOR | \$32 (\$52) | | | | | | | | | | |
| S90(\$59) | \$31 ⁽¹⁾ (\$51) ⁽¹⁾ | | 1 | | | | | | | | |
| SEP_SHEF TOCRIA COMITAL COMITAL COMITAL COMITAL COMITAL COMITAL COMITAL COMITAL SEP_SHEF TOCRIA COMITAL CO | 000 (050) | | | 7 - 7 | 10700 | | 1 | L sus | Dono | DOD 40 | |
| SPE_EMED_ TOCRED CONTENT CORST WGM13 WGM12 CS12 CS11 CS10 114 | ` ' | | ł | | | | | | 1 | | |
| SZC S4C TONTH | ` ' | | 1 | | COM1B1 | | | | | | |
| SEC_64C TONTIL TimerCounterT - Counter Register Low Byte 116 | ` ' | | | 1 | inter High Dista | WGM13 | WGM12 | US12 | US11 | CS10 | |
| S2B (\$4B) | ` ' | | ł | | <u> </u> | | | | | | |
| S20 (S49) | ` ' | | | | | ah Ryte | | | | | |
| S26(549) | · · · · · | | ł | | - | | | | | | |
| S20 (S48) OCR18 | | | | | • | | | | | | |
| \$27 (\$47) | | | | | _ | | | | | | |
| S26 (546) ICRIL Timer/Counter1 - Input Capture Register Low Byte | ` ' | | 1 | | | • | | | | | |
| \$25 (845) TCCR2 | ` ' | | 1 | | | | | | | | |
| \$22 (\$44) TCNT2 Timer/Counter2 (Bills) 135 523 (\$43) OCR2 Timer/Counter2 Output Compare Register 135 523 (\$43) OCR2 Timer/Counter2 Output Compare Register 135 522 (\$42) ASSR | | | | · · · · · · · · · · · · · · · · · · · | 1 | 1 | WGM21 | CS22 | CS21 | CS20 | |
| \$22 (\$42) | | | | | OCIVIZ | OGIVIZO | WOME | COZZ | 0021 | 0020 | |
| \$22 (841) | ` ' | | | | re Register | | | | | | |
| \$21 (\$41) | ` ' | | | _ | _ | _ | AS2 | TCN2UB | OCR2UB | TCR2UB | |
| S20° (\$40)° UBRRH | ` ' | | _ | _ | _ | WDTOE | | | | | |
| SUCC STE | | | URSEL | _ | _ | _ | | | 1 | | |
| \$1E (\$3E) | \$20 ⁽²⁾ (\$40) ⁽²⁾ | | | UMSEL | UPM1 | UPM0 | USBS | 1 | | UCPOL | |
| \$1D (\$3D) | \$1F (\$3F) | EEARH | _ | - | _ | _ | - | - | EEAR9 | EEAR8 | 20 |
| S1C (S3C) EECR - | \$1E (\$3E) | EEARL | EEPROM Add | ress Register Lov | w Byte | | | | - | | 20 |
| \$18 (\$38) PORTA PORTA7 PORTA6 PORTA5 PORTA4 PORTA3 PORTA2 PORTA1 PORTA0 66 \$14 (\$3A) DDRA DDA7 DDA6 DDA5 DDA4 DDA3 DDA2 DDA1 DDA0 66 \$19 (\$39) PINA PINA7 PINA6 PINA5 PINA5 PINA4 PINA3 PINA2 PINA1 PINA0 66 \$18 (\$38) PORTB PORTB7 PORTB6 PORTB5 PORTB4 PORTB3 PORTB2 PORTB1 PORTB0 67 \$17 (\$37) DDRB DDB7 DDB6 DDB5 DDB4 DDB3 DDB2 DDB1 DDB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC7 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PORTD6 PORTD5 PINC4 PINC3 PINC2 PINC1 PINC0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 DDD2 DDD1 DDD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 DDD2 DDD1 DDD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 PORTD2 PORTD0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 67 \$10 (\$30) PIND PIND7 PORTD6 PORTD6 PORTD5 PORTD4 PORTD9 PORTD1 PORTD0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 68 \$0F (\$2F) SPBR SPIF WCOL — — — — — SPI2X 145 \$0E (\$2E) SPSR SPIF WCOL — — — — — SPI2X 145 \$0E (\$2E) SPSR SPIF SPIF WCOL — — — — — SPI2X 145 \$0B (\$2B) UCSRA RXC TXC UDRE FE DOR PE U2X MPCM 168 \$0A (\$2A) UCSRB RXCIE TXCIE UDRIE RXEN TXEN UCS22 RXB8 TXB8 169 \$09 (\$29) UBRRL USART IJO Data Register \$0B (\$2B) UGSRA RXC TXC UDRE FE DOR PE U2X MPCM 168 \$0A (\$2A) UCSRB RXCIE TXCIE UDRIE RXEN TXEN UCS22 RXB8 TXB8 169 \$0A (\$2B) ADCSRA ADEN ADSC ADATE ADIF ADIF ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$2B) ADCSA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$2B) ADCSA ADCN ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$2B) ADCSA ADCN ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 225 \$04 (\$2B) ADCH ADC DATA Register USByte | \$1D (\$3D) | EEDR | EEPROM Data | a Register | | | | | | | 21 |
| \$14 (\$34) DDRA DDA7 DDA6 DDA5 DDA4 DDA3 DDA2 DDA1 DDA0 66 \$19 (\$39) PINA PINA7 PINA6 PINA5 PINA4 PINA3 PINA2 PINA1 PINA0 66 \$18 (\$38) PORTB PORTB7 PORTB6 PORTB5 PORTB4 PORTB3 PORTB2 PORTB1 PORTB0 67 \$17 (\$37) DDRB DDB7 DDB6 DDB5 DDB4 DDB3 DDB2 DDB1 DDB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC7 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PINC6 PINC5 PINC4 PINC3 PINC2 PINC1 PINC0 67 \$12 (\$32) PORTD PORTD7 PORTD6 PORTD5 PORTD4 PORTD3 PORTD2 PORTD1 PORTD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 DDD2 DDD1 DDD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 DDD2 DDD1 DDD0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 68 \$0F (\$2F) SPDR SPI Data Register 145 \$0C (\$2E) SPSR SPIF WCOL — — — — — — — — SPIZX 145 \$0D (\$2C) UDR USART I/O Data Register 145 \$0C (\$2C) UDR USART I/O Data Register 145 \$0A (\$2A) UCSRB RXCE TXCE UDRIE RXEN TXEN UCSZ2 RXB8 TXB8 169 \$09 (\$29) UBRRL USART BBud Rate Register Low Byte 167 \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIE ACIC ACIS1 ACIS0 226 \$04 (\$24) ADCL ACC BARBESTER DDRA TWORN BYTE DATE ACID ADPS ADES ADES ADES ADES ADEN ADS ADES ADES ADES ADES ADES ADES ADES | \$1C (\$3C) | EECR | _ | _ | - | - | EERIE | EEMWE | EEWE | EERE | 21 |
| \$19 (\$39) PINA PINA7 PINA6 PINA5 PINA4 PINA3 PINA2 PINA1 PINA0 66 \$18 (\$38) PORTB PORTB7 PORTB6 PORTB5 PORTB4 PORTB3 PORTB2 PORTB1 PORTB0 67 \$17 (\$37) DDRB DDB7 DDB6 DDB5 DDB5 DDB4 DDB3 DDB2 DDB1 DDB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC7 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PINC6 PORTD5 PORTD4 PORTD3 PORTD2 PORTD1 PORTD0 67 \$11 (\$31) DDRD DD7 DDD6 DD5 DDD4 DDD3 DDC2 DDC1 DDC0 67 \$11 (\$31) DDRD DD7 DDD6 DD5 DDD4 DDD3 DDD2 PORTD1 PORTD0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 68 \$0F (\$2F) SPDR SPID Ata Register 145 \$0C (\$2C) UDR USART I/O DATA Register 143 \$0C (\$2C) UDR USART I/O DATA REGISTER PORTD4 PORTD5 PORTD4 PORTD5 PIND1 PIND0 68 \$0A (\$2A) UCSRB RXCIE TXCIE UDRIE RXEN TXEN UCS22 RXB8 TXB8 169 \$08 (\$28) UBRRL USART BAU RATE Register LOW Byte 171 \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIE ACIE ACIE ACIE ACIE ACIE | \$1B (\$3B) | PORTA | PORTA7 | PORTA6 | PORTA5 | PORTA4 | PORTA3 | PORTA2 | PORTA1 | PORTA0 | 66 |
| \$18 (\$38) PORTB PORTB7 PORTB6 PORTB5 PORTB4 PORTB3 PORTB2 PORTB1 PORTB0 67 \$17 (\$37) DDRB DDB7 DDB6 DDB5 DDB4 DDB3 DDB2 DDB1 DDB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC6 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PINC6 PINC5 PINC4 PINC3 PINC2 PINC1 PINC0 67 \$12 (\$32) PORTD PORTD7 PORTD6 PORTD5 PORTD4 PINC3 PINC2 PINC1 PINC0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD7 DDD6 DDD5 DDD4 DDD9 PORTD1 PORTD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD9 DDD0 DDD DDD DDD0 DD0 DD0 DD0 DD0 | \$1A (\$3A) | DDRA | DDA7 | DDA6 | DDA5 | DDA4 | DDA3 | DDA2 | DDA1 | DDA0 | 66 |
| \$17 (\$37) DDRB DDB7 DDB6 DDB5 DDB4 DDB3 DDB2 DDB1 DDB0 67 \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC7 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PINC6 PINC5 PINC4 PINC3 PINC2 PINC1 PINC0 67 \$12 (\$32) PORTD PORTD7 PORTD6 PORTD5 PORTD4 PORTD3 PORTD2 PORTD1 PORTD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 PORTD2 PORTD1 PORTD0 67 \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 DDD0 67 \$06 (\$25) SPDR SPI Data Register \$06 (\$25) SPSR SPIF WCOL SPIZX 145 \$00 (\$20) UDR USART I/O Data Register \$07 (\$20) UDR USART I/O Data Register \$08 (\$28) UCSRA RXC TXC UDRE FE DOR PE UZX MPCM 168 \$09 (\$29) UBRRL USART BAUR ARE REgister LOWED SPIR SPIS WIGH RXEN TXEN UCSZ2 RXB8 TXB8 169 \$09 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$26) ADCSRA ADEN ADC ADC AD AD ADC | · ' ' | PINA | PINA7 | PINA6 | PINA5 | PINA4 | PINA3 | PINA2 | PINA1 | PINA0 | 66 |
| \$16 (\$36) PINB PINB7 PINB6 PINB5 PINB4 PINB3 PINB2 PINB1 PINB0 67 \$15 (\$35) PORTC PORTC7 PORTC6 PORTC5 PORTC4 PORTC3 PORTC2 PORTC1 PORTC0 67 \$14 (\$34) DDRC DDC7 DDC6 DDC5 DDC4 DDC3 DDC2 DDC1 DDC0 67 \$13 (\$33) PINC PINC7 PINC6 PINC5 PINC4 PINC3 PINC2 PINC1 PINC0 67 \$12 (\$32) PORTD PORTD7 PORTD6 PORTD5 PORTD4 PORTD3 PORTD2 PORTD1 PORTD0 67 \$11 (\$31) DDRD DDD7 DDD6 DDD5 DDD4 DDD3 DDD2 DDD1 DDD0 67 \$11 (\$31) DDRD PIND7 PIND6 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 68 \$0F (\$2F) SPDR SPI Data Register 145 \$0E (\$2E) SPSR SPIF WCOL SPI2X 145 \$0D (\$2D) SPCR SPIE SPE DORD MSTR CPOL CPHA SPR1 SPR0 143 \$0C (\$2C) UDR USART I/O Data Register 167 \$08 (\$2B) UCSRA RXC TXC UDRE FE DOR PE U2X MPCM 168 \$09 (\$29) UBRRL USART BADA RAE Register 171 \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIC ACIS1 ACIS0 206 \$07 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$25) ADCH ADC Data Register 1 ADIF ADIF ADIF ADIF ADIF ADIF ADIF ADIF | | | | | | | | | | | |
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| \$11 (\$31) | | | | | | | | | | | |
| \$10 (\$30) PIND PIND7 PIND6 PIND5 PIND5 PIND4 PIND3 PIND2 PIND1 PIND0 68 \$0F (\$2F) SPDR SPI Data Register 145 \$0E (\$2E) SPSR SPIF WCOL — — — — — — — — — SPI2X 145 \$0D (\$2D) SPCR SPIE SPE DORD MSTR CPOL CPHA SPR1 SPR0 143 \$0C (\$2C) UDR USART I/O Data Register 167 \$0B (\$2B) UCSRA RXC TXC UDRE FE DOR PE U2X MPCM 168 \$0A (\$2A) UCSRB RXCIE TXCIE UDRIE RXEN TXEN UCSZ2 RXB8 TXB8 169 \$09 (\$29) UBRRL USART Baud Rate Register Low Byte 171 \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIC ACIS1 ACIS0 206 \$07 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$26) ADCSRA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$25) ADCH ADC Data Register High Byte 225 \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register | ` ' | | | | | 1 | | | | | |
| \$0F (\$2F) | | | | | | | | | | | |
| \$0E (\$2E) | · ' ' | | | 1 | PINDS | PIND4 | PIND3 | PINDZ | LINDJ | PINDU | |
| \$0D (\$2D) | ` ' | | | 1 | | | | | | SDION | |
| \$0C (\$2C) UDR USART I/O Data Register | | | | | DORD | MSTR | | | SPR1 | | |
| \$0B (\$2B) | | | | | סויס | NISIK | J OF OL | _ OFTIA | SEINT | OF INU | |
| \$0A (\$2A) | ` ' | | | 1 | LIDRE | FF | DOB | PE | H2X | MPCM | |
| \$09 (\$29) UBRRL USART Baud Rate Register Low Byte 171 \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIC ACIS1 ACIS0 206 \$07 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$26) ADCSRA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$25) ADCH ADC Data Register High Byte 225 225 225 225 225 225 225 225 203 3(\$23) TWDR Two-wire Serial Interface Data Register 203 | · · · · | | | | | | | | | | |
| \$08 (\$28) ACSR ACD ACBG ACO ACI ACIE ACIC ACIS1 ACIS0 206 \$07 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$26) ADCSRA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$25) ADCH ADC Data Register High Byte 225 \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | | | | | | IOLEN | IXLIN | 00022 | 10000 | 17,00 | |
| \$07 (\$27) ADMUX REFS1 REFS0 ADLAR MUX4 MUX3 MUX2 MUX1 MUX0 222 \$06 (\$26) ADCSRA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$25) ADCH ADC Data Register High Byte 225 \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | ` ' | | | 1 | | ACI | ACIE | ACIC | ACIS1 | ACISO | |
| \$06 (\$26) ADCSRA ADEN ADSC ADATE ADIF ADIE ADPS2 ADPS1 ADPS0 224 \$05 (\$25) ADCH ADC Data Register High Byte 225 \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | ` ' | | | | | | | | | | |
| \$05 (\$25) ADCH ADC Data Register High Byte 225 \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | ` ' | | | | 1 | | | | | | |
| \$04 (\$24) ADCL ADC Data Register Low Byte 225 \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | ` ' | | | | ,, (1) | , | , | , 02 | , | 7.21.00 | |
| \$03 (\$23) TWDR Two-wire Serial Interface Data Register 203 | ` ' | | | | | | | | | | |
| | | | | | Register | | | | | | |
| | \$02 (\$22) | TWAR | TWA6 | TWA5 | TWA4 | TWA3 | TWA2 | TWA1 | TWA0 | TWGCE | 204 |

| Address | Name | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | Page |
|-------------|------|---|-------|-------|-------|-------|-------|-------|-------|------|
| \$01 (\$21) | TWSR | TWS7 | TWS6 | TWS5 | TWS4 | TWS3 | - | TWPS1 | TWPS0 | 203 |
| \$00 (\$20) | TWBR | Two-wire Serial Interface Bit Rate Register | | | | | | | | 201 |

Notes: 1. When the OCDEN Fuse is unprogrammed, the OSCCAL Register is always accessed on this address. Refer to the debugger specific documentation for details on how to use the OCDR Register.

- 2. Refer to the USART description for details on how to access UBRRH and UCSRC.
- 3. For compatibility with future devices, reserved bits should be written to zero if accessed. Reserved I/O memory addresses should never be written.
- 4. Some of the Status Flags are cleared by writing a logical one to them. Note that the CBI and SBI instructions will operate on all bits in the I/O Register, writing a one back into any flag read as set, thus clearing the flag. The CBI and SBI instructions work with registers \$00 to \$1F only.

