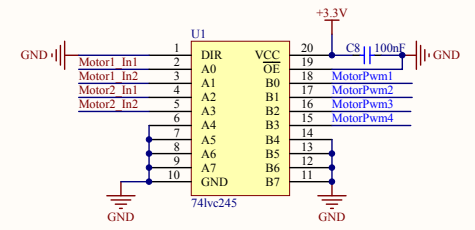
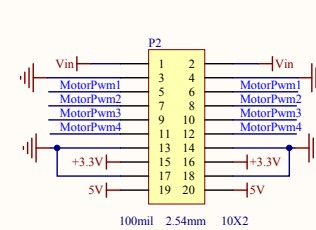


The diagram shows a motor driver circuit for a 12V motor. It consists of two 7960 PNP transistors (IC1 and IC2) and a 555 timer (IC3). The 555 timer is configured as a monostable multivibrator, triggered by a push button (BTN7960) and a 10k resistor (R10). The timer's output (pin 8) drives the base of IC1. IC1's emitter is connected to Vbat, and its collector drives the base of IC2. IC2's emitter is also connected to Vbat, and its collector drives the motor (MOTOR1). The motor's other terminal is connected to GND. The circuit includes various passive components: capacitors C1 (100nF), C2 (10uF), C3 (100nF), C4 (10k), C5 (10k), C6 (10k), C7 (10k), C8 (10k), C9 (10k), C10 (10k), C11 (10k), C12 (10k), C13 (10k), C14 (10k), C15 (10k), C16 (10k), C17 (10k), C18 (10k), C19 (10k), C20 (10k), C21 (10k), C22 (10k), C23 (10k), C24 (10k), C25 (10k), C26 (10k), C27 (10k), C28 (10k), C29 (10k), C30 (10k), C31 (10k), C32 (10k), C33 (10k), C34 (10k), C35 (10k), C36 (10k), C37 (10k), C38 (10k), C39 (10k), C40 (10k), C41 (10k), C42 (10k), C43 (10k), C44 (10k), C45 (10k), C46 (10k), C47 (10k), C48 (10k), C49 (10k), C50 (10k), C51 (10k), C52 (10k), C53 (10k), C54 (10k), C55 (10k), C56 (10k), C57 (10k), C58 (10k), C59 (10k), C60 (10k), C61 (10k), C62 (10k), C63 (10k), C64 (10k), C65 (10k), C66 (10k), C67 (10k), C68 (10k), C69 (10k), C70 (10k), C71 (10k), C72 (10k), C73 (10k), C74 (10k), C75 (10k), C76 (10k), C77 (10k), C78 (10k), C79 (10k), C80 (10k), C81 (10k), C82 (10k), C83 (10k), C84 (10k), C85 (10k), C86 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The diagram illustrates a motor speed control circuit. It features two operational amplifiers, IC3 and IC4, both of type BIN7960. IC3 is configured as a non-inverting amplifier with a feedback network consisting of resistors R6 (10K), R7 (10K), and R8 (10K), and a capacitor C12 (1nF). Its input is connected to 'Motor2_In1' and its output to 'Current_Feedback2'. IC4 is configured as a non-inverting amplifier with a feedback network consisting of resistors R9 (10K), R10 (10K), and R11 (10K), and a capacitor C13 (1nF). Its input is connected to 'Current_Feedback1' and its output to 'Motor2_In2'. A central motor driver IC, labeled 'MOTOR2', is connected to the outputs of both op-amps. The motor driver has a feedback network consisting of resistors R12 (10K), R13 (470K), R14 (510K), R15 (510K), and R16 (510K), and a capacitor C14 (100nF). The motor driver is powered by a Vbat supply and has a ground connection. The motor driver's output is connected to the motor, labeled 'MOTOR2'.

The diagram shows a 5V to 5V step-up converter circuit. The input is a 5V source connected to the FB pin of the MP2359 IC via a 9.53K resistor (R22). The SW pin is connected to the output of the IC, which is also connected to a 10nF capacitor (C17) to GND. The output is taken from the SW pin through an inductor (L1, 4.7uH) and a 10uF capacitor (C18) to GND. The output voltage is 5V. The IC is powered by a 5V source through a 9.9K resistor (R24) and a 10uF capacitor (C22) to GND. The IC also has a 100nF capacitor (C23) connected to its VCC pin to GND. The IC is labeled with pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100.



Title				FRDM KL25Z			
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