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HW14: ASM control flow (CS220-06)

As you complete the following conversions from C/C++ code to SPU assembly, be sure only the variables modified in the original code are modified in your code.

1) Given the snippet of C code below, write the equivalent SPU assembly code.

2) Given the snippet of C code below, write the equivalent SPU assembly code.

```
if(W < 5) {
     W++;
}
else {
     W--;
}
load A, !W

brage A, 5, else
inca
bra done

!else
deca
!done

stor !W , A</pre>
```

3) Given the snippet of C code below, write the equivalent SPU assembly code.

```
if((Q * 2) - 10 < Q) {
     printf("T");
                                     load A, !Q
}
                                     rcpy B, A
else {
                                                      #equivalent of *2
                                     add B, B, B
     printf("F");
                                     sub B, B, 10
}
                                     brage B, Q, else
                                     print
                                     done
                                      !else
                                     print
                                      !done
```

4) Given the snippet of C code below, write the equivalent SPU assembly code.

```
Given the snippet of C code below, write the equivalent SPU assembly code. Don't
forget the true block of the if should be at the bottom of the code.
                                                             load A, !E
                                                             load B, !M
if(E != M \&\& E > 5) {
      E++;
                                                             brane !e_neq_m
}
                                                             else
else {
      M--;
}
                                                             !e_neq_m
                                                             brag A, 5, true_case
                                                             else
                                                             !else
                                                             decb
                                                             !done
                                                             !true case
                                                             inca
                                                             stor !E, A
                                                             stor !M, B
6) Given the snippet of C code below, write the equivalent SPU assembly code.
void times5(int *num) {
      *num *= 5;
      return;
                         !times5
}
                        push A
                        push B
                                                       seta '0'
                        push C
                                                       stor !Q, A
                        loadp B
                                                       seta !Q
                         seta 5
                                                       rcpy AR, A
                         add
                                                       call !times5
                         add
                         add
                                                       sys 0
                         add
                         add
                        pop C
                        pop B
                        pop A
```

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EXTRA CREDIT

7) Given the snippet of C code below, write the equivalent SPU assembly code. Do not use a variable for "i" as it is temporary. Also, do not use register B... mwah ha HA HAAAAAA!

```
for(int i = 10; i != 0; i--) {
    // do some stuff
}
```