

Operating Systems.

Final Exam

Uzair Ahmed Syighal

18K-0179

Section - 4B

Letter Grade.

Q1

① Drawbacks of many-to-one thread model:

- ⇒ In this model a thread blocking system call would block the entire process of the running thread. Because we have one one kernel level thread available.
- ⇒ Multiple threads of a process are not able to access the kernel at the same time ie no parallelism on threads.

② Micro kernel

MonoLithic

| | |
|---|---|
| ⇒ Provides low level address space management, IPC to implement the OS and thread management. | ⇒ OS works in the kernel space and direct access of hardware. |
| ⇒ OS and kernel are separated. | ⇒ Kernel contains the OS services |
| ⇒ It's slow | ⇒ It's fast |
| ⇒ If one component fails other will run | ⇒ If one component fails the entire system will be affected. |

10

- New features can be added easily. → It's difficult to add new features.
- Quite small → Quite large

11

(iii). When a lower priority process holds a lock needed by a high-priority process this problem can be solved by Priority Inheritance Protocol. In this process, the lower-priority process executes its critical section at an elevated priority level, after execution of that process its priority returns to its original state.

12

(iv) User Level Thread.

Thread is a light weight process uses less resources compared to the process.

PROS

- Thread switching does not involve OS.
- It consumes less resources
- All threads share the data of the process.
- If one thread is blocked other can run.



Cons.

- Lack of coordination b/w threads and os kernel.
- Process gets only time slice irrespective of the no. of threads.
- Requires non-blocking system call.

Kernel Level threads.

Threads managed by kernel.

Pros.

- Scheduler decides to give more time to a process having large number of threads.
- Good for applications that frequently blocks.

Cons

- Slow and inefficient
- Due to extensive sharing among kernel level threads, there is an issue of security.

~~Q(V)~~

Data Parallelism

→ Some operations are performed on different subsets of same data.

→ Synchronous

→ Speedup is more as there is only one execution thread operating on all sets of data.

→ Parallelization is proportional to the input data size

→ Different operations are performed on the same or different data.

→ Asynchronous.

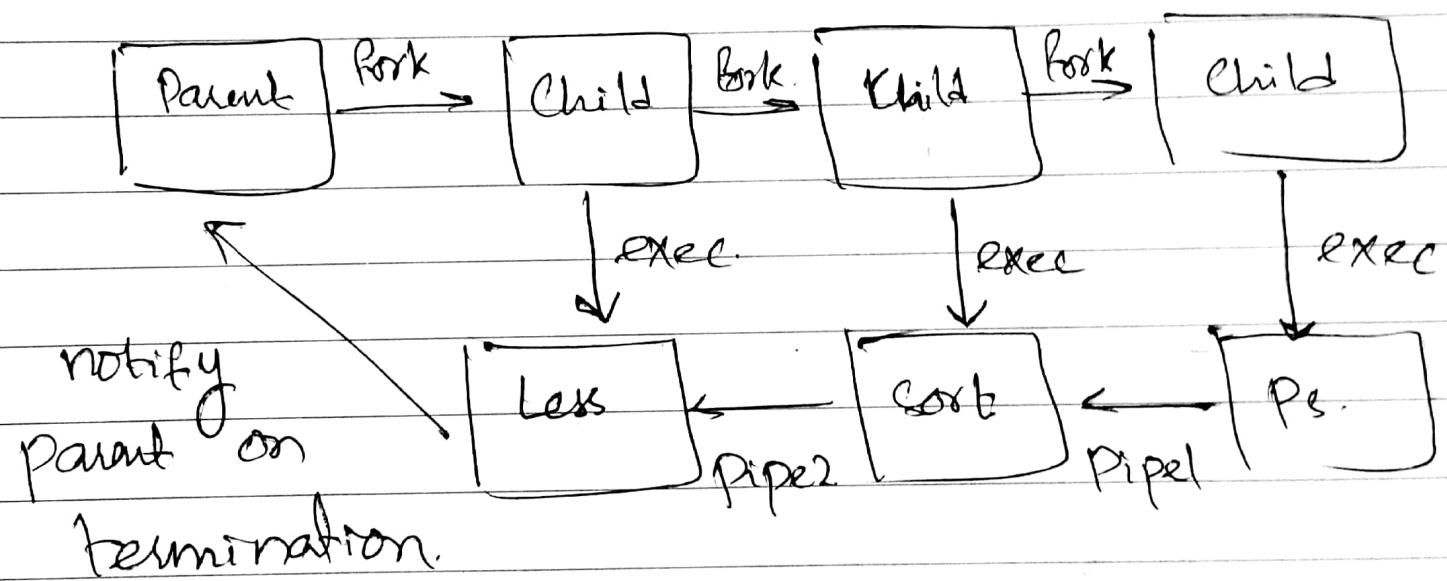
→ Speedup is less as each processor will be busy with a different thread.

→ Parallelization is proportional to the number of independent tasks.

18/12-0179

Date

Q2 ps | sort | less.



Q3Semaphore = ~~lock~~ lock=1

Enter-Bridge() {

wait (lock);

// crosses bridge

{

Exit-Bridge() {

signal (lock);

}

This pseudo code implementation can prevent deadlock caused by vermont farmers.

| Process | AT | BT | Priority. | WT | CT | TAT |
|------------------|----|-----|-----------|----------|----|-----------|
| P ₁) | 0 | 880 | 5 | 19-0=19 | 27 | 27 |
| P ₂) | 1 | 430 | 3 | 13-1=12 | 16 | 15 |
| P ₃) | 2 | 80 | 1 | 12-0=12 | 6 | 4 |
| P ₄) | 3 | 70 | 2 | 6-3=3 | 13 | 10 |
| P ₅) | 4 | 30 | 4 | 16-4=12 | 19 | 15 |
| | | | | Avg: 9.2 | | Avg: 14.2 |

| P ₁) | P ₂) | P ₃) | P ₄) | P ₅) | P ₁) | P ₂) | P ₃) | P ₄) |
|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|
| 0 | 1 | 2 | 7 | 14 | 17 | 20(20-21) | 28 | |
| (0-2) | (1-1) | (2-6) | (7-13) | (14-16) | (17-19) | | | |

| Process | AT | BT | WT | CT | TAT | TQ=3 |
|------------------|----|--------|----------|----|--------|------|
| P ₁) | 0 | 9830 | 23+23=26 | 26 | 26 | |
| P ₂) | 1 | 430 | 17-1=16 | 18 | 17 | |
| P ₃) | 2 | 80 | 18-2=16 | 26 | 18 | |
| P ₄) | 3 | 70 | 26-3=23 | 27 | 24 | |
| P ₅) | 4 | 30 | 11-4=7 | 14 | 16 | |
| | | Avg=17 | | | Avg=18 | |

| P ₁) | P ₂) | P ₃) | P ₄) | P ₅) | P ₁) | P ₂) | P ₃) | P ₄) | P ₁) | P ₂) |
|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|
| 0 | 3 | 6 | 9 | 12 | 15 | 18 | 19 | 21 | 24 | 27 |
| (0-2) | (3-5) | (6-9) | (9-11) | (12-14) | (15-17) | (18-20) | (21-23) | (24-26) | (27-29) | |

181K-0179

Date 20

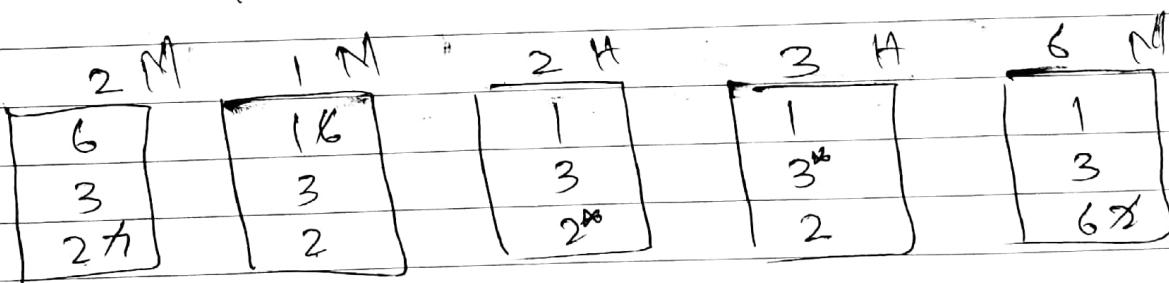
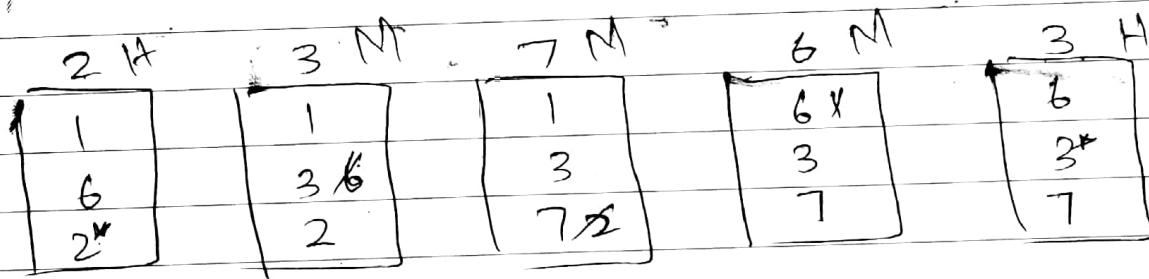
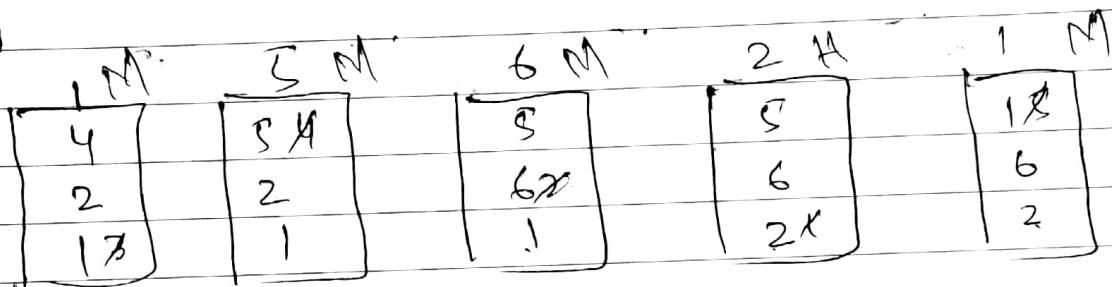
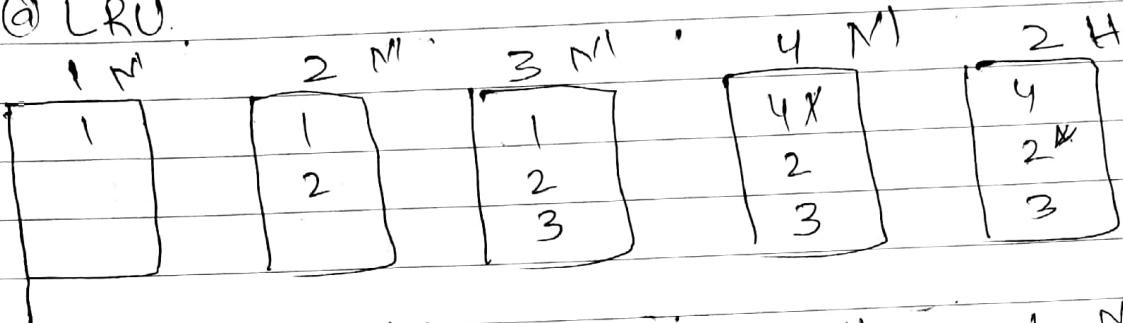
III). SRTF

| Process | A.T | B.T | W.T | C.T | TAT. |
|----------------|-----|-----|---------|-----|------------|
| P ₁ | 0 | 8 | 19-0=19 | 27 | 27 |
| P ₂ | 1 | 4 | 0-1=0 | 4 | 3 |
| P ₃ | 2 | 8 | 7-2=5 | 12 | 10 |
| P ₄ | 3 | 7 | 20-3=17 | 27 | 28 |
| P ₅ | 4 | 3 | 4-4=0 | 7 | 3 |
| | | | | | Aug = 12.8 |
| | | | | | Aug = 6.6. |

| P ₁ | P ₂ | P ₅ | P ₃ | P ₄ | P ₂ |
|----------------|----------------|----------------|----------------|----------------|----------------|
| 0 | 1 | 5 | 8 | 13 | 20 |
| (0-0) | (1-4) | (5-7) | (8-12) | (13-20) | (21-27) |

Q8

@ LRU.



Page Hits = 5

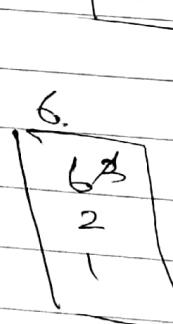
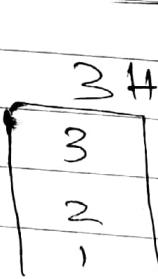
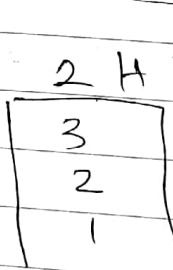
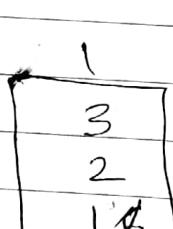
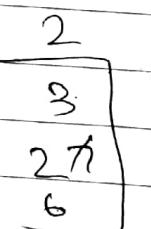
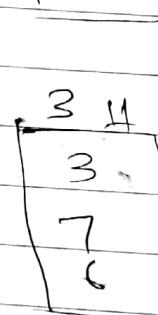
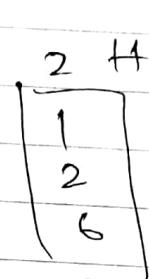
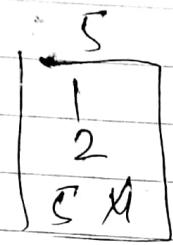
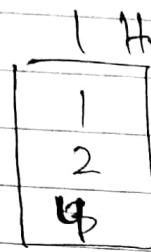
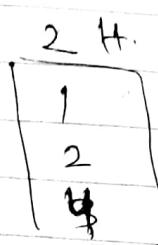
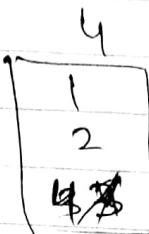
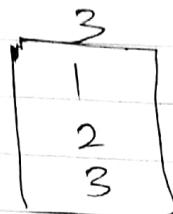
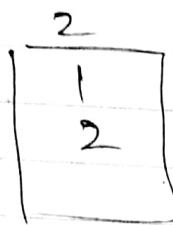
Page Faults = 15

18K-0178

Date

20

(b) Optimal



Page Hits = 9

Page Faults = 11

c) Second Chance

| | 1 | 2 | 3 | 4 | 2 H |
|---|---|-----|-----|-------|-----|
| 0 | 1 | 0 1 | 0 1 | 0 4 X | 0 4 |
| | | 0 2 | 0 2 | 0 2 | 1 2 |
| | | | 0 3 | 0 3 | 0 3 |
| | | | | | |

| | 1 | 5 | 6 | 2 X | 1 |
|---|-----|-------|-------|-------|-------|
| 0 | 4 | 0 3 X | 0 5 | 0 5 | 0 1 X |
| 0 | 2 | 0 2 | 0 6 X | 0 6 | 0 6 |
| 0 | 1 X | 0 1 | 0 1 | 0 2 X | 0 2 |
| | | | | | |

| | 2 H | 3 | 7 | 6 | 3 |
|---|-----|-------|-------|-------|-------|
| 0 | 1 | 0 1 X | 0 7 X | 0 7 | 0 7 |
| 0 | 6 | 0 3 X | 0 3 | 0 8 X | 0 6 |
| 1 | 2 | 1 2 | 0 2 | 0 2 | 0 3 X |
| | | | | | |

| | 2 | 1 | 2 H | 3 H | 6. |
|---|-----|-------|-----|-----|-------|
| 0 | 2 X | 0 2 | 1 2 | 1 2 | 0 2 |
| 0 | 5 | 0 1 6 | 0 1 | 0 1 | 0 6 X |
| 0 | 3 | 0 3 | 0 3 | 1 3 | 0 3 |
| | | | | | |

Page hits = 4

Page faults = 16

1812-0178.

Dat

Q6

Address.

Page size.

~~XP~~

↓ (offset).

| | | | |
|---------|-----------------|-----|------|
| 3085 | 4096 | 0 | 3085 |
| 42095 | 4096 | 10 | 1135 |
| 215201 | 4096 | 52 | 2209 |
| 650000 | 4096 | 158 | 2832 |
| 2000001 | 4096 | 488 | 1153 |
| | 4096 | | |

18K-0179.

Date 20

Q7

Logical Address Space = 32 Bits.

Page size = $4KB = 2^2 \cdot 2^{10} = 2^{12}$

Physical Address Space = $512 MB = 2^8 \cdot 2^{20} = 2^{28}$ Bits

Logical Address bits: $\frac{2^{32}}{2^{12}} = 2^{20}$ Bits.

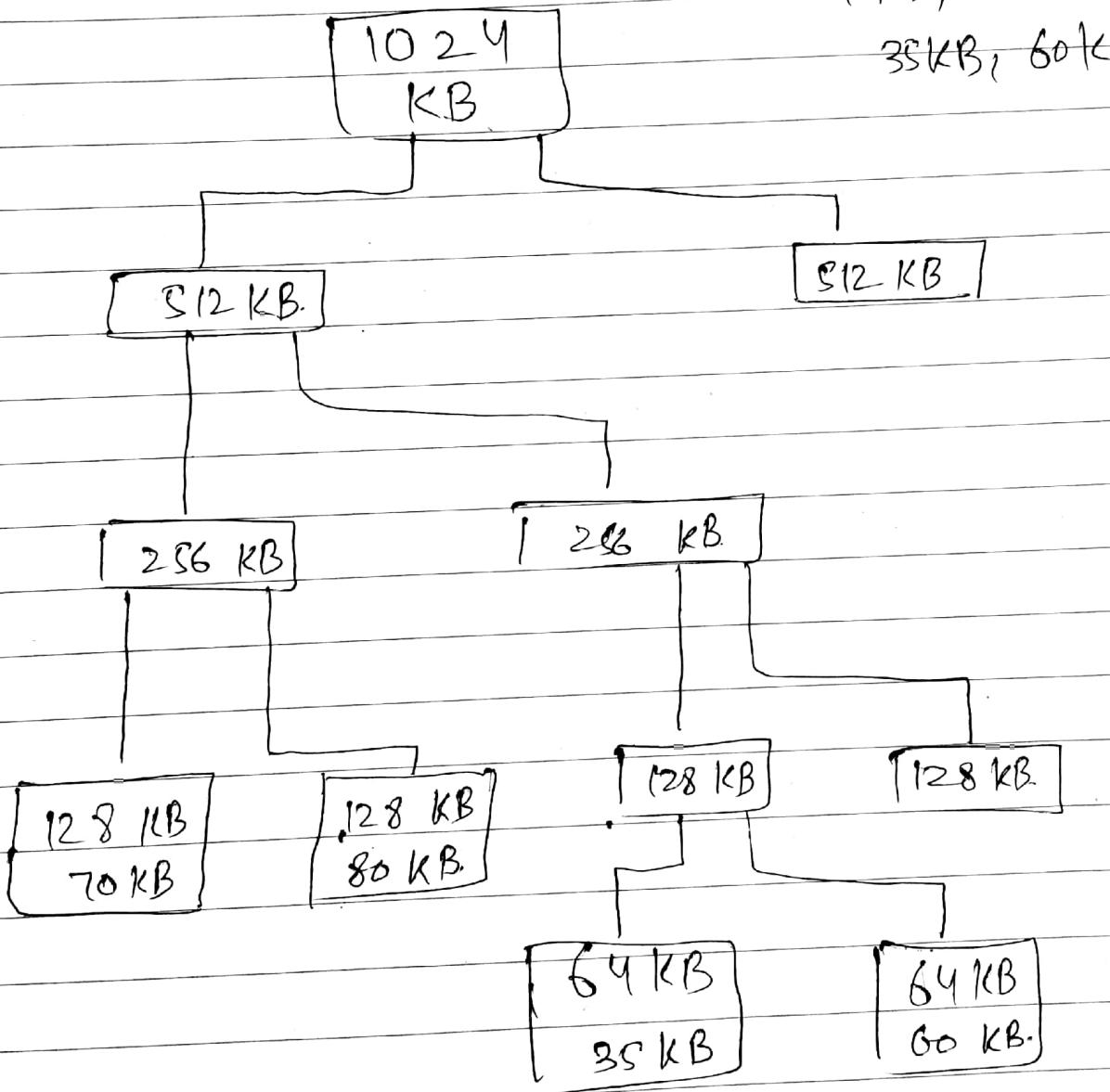
Physical Address bits = $\frac{2^{28}}{2^{12}} = 2^{16}$ Bits.

Q8

$$1 \text{ MB} = 1024 \text{ KB}$$

70 KB, 80 KB

35 KB, 60 KB



18K-0178.

Date 20

Q8

Access time = 40ns.

Hit rate = 90% = 0.9.

Miss rate = 0.1

Search time = 10ns.

EAT = ?

$$EAT = (0.9)(40+10) + (0.1 * (40+40+10))$$

$$EAT = 45 + 9$$

$$EAT = 54.$$

Q10

| Processes | Allocated | Maximum | Needed | Available |
|----------------|-----------|---------|---------|-----------|
| | A B C D | A B C D | A B C D | A B C D |
| P ₀ | 2 0 2 1 | 9 5 5 5 | 7 5 3 4 | 6 3 5 4 |
| P ₁ | 0 1 1 1 | 2 2 3 3 | 2 1 2 2 | |
| P ₂ | 4 1 0 2 | 7 5 4 4 | 3 4 4 2 | |
| P ₃ | 1 0 0 1 | 3 3 3 2 | 2 3 3 1 | |
| P ₄ | 1 1 0 0 | 5 2 2 1 | 4 1 2 1 | |
| P ₅ | 1 0 1 1 | 4 4 4 4 | 3 4 3 3 | |

P₁₂ 1 7 9 7

(P₀) - Need > work

(P₁) - Need < work, Available: [6 4 6 5]

(P₂) - Need < work, Available: [10 5 6 7]

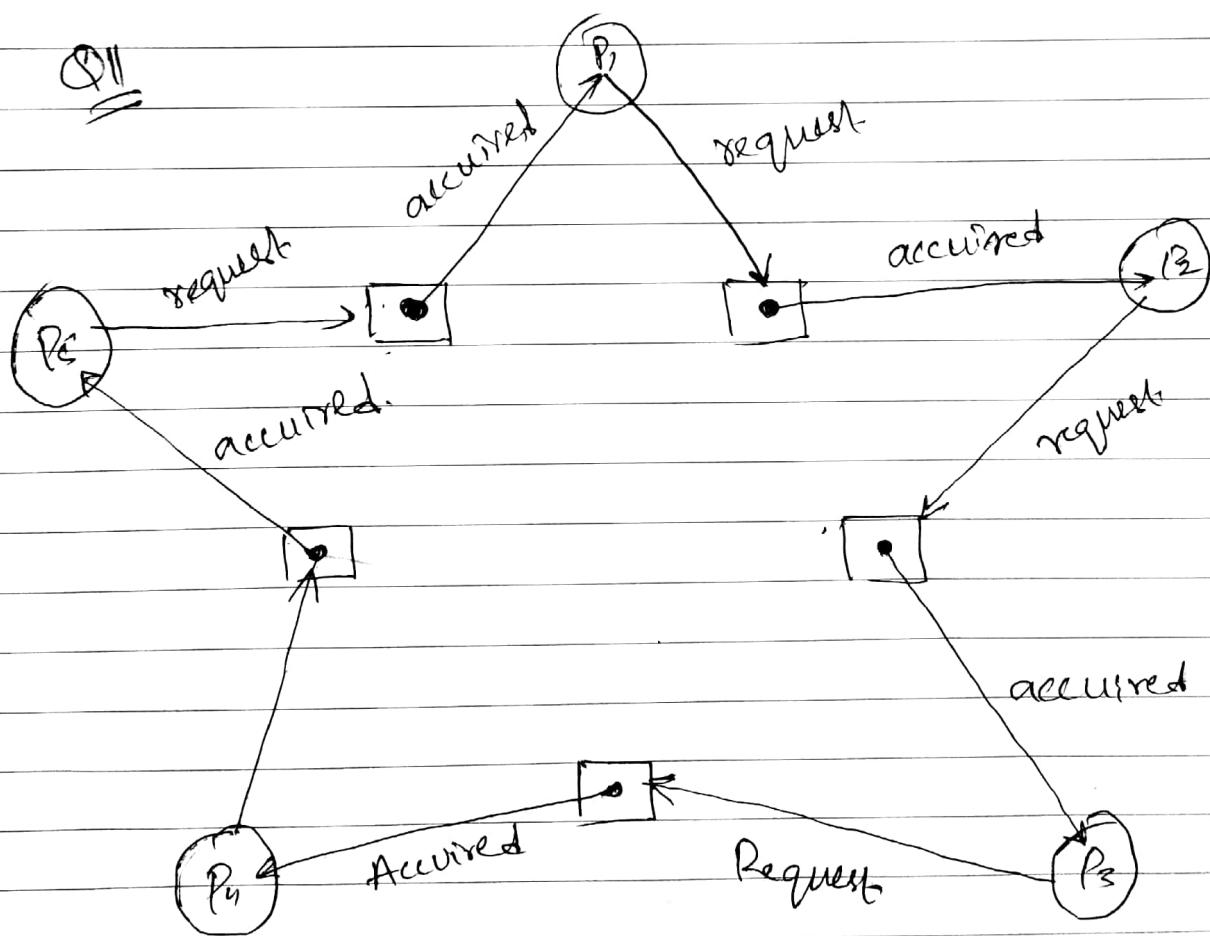
(P₃) - Need < work, Available: [11 5 6 8]

(P₄) - Need < work, Available: [12 6 6 8]

(P₅) - Need < work, Available: [13 6 7 9]

(P₆) - Need < work, Available: [15 6 9 10].

Safe seq.: {1, 2, 3, 4, 5, 0}.



Deadlock.