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A17MB0220

No. \_\_\_\_\_

Date \_\_\_\_\_

Unsigned char R3 = 0xB6;

Signed char R4 = 0x9A;

R3 : 1011 0110 (182)

R4 : 1001 1010

0110 0110  $\downarrow$  2's complement (-102)

R3 = R3 | R4

$\hookrightarrow$  1011 0110

1001 1010

1011 1110 (190)

R4 = R4 << 2

$\hookrightarrow$  1001 1010  $\Rightarrow$  0110 1000 (104)

R3 = R3 - R4

$\hookrightarrow$  1011 1110

0110 1000

0101 0110 (86)

Check Odd or even

if LSB = 1 [odd]

if LSB = 0 [even]

R3 = 0101 0110

0000 0001

0000 0000  $\rightarrow$  even number.

• Check if H Flag is Set. [0010 0000]  
ITHS VNZC

↳ 0101 0110

0010 0000 &

$((R3 \& 0x20) == 0x20)$

0010 0000 Not set (clear)

• Check if S Flag is clear. [0001 0000]

↳ 0101 0110

0001 0000 &

$((R3 \& 0x10) == 0x0)$

0001 0000 Set.

• Check if V Flag is set. [0000 1000]

↳ 0101 0110

0000 1000 &

$((R3 \& 0x8) == 0x8)$

0000 1000 Not set (clear)

• Check if N Flag is clear [0000 0100]

↳ 0101 0110

0000 0100 &

$((R3 \& 0x4) == 0x0)$

0000 0100 Set

• Check if Z Flag is set [0000 0010]

↳ 0101 0110

0000 0010 &

$((R3 \& 0x2) == 0x2)$

0000 0010 Set

• Check if C Flag is Set [0000 0001]

↳ 0101 0110

0000 0001 &

$((R3 \& 0x1) == 0x1)$

0000 0000 Not set (clear)

Set I flag (bit 7 to 1) and clear the T flag (6 to 0)

↳ 0101 0110 I to 1

1000 0000 | (bitwise OR)

1101 0110 T to 0

1011 1111 & (bitwise AND)

1001 0110 (150) ✓

$R3 = ((R3 | 0x80) \& 0xBF);$

\* Proven