



# Amogha M P

**Work permit:** German | **Date of birth:** 26/08/2001 | **Nationality:** Indian | **Gender:** Male |

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## ABOUT ME

Master's student in Information Technology at the University of Stuttgart with hands-on experience in embedded systems, industrial automation, and firmware development. Skilled in Rust, C/C++, Python, and HDL with a proven track record of building secure, scalable, and power-efficient solutions across hardware and software platforms.

## SKILLS

### Programming Languages

C | Embedded C | C++ | HDL(SystemVerilog,Vhdl, Verilog) | bitbake | rust | Python

### Tools and Software

Matlab/Simulink | Simulink | Xilinx Ise | STMCube IDE | JIRA | Git | Visual Studio & Visual Studio Code | Yocto | CCS Studio | Creo | MS Office | KiCAD , Altium CAD tools | target process

### Skills

Linux | SPI | Microcontroller programming | I2C | Firmware Development | PCB-Design | RTOS | CAN | UART | UML | Driver Development | INDUSTRY 4.0 | Digital twin | Embedded Systems | Internet of Things (IoT)

## WORK EXPERIENCE

### BELDEN/HIRSCHMANN AUTOMATION AND CONTROL – STUTTGART, GERMANY

#### MASTER THESIS – 01/10/2023 – CURRENT

- Developing a modular, stateful failure monitoring solution for belden-based industrial routers.
- Designing platform-agnostic concepts for automated fault detection, isolation, and recovery.
- Aligned design with compliance and quality requirements in collaboration with academic and industrial stakeholders.

### BELDEN/HIRSCHMANN AUTOMATION AND CONTROL – STUTTGART, GERMANY

#### RESEARCH AND DEVELOPMENT INTERN – 01/02/2025 – 31/08/2025

- Developed and deployed a Rust proxy application for industrial Linux devices, improving security and bridging applications with system services.
- Implemented Rust-C++ interoperability using the cxx crate.
- Integrated Rust apps into Yocto images (meta-rust).
- Collaborated in Agile teams (Git, Jira, Jenkins), accelerating feature delivery and improving code quality.
- Optimized for low-latency and high-throughput leveraging Rust's concurrency and memory safety.
- Authored documentation and test frameworks ensuring long-term scalability.

### UNIVERSITY OF STUTTGART – STUTTGART, GERMANY

#### RESEARCH ASSISTANT IN UNIVERSITY – 01/01/2024 – 01/01/2025

- Built efficient BLE firmware and drivers in Embedded C for microcontroller data transmission.
- Led complex PCB designs (KiCad) and resolved hardware/software issues, improving system reliability.

### SCHNEIDER ELECTRIC

#### INTERNSHIP – 01/02/2023 – 01/05/2023

- Deployed an AR-based soldering training program, reducing operator training time.
- Built prototypes with Blender, Unity, and Vuforia AR SDK.

### INDIAN INSTITUTE OF TECHNOLOGY

