

## Output

1. (a)

	Value
Register B	32
Register C	32
Register D	32
Register E	32
Register H	32
Register L	32

(b)

	Value
Accumulator	45

(c)

Input { 2500H → 13  
2501H → 32  
Result 2502H → 45

Accumulator → 45

B → 13

LAB ASSIGNMENT8085 Microprocessor

1.

- (a) Write a program to store 8-bit data into one register and then copy that to all registers

```
MVI B, 32H
```

```
MOV C, B
```

```
MOV D, B
```

```
MOV E, B
```

```
MOV H, B
```

```
MOV L, B
```

- (b) Write a program for addition of two 8-bit nos.

```
MVI A, 32H
```

```
MVI B, 13H
```

```
ADD B
```

```
STA 2500H
```

```
RST 5
```

- (c) Write a program to add 8-bit nos. using direct and indirect addressing mode.

Direct

```
LDA 2500H
```

```
MOV B, A
```

```
LDA 2501H
```

```
ADD B
```

```
STA 2502H
```

```
RST 5
```

Teacher's Signature : \_\_\_\_\_

input  $\begin{cases} 2500 \rightarrow 32 \\ 2501 \rightarrow 11 \end{cases}$

$2502 \rightarrow 43$

Accumulator  $\rightarrow 43$

Register H  $\rightarrow 25$

Register L  $\rightarrow 02$

Memory (M)  $\rightarrow 43$

(d)

Direct

$\begin{cases} 2500 \rightarrow 21 \\ 2502 \rightarrow 11 \end{cases}$

$2504 \rightarrow 33$

Register C  $\rightarrow 01$

Register E  $\rightarrow 21$

Register H  $\rightarrow 12$

Indirect



Indirect

LXI H, 2500H

MOV A, M

INX H

ADD M

INX H

MOV M, A

RST 5

- (d) Write a program to add 16-bit nos. using direct and indirect addressing mode.

Direct

LHLD 2500H

XCHG

LHLD 2502H

DAD D

SHLD 2504H

RST 5

Indirect

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2500 → 35

2501 → 21

Result

2501 → 6A

Accumulator → 6A

Register H → 25

Register L → 02

Expt. No. ....

(e) WAP to add 8-bit nos. using carry

MVI C, 00H

LXI H, 2500H

MOV A, M

ADD M

INX H

JNC Next

INR C

Next: MOV M, A

INX H

MOV M, C

RST 5

(f) WAP to find 1's complement and 2's complement of a number.

LDA 2500H

← 1's complement

CMA

STA 2501H

RST 5

2's complement

LDA 2500H

CMA

ADI, 01H

STA 2501H

RST 5

Teacher's Signature : \_\_\_\_\_