

EFFICIENT PROCESSING OF ATLAS EVENTS ANALYSIS IN PLATFORMS WITH ACCELERATOR DEVICES

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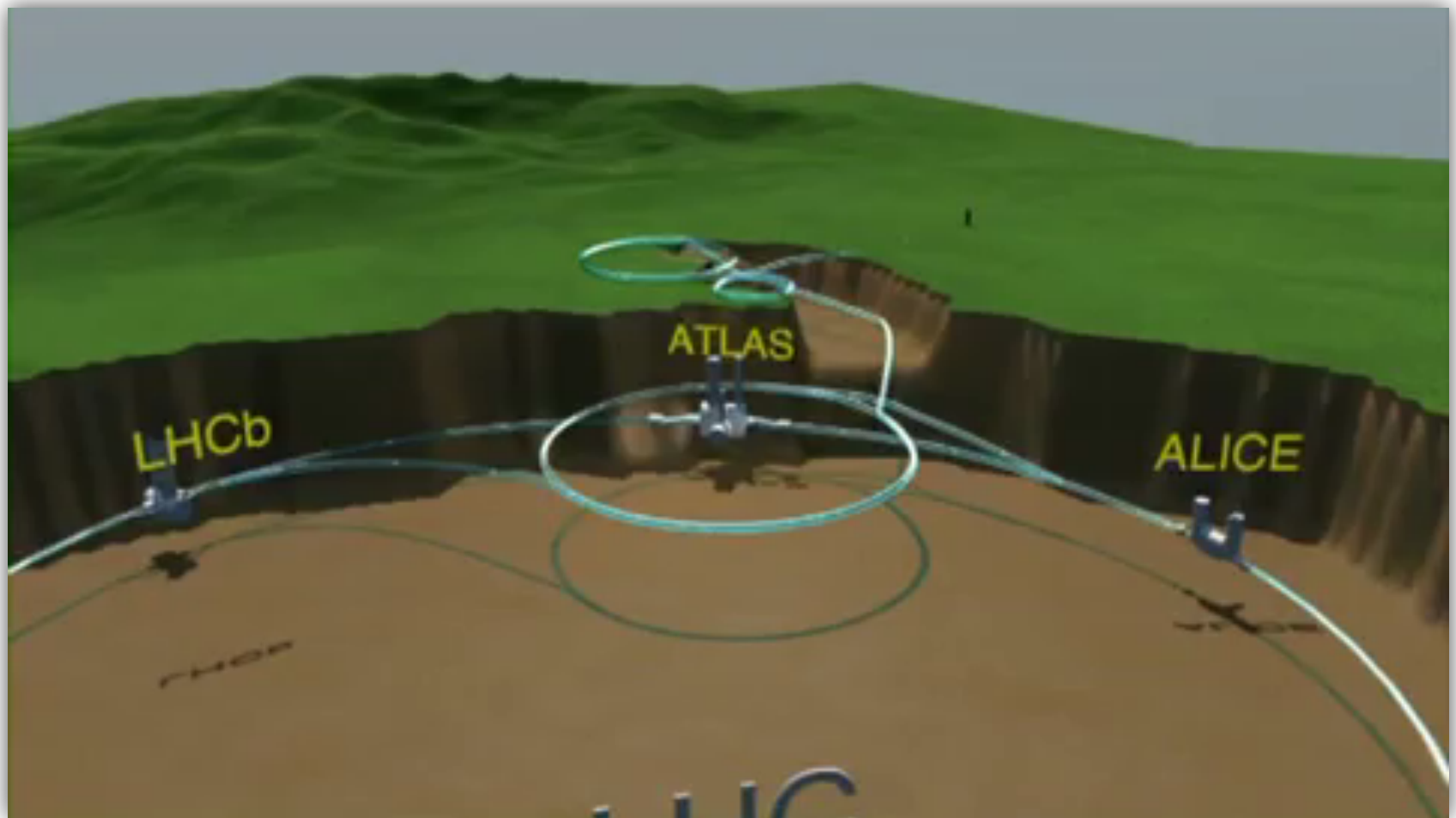
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 - ▣ Heterogeneous Platforms
 - ▣ Software
- The $t\bar{t}H_{d\ell\ell}$ Analysis Application
- Future Work

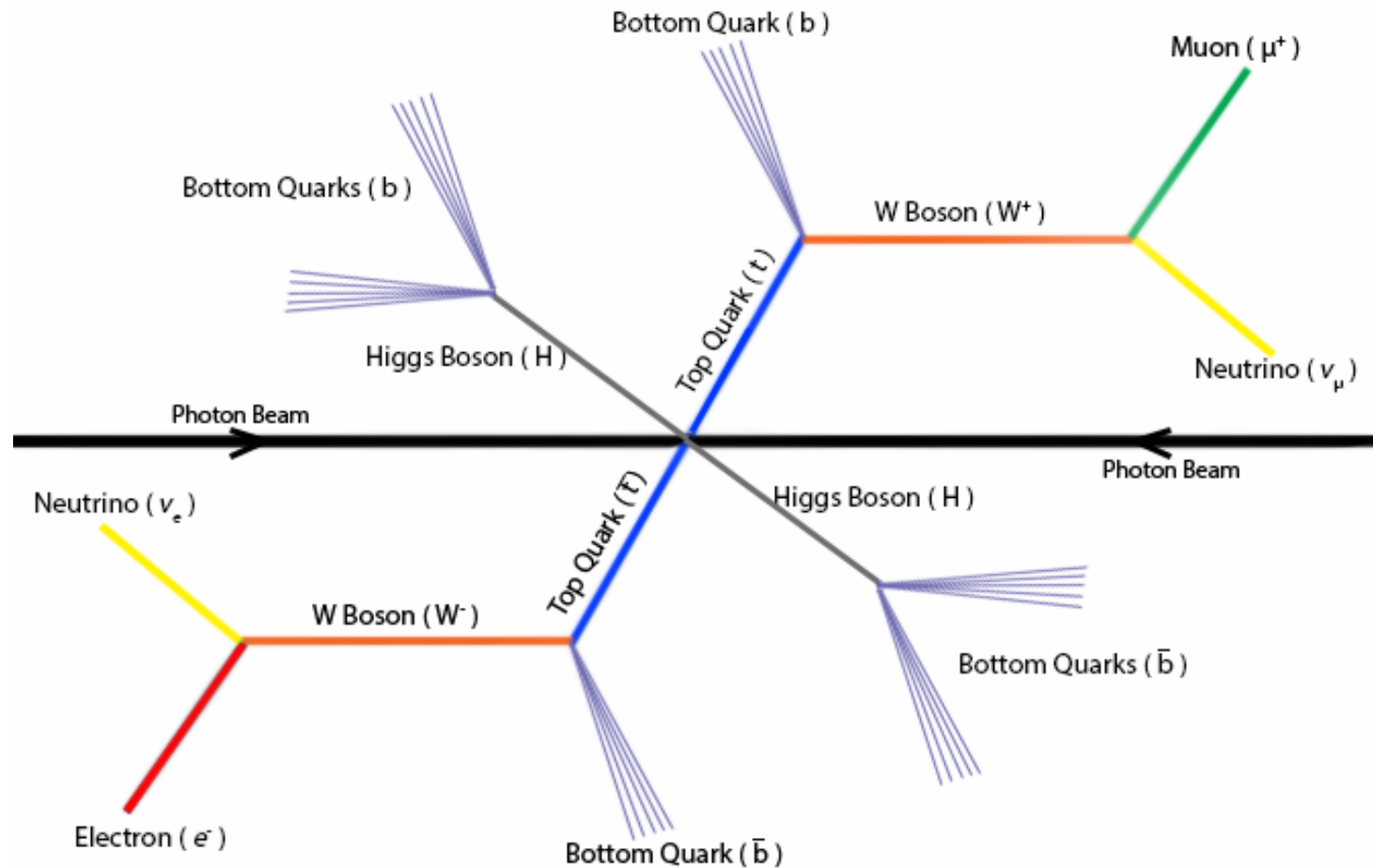
Motivation

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Motivation

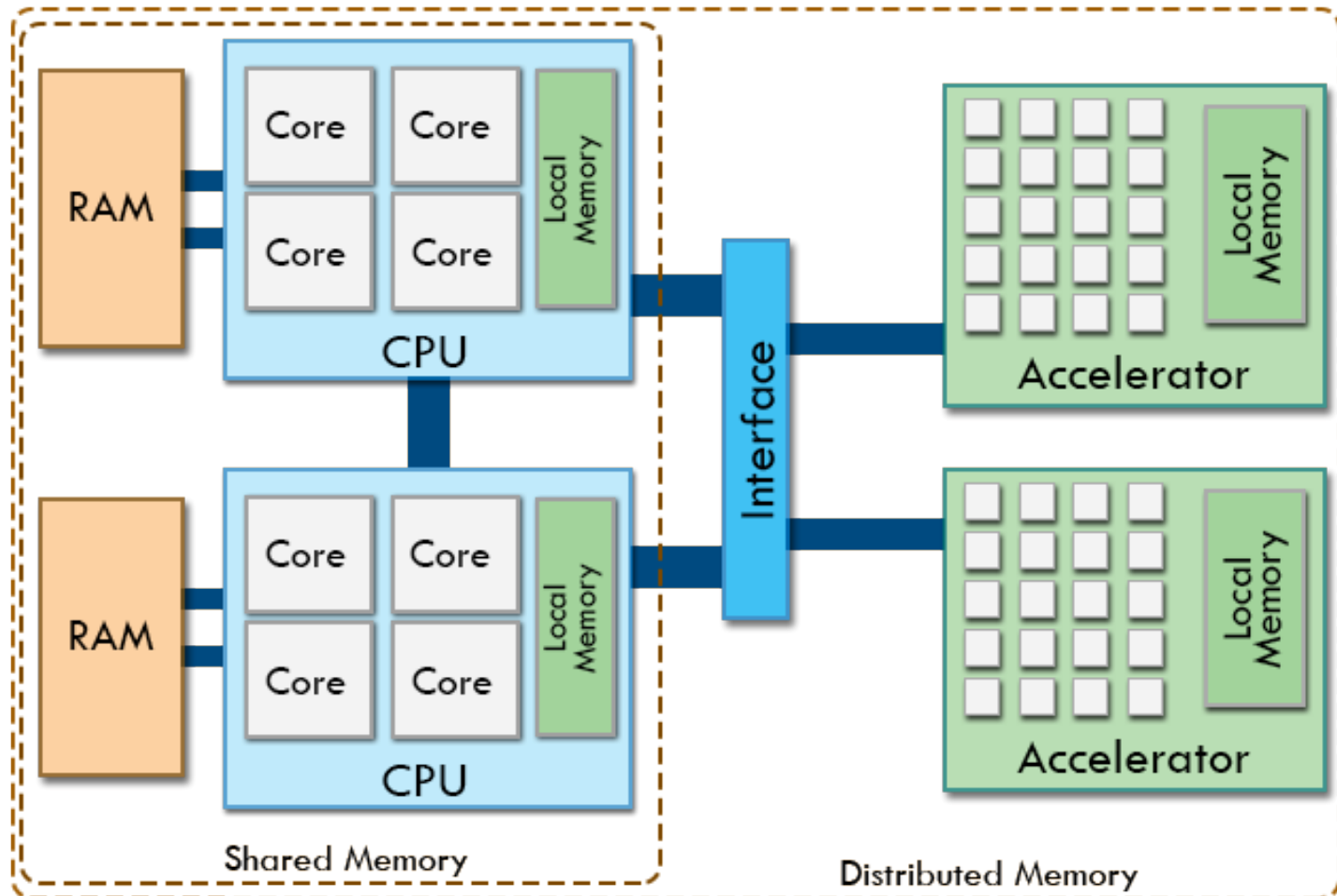
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$t\bar{t}$ system + Higgs decay

Heterogeneous Platforms

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Heterogeneous Platforms

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- Several challenges
 - ▣ Different architectures
 - ▣ Different programming paradigms
 - ▣ Load balancing
 - ▣ Debugging

Accelerator Devices

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- Usually based on the SIMD model
- **Graphics Processing Units** architectures
 - ▣ NVidia Fermi
 - ▣ NVidia Kepler
- Intel **Many Integrated Core** architecture
 - ▣ Intel Xeon Phi

Development Frameworks

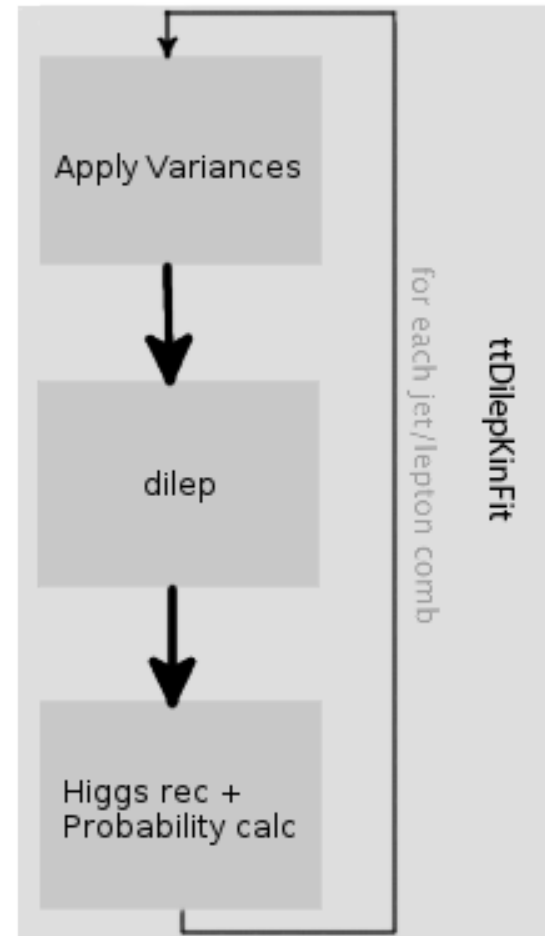
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- Available frameworks
 - ▣ OpenMP (Homogeneous Platforms)
 - ▣ OpenACC (Heterogeneous Platforms)
 - ▣ GAMA (Heterogeneous Platforms only with GPUs)

The ttH_dilep Analysis Application

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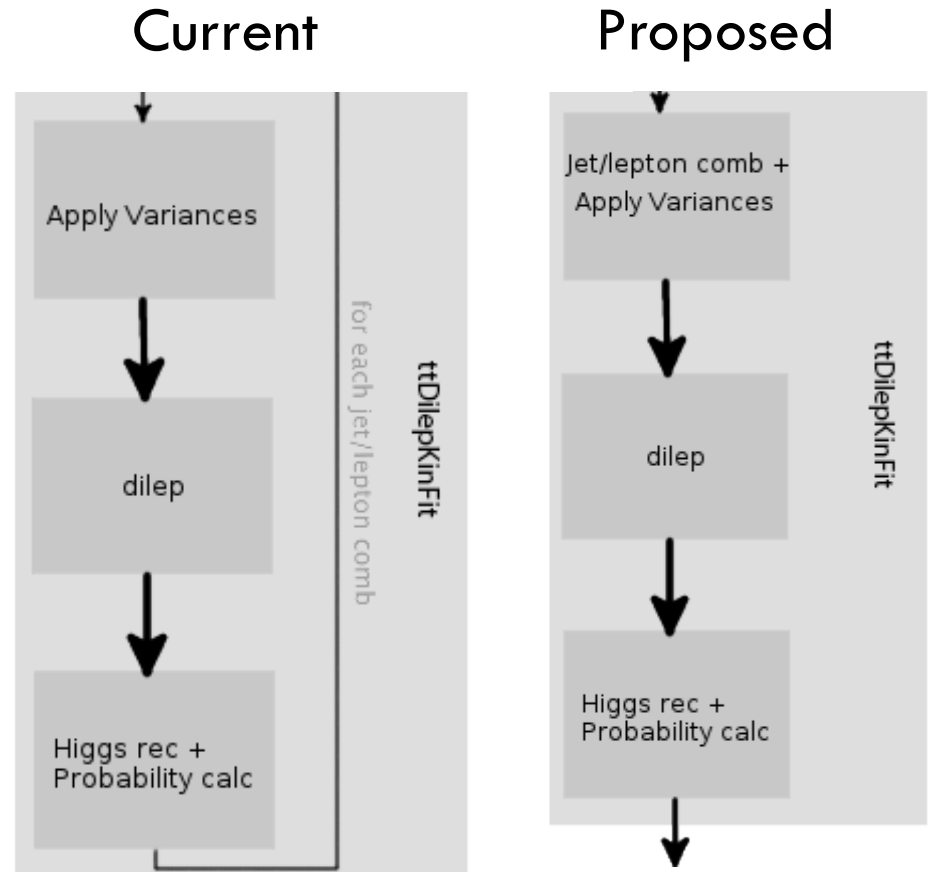
- Series of filters applied to the events;
- Reconstruct the ttbar system and Higgs bosons;
 - ▣ Critical region: ttDilepKinFit



Future Work

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▣ Refactor ttDilepKinFit



Future Work

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- Parallelization on heterogeneous platforms
 - ▣ Comparing accelerator devices
- OpenACC vs GAMA

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