# EFFICIENT PROCESSING OF ATLAS EVENTS ANALYSIS IN PLATFORMS WITH ACCELERATOR DEVICES

André Pereira

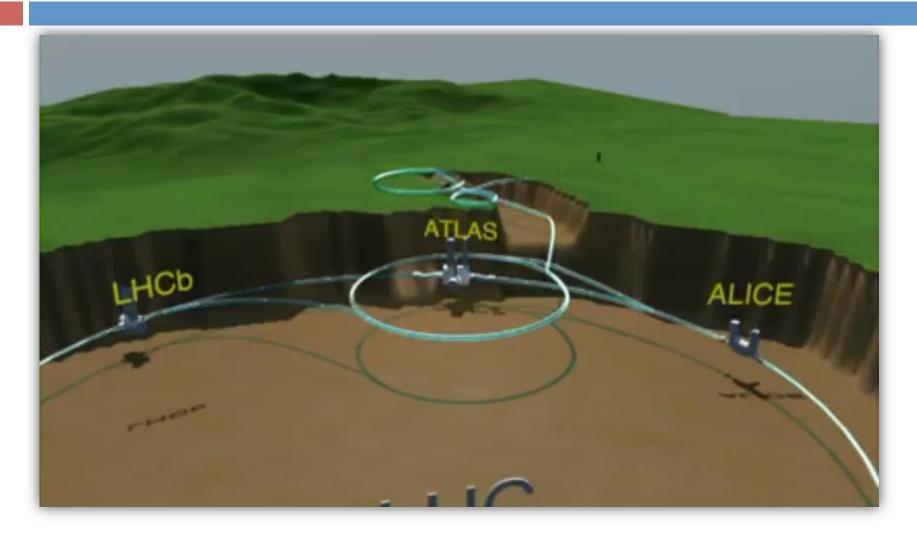
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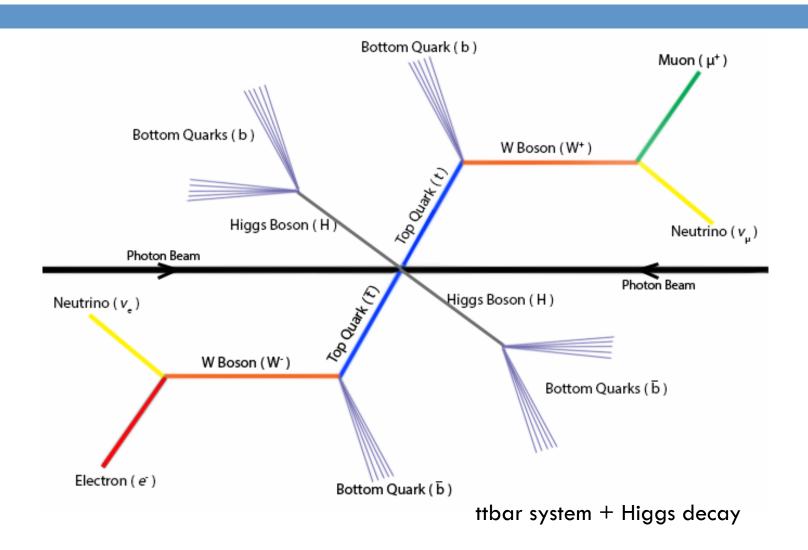
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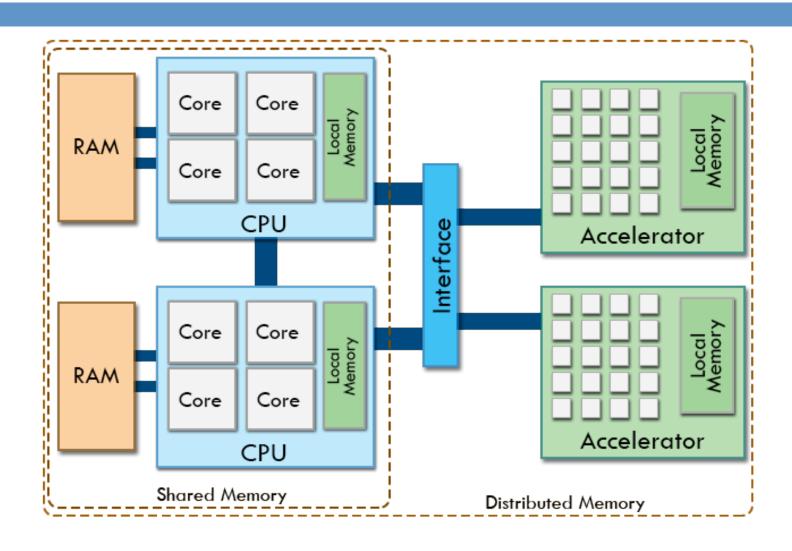
## Motivation



### Motivation



## Heterogeneous Platforms



## Heterogeneous Platforms

- Several challenges
  - Different architectures
  - Different programming paradigms
  - Load balancing
  - Debugging

#### **Accelerator Devices**

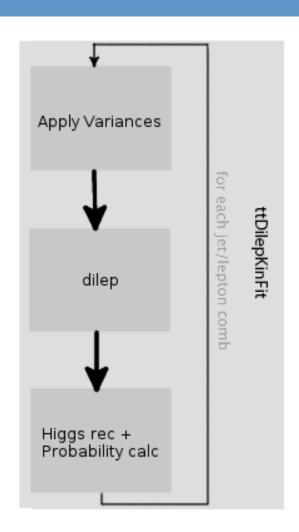
- Usually based on the SIMD model
- Graphics Processing Units architectures
  - NVidia Fermi
  - NVidia Kepler
- □ Intel Many Integrated Core architecture
  - Intel Xeon Phi

#### Software

- Available frameworks
  - OpenMP (Homogeneous Platforms)
  - OpenACC (Heterogeneous Platforms)
  - □ GAMA (Heterogeneous Platforms only with GPUs)

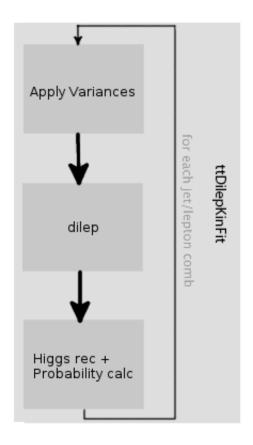
## The ttH\_dilep Analysis Application

- Series of filters applied to the events;
- Reconstruct the ttbar system and Higgs bosons;
  - Critical region: ttDilepKinFit

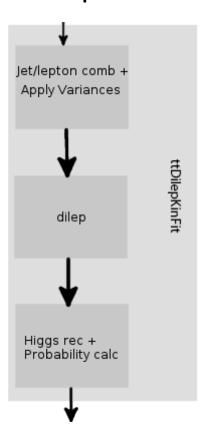


## Future Work

#### Current



#### Proposed



### **Future Work**

- Parallelization on heterogeneous platforms
  - Comparing accelerator devices
- OpenACC vs GAMA

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