Advantages of mixed unitary operators for quantum information processing

Anthony Polloreno*
Rigetti Computing, Berkeley, CA

Kevin C. Young Sandia National Laboratories, Livermore, CA (Dated: December 8, 2018)

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I. INTRODUCTION

The past decade has seen a dramatic increase in the performance and scale of quantum information processors (QIPs). Gate fidelities are now routinely in the 99% to 99.99% range, and dozens of individually-addressable qubits are becoming available on integrated devices. While these advances represent important steps forward on the path towards a computationally useful QIP, the quantum advantage milestone has yet to be definitively reached. The limiting factor, of course, is errors in the quantum gate operations.

The impact of an error in a quantum gate depends strongly on both the magnitude and nature of the errors. Systematic, or *coherent*, errors can arise from poorly calibrated controls or imperfect gate compilations. These errors induce repeatable, undesired unitary errors on the state of a QIP. Errors of this type are correlated in time and add up coherently, so they are computationally expensive to model and it is difficult to place analytic bounds on circuit performance. Contrast this against random, or stochastic, errors, which result from highfrequency noise in the controls or the environment. These errors can be modeled by defining a rate of various discrete errors in the system, such as a bit flip or phase flip. These errors are significantly easier to simulate on a classical computer, and their impact on quantum circuits is much easier to estimate.

Despite the relative ease of modeling stochastic errors, coherent errors are often much more likely to appear in QIPs. Drifting control parameters or environmental variables can easily have long correlation times that result in errors which is strongly coherent over the length of a quantum circuit. While these errors can often be reconstructed using various tomographic techniques, their impact is difficult to predict. The diamond distance can be used to estimate the failure probability of a quantum circuit, but the diamond distance can grow quadratically with repeated application of gate with coherent errors. For long circuits, this can add up extremely quickly. Recent work by Hastings and Campbell, however, has shown that coherent noise can be strongly suppressed by probabilistic sampling over various implementations of the target quantum gates. This averaging results in quantum

* Email: anthony@rigetti.com

processes with diamond distances that grow only linearly quadratic in the over/under rotation angle of the component gates. [Clear this up.]

In this article we discuss various applications of mixed unitary controls, and show that the advantages of this approach can be made robust to drift in the target gates. In this article, we discuss an optimal control approach to the mixed unitary control design problem. We apply our methods in simulation where we construct single-and two-qubit mixed unitary controls which are robust to drift and uncertainty in the control parameters. We further present an experimental implementation of single-qubit mixed unitary controls on a superconducting qubit testbed at Rigetti Quantum Computing. Using randomized benchmarking, we are able to show a marked improvement in error rates, as well as a reduced variance in circuit outcome probabilities, indicating a reduction in the coherence of the error.

II. REPRESENTING QUANTUM GATES

The standard model for quantum gate operations is as linear maps on the state of some qubits. The state is usually represented as a density operator. It is often convenient, however, to represent the states instead as a generalized Bloch vector,

$$\vec{\rho} = \text{Tr}\left(\rho\vec{\sigma}\right) \tag{1}$$

Operations on this Bloch vector can then be represented as process matrices, \mathcal{G} , and their action on a quantum state given by standard matrix multiplication

$$\rho \to \mathcal{G}\rho = \mathcal{E}\tilde{\mathcal{G}}\rho \tag{2}$$

Here \mathcal{G}_i is the target operation, $\tilde{\mathcal{G}}$ is the actual gate as implemented, and \mathcal{E} is the effective error channel. If the error channel is unitary, then the error is coherent. An advantage of this particular representation of quantum gates is that Pauli stochastic channels are diagonal, with each entry corresponding to the probability that the associated Pauli error occurs in a given application of the gate.

FIG. 1. An example of a balanced control solution. Using optimal control, two implementations of a Z_{π} gate are designed to have equal and opposite sensitivity to errors (if one implementation over-rotates by angle θ , then the other *under*-rotates by θ). Each time the gate is used, one of these implementations is chosen at random. The resulting quantum channel is equivalent to a perfect implementation of the gate followed by dephasing of $\mathcal{O}\left(\theta^2\right)$.

III. EXAMPLE MIXED UNITARY PROCESSES

There are often many possibly ways of implementing a target quantum gate. Hastings and Campbell consider gates compiled using the Solovey-Kitaev algorithm, for which there are many choices of approximate quantum gate implementations. By implementing some number of these implementations at random, we are able to construct a quantum channel that has reduced coherent error. As a simple example, we have:

** This example is really useful and should be expanded. We should discuss the various optimization problems, a mixed unitary strategy, the fidelity of the resulting channel.

IV. ROBUSTLY MIXED UNITARY PROCESSES

V. EXPERIMENTAL RESULTS

VI. FIGURES

VII. ACKNOWLEDGEMENTS

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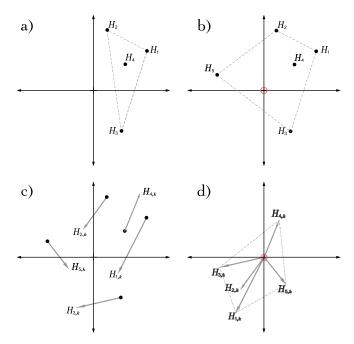


FIG. 2. A target unitary gate can be implemented a number of ways, each with a different effective Hamiltonian error. These error Hamiltonians lie in a vector space. a) Four effective Hamiltonians. The origin is not contained in their convex hull, so there are no balanced control solutions. b) The origin is contained in the covex hull after adding an additional control solution. Because there are more than n+1 implementations, there exist an infinite number of balanced control solutions. c) The error Hamiltonians shown with their derivative with respect to a control parameter. As this parameter drifts, a $0^{\rm th}$ -order balanced control solution may drift, leading to a first-order error. d) The derivatives also lie in a vector space. If the origin lies in their convex hull, then it may be possible to construct a $1^{\rm st}$ -order robust balanced control solution.

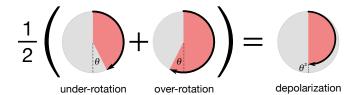


FIG. 3. An example of a balanced control solution. Using optimal control, two implementations of a Z_{π} gate are designed to have equal and opposite sensitivity to errors (if one implementation over-rotates by angle θ , then the other *under*-rotates by θ). Each time the gate is used, one of these implementations is chosen at random. The resulting quantum channel is equivalent to a perfect implementation of the gate followed by dephasing of $\mathcal{O}\left(\theta^2\right)$.

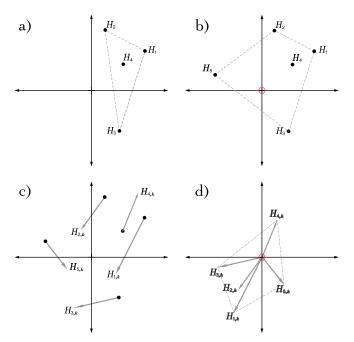


FIG. 4. A target unitary gate can be implemented a number of ways, each with a different effective Hamiltonian error. These error Hamiltonians lie in a vector space. a) Four effective Hamiltonians. The origin is not contained in their convex hull, so there are no balanced control solutions. b) The origin is contained in the covex hull after adding an additional control solution. Because there are more than n+1 implementations, there exist an infinite number of balanced control solutions. c) The error Hamiltonians shown with their derivative with respect to a control parameter. As this parameter drifts, a $0^{\rm th}$ -order balanced control solution may drift, leading to a first-order error. d) The derivatives also lie in a vector space. If the origin lies in their convex hull, then it may be possible to construct a $1^{\rm st}$ -order robust balanced control solution.

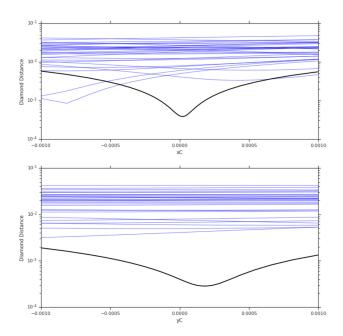


FIG. 5. Placeholder image until we figure out what to plot. This currently shows that for one of the control the diamond norm decreased by over an order of magnitude. The labels need to be bigger.

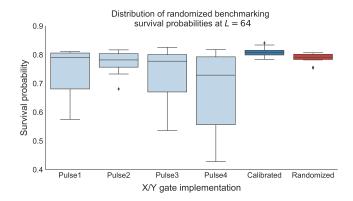


FIG. 6. Randomized benchmarking experiments ran using different pulse definitions. The four plots on the left are from the incorrectly calibrated pulse, while the top right is the calibrated pulse, and the bottom right is the BCS.