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Former Home of SpinCAD Designer

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No, it's really supposed to sound like that

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No, it's really supposed to sound like that (#p2337)

by **Digital Larry** » Sun May 03, 2015 1:52 pm

This is another one where perhaps incarceration is too kind.

Oh but I see a bug in the output block! Weird. I did make some changes there that still need ironing out.

```
; Patch: C:\Users\User\Documents\SpinCAD Designer\double-blender-delay.spcd
; SpinCAD Designer version: 931
; Pot 0: sample/hold LFO speed
; Pot 1: Feedback level
; Pot 2: Effect level
;
;
; --------------------------
;------ Feedback Output
;------ Input
;------ Pot 1
;------ Noise_AMZ
SKP RUN ,2
SOF 0.0000000000,0.6660000000
WRAX REG1,0.0000000000
LDAX REG1
AND $00000001
WRAX REG2,0.0000000000
RDAX REG1,0.5000000000
AND $007FFFFF
WRAX REG1,0.0000000000
LDAX REG2
SKP ZRO,4
```

```
CLR
LDAX REG1
XOR $00D80000
WRAX REG1,0.0000000000
LDAX REG1
ABSA
WRAX REG3,0.0000000000
;------ Pot 0
;------ SampleHold
SKP RUN ,1
WLDR 0, 20, 4096
LDAX POT0
MULX POT0
MULX POT0
SOF 0.5000000000,0.1000000000
WRAX RMP0_RATE,0.0000000000
CHO RDAL,2
SOF 1.0000000000,-0.2500000000
SKP ZRC,4
CLR
RDAX REG5,1.0000000000
WRAX REG4,0.0000000000
SKP ZRO,2
LDAX REG3
WRAX REG5,0.0000000000
;------ Smoother
RDAX REG4,1.0000000000
RDFX REG6,0.0004006175
WRAX REG6,0.0000000000
;------ Mixer 2:1
RDAX ADCL,1.0000000000
WRAX REG7,0.0000000000
RDAX REG0,1.0000000000
MULX POT1
RDAX REG7,1.0000000000
WRAX REG7,0.0000000000
;------ Scale/Offset
RDAX REG6,1.0000000000
SOF -0.5200000000,0.8200000000
WRAX REG8,0.0000000000
;------ Scale/Offset
RDAX REG6,1.0000000000
SOF 0.4300000000,0.5700000000
WRAX REG9,0.0000000000
;------ Coarse Delay
RDAX REG7,1.0000000000
WRA 0,0.0
```

```
CLR
OR $007FFF00
MULX REG4
SOF 0.7349853516,0.0000305176
WRAX ADDR_PTR,0.0000000000
RMPA 1.0
WRAX REG10,0.0000000000
;------ 2 pole low pass
RDAX REG8,1.0000000000
SOF 0.3500000000,-0.3500000000
EXP 1.0,0.0
WRAX REG11,0.0000000000
RDAX REG8,1.0000000000
SOF 1.0000000000,-0.9990000000
EXP 1.0,0.0
WRAX REG12,0.0000000000
RDAX REG14,1.0000000000
MULX REG11
RDAX REG13,1.0000000000
WRAX REG13,-1.0000000000
WRAX REG16,0.0000000000
RDAX REG14,-0.3333333333
MULX REG9
RDAX REG16,1.0000000000
RDAX REG10,0.5000000000
WRAX REG15,1.0000000000
MULX REG11
RDAX REG14,1.0000000000
WRAX REG14,0.0000000000
;------ Invert
RDAX REG4,1.0000000000
SOF -0.9990000000,0.9990000000
WRAX REG17,0.0000000000
;------ Coarse Delay
RDAX REG13,1.0000000000
WRA 24085,0.0
CLR
OR $007FFF00
MULX REG17
SOF 0.2500000000,0.7350158691
WRAX ADDR_PTR,0.0000000000
RMPA 1.0
WRAX REG18,0.0000000000
;------ Pot 2
;------ FB In 1
RDAX REG18,0.7500000000
WRAX REG0,0.0000000000
```

```
;------ Scale/Offset
RDAX POT2,1.0000000000
SOF 0.6400000000,0.3600000000
WRAX REG19,0.0000000000
;------ Mixer 3-1
RDAX ADCL,0.3333333333
WRAX REG20,0.0000000000
RDAX REG10,0.3333333333
MULX REG19
RDAX REG20,1.0000000000
WRAX REG20,0.0000000000
RDAX REG18,0.3333333333
MULX POT2
RDAX REG20,1.0000000000
WRAX REG20,0.0000000000
;------ Output
RDAX REG20,0.7943282347
RDAX REG20,0.7943282347
; RDAX SIN0_RATE,1.0000000000 - this looks like a bug
WRAX DACL,1.0000000000
WRAX DACR,0.0000000000
```

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