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A couple of ideas for new blocks (#p2339)

by **drolo** » Tue May 05, 2015 1:00 am

I have been having great fun using SpinCAD, the blocks available (currently version 922) are great and very usable. Here are a few ideas for new blocks that could potentially be interesting:

PWM LFO: with a control for rate and one for duty cycle.

Square wave LFO: (already discussed in another thread)

Reverse delay: (even a crude one)

Higher rate in the existing LFO's: to allow going into ring mod kind of sounds

Pot mapping: for example with 4 outputs, allowing to use the pot as a switch with multiple positions

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Re: A couple of ideas for new blocks (#p2340)

by **Digital Larry** » Tue May 05, 2015 5:29 am

Hi drolo,

Thanks for the ideas.

There is an "Oscillator" block under the Controls menu that goes a bit higher than the hardware LFOs. And there is an actual Ring Modulator block under Modulation that Ring Modulates all over the place.

Reverse delay, have to think about that. No idea how to do it.

Pot mapping, requires use of the SKP instruction and is hard to generalize as a block, but I can try. I do have one in development that lets you select one of four pitch shifts from a pot for example.

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Re: A couple of ideas for new blocks (#p2341)

by **drolo** » Wed May 06, 2015 3:41 am

Thanks for the tip about the oscillators, had not got to trying them out yet 😊

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Re: A couple of ideas for new blocks (#p2631)

by **MacroMachines** » Tue Jan 19, 2016 9:38 am

reverse delay from the forum

```

mem delay 32767

skp run, loop

sof 0, -0.25    ;clear acc and set -0.25
wrax rmp0_rate, 0    ;ramp rate = -.25, clear to 0
wrax rmp0_range, ;ramp range= 0

loop:

sof 0,0        ;clear acc

rdax adcl, 1.0    ;read left adc
wra delay#, 0.0    ;write to delay ram and clear
cho rdal, rmp0     ;get ramp lfo
wrax addr_ptr, 0    ;write data at delay ram address
rmpa 1.0          ;read data at delay ram address 1.0

wrax dacr, 1.0
wrax dacl, 0.0
    
```

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