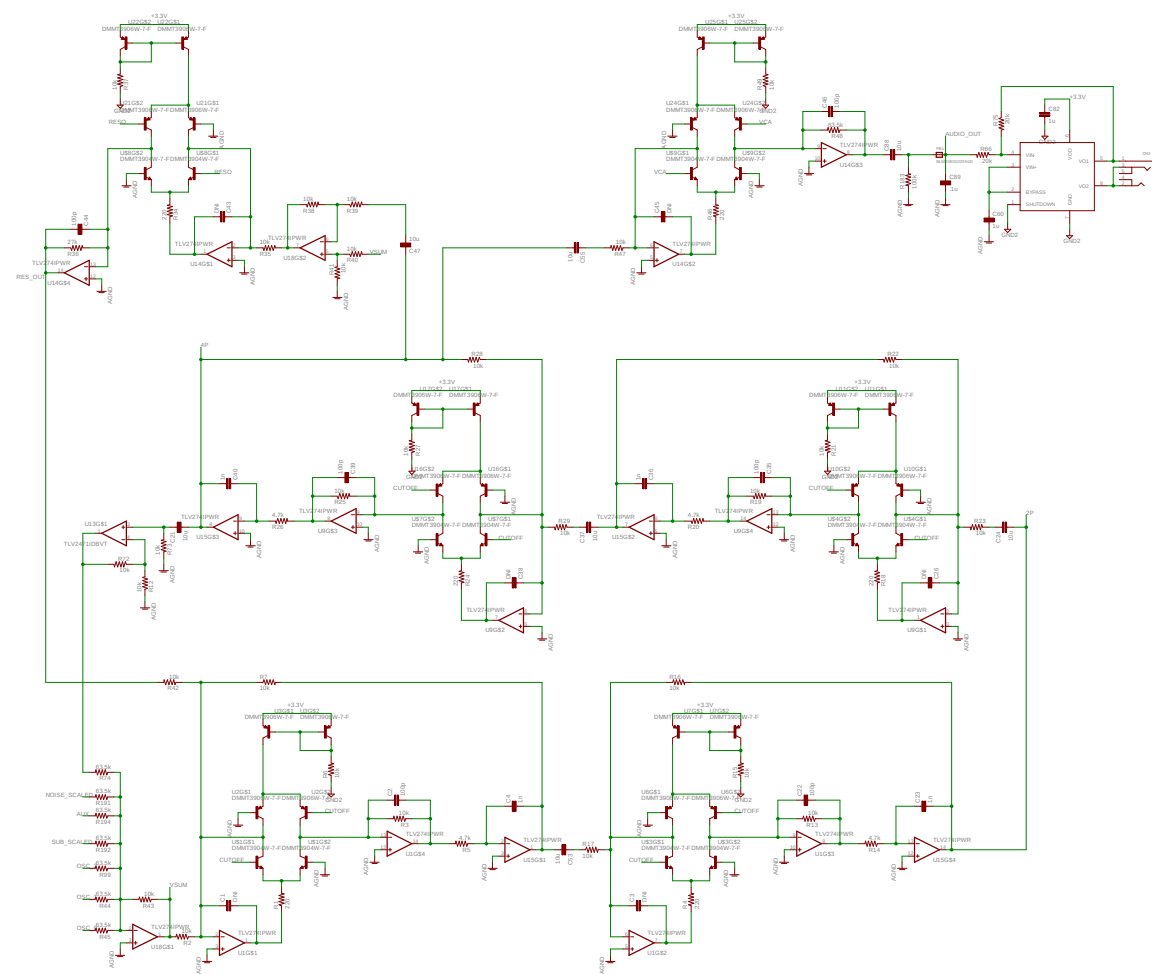
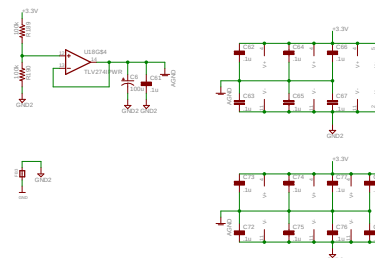


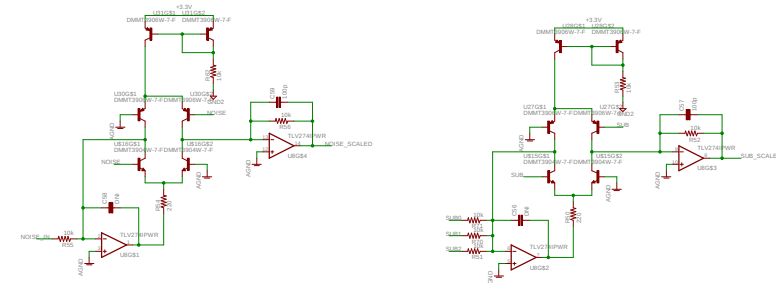
Filter



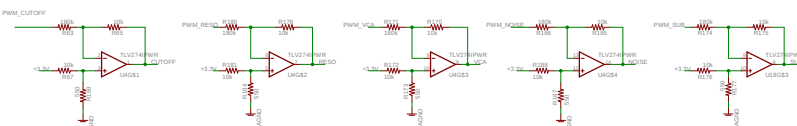
Power

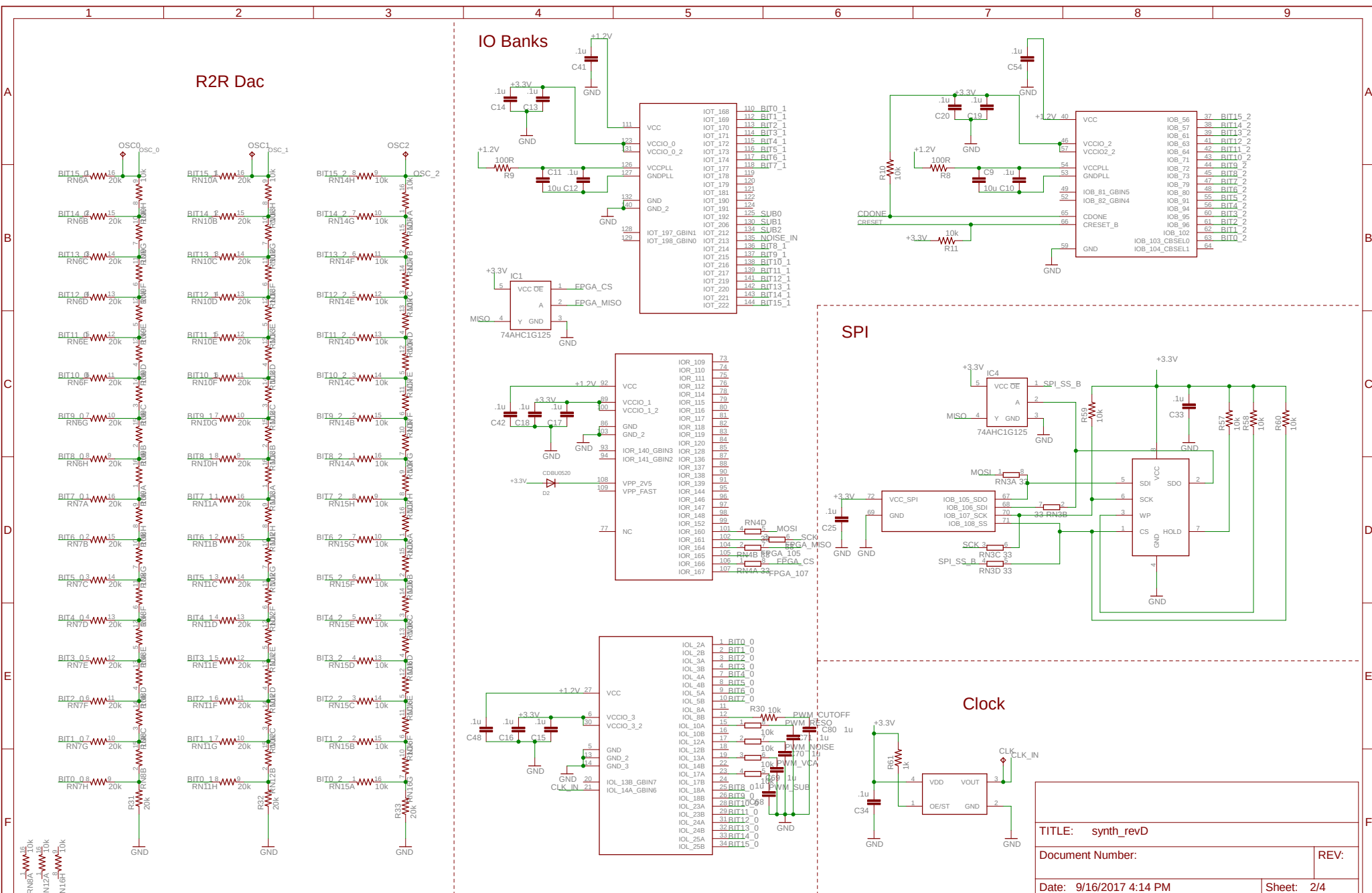


sub/noise vol



CV scaling





The schematic diagram illustrates the SD & MMC interface circuit. It features an Arduino Uno (labeled 'X4 microed') connected to an SD card module (labeled 'SD & MMC') and an IC3 (74AHC1G125) buffer. The SD card module has pins for VCC (+3.3V), GND, MISO, MOSI, SCK, CS, DAT1, DAT2, DATA_OUT, DATA_IN, SCL, and SDA. The IC3 has pins for OE/VCC, A, GND, Y, and MISO. The circuit is powered by a +3.3V supply and a 10k resistor (R64) is connected to the MISO pin.

Pin connection diagram for the ATSAM21G18-QFN package. The diagram shows the internal connections between the package pins and the microcontroller's internal pins. It includes power supply connections (VDDIO, VDDA, VDDIN, VDDCORE), reset connections, and various peripheral pins like SPI, I2C, and SWD. The package is shown in a top-down view with pins numbered 1 through 46.

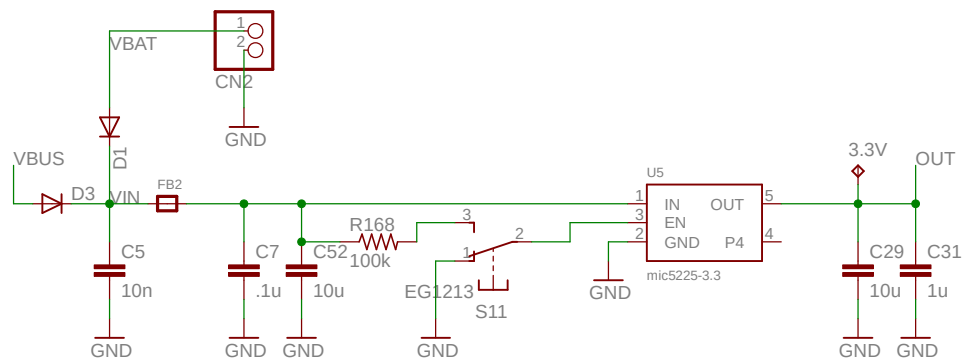
Internal Connections:

- RESET:** Connected to internal pin 40.
- +3.3V:** Connected to internal pin 17/2.
- VDDIO:** Connected to internal pin 6.
- VDDA:** Connected to internal pin 44.
- VDDIN:** Connected to internal pin 43.
- VDDCORE:** Connected to internal pin 44.
- GND:** Connected to internal pins 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100.

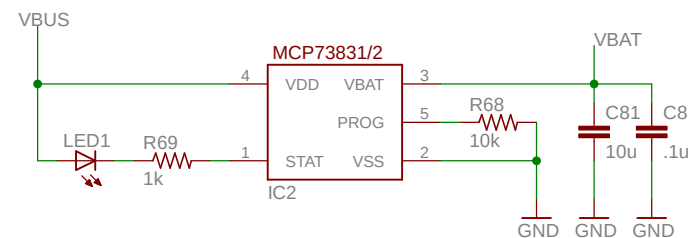
Peripheral Connections:

- SPI:** CS (pin 3), SCK (pin 4), MISO (pin 21), MOSI (pin 22).
- I2C:** SDA (pin 31), SCL (pin 32).
- SWD:** SWCLK (pin 45), SWDIO (pin 46).
- Other:** TXLED (pin 48), RXLED (pin 49), TXD (pin 37), RXD (pin 38), TXEN (pin 39), RXEN (pin 40), TXB0 (pin 41), RXB0 (pin 42), TXB1 (pin 43), RXB1 (pin 44), TXB2 (pin 45), RXB2 (pin 46), TXB3 (pin 47), RXB3 (pin 48), TXB4 (pin 49), RXB4 (pin 50), TXB5 (pin 51), RXB5 (pin 52), TXB6 (pin 53), RXB6 (pin 54), TXB7 (pin 55), RXB7 (pin 56), TXB8 (pin 57), RXB8 (pin 58), TXB9 (pin 59), RXB9 (pin 60), TXB10 (pin 61), RXB10 (pin 62), TXB11 (pin 63), RXB11 (pin 64), TXB12 (pin 65), RXB12 (pin 66), TXB13 (pin 67), RXB13 (pin 68), TXB14 (pin 69), RXB14 (pin 70), TXB15 (pin 71), RXB15 (pin 72), TXB16 (pin 73), RXB16 (pin 74), TXB17 (pin 75), RXB17 (pin 76), TXB18 (pin 77), RXB18 (pin 78), TXB19 (pin 79), RXB19 (pin 80), TXB20 (pin 81), RXB20 (pin 82), TXB21 (pin 83), RXB21 (pin 84), TXB22 (pin 85), RXB22 (pin 86), TXB23 (pin 87), RXB23 (pin 88), TXB24 (pin 89), RXB24 (pin 90), TXB25 (pin 91), RXB25 (pin 92), TXB26 (pin 93), RXB26 (pin 94), TXB27 (pin 95), RXB27 (pin 96), TXB28 (pin 97), RXB28 (pin 98), TXB29 (pin 99), RXB29 (pin 100).

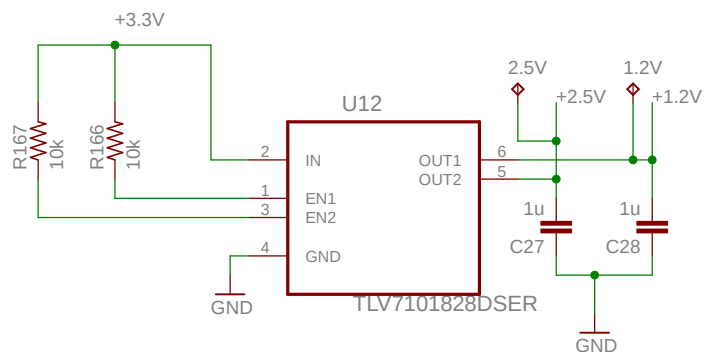
3V3 Reg



Lipo charging



FPGA power



synth_revD

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