	Programme: BE Sem: I Sec: J to R Term:8/8/16 to 17/12/16 Course: Basic Electronics
	SI# Question
Define and ex	Define and explain the following (i) CMRR (ii) virtual ground (iii)Input Offset
Ib) A voltage ,R <sub>E</sub> =1.21	Voltage  Noltage divider bias circuit has $V_{CC}=18$ volts, $R_1=33$ k ohms, $R_2=12$ kohms, $R_C=1$ kohms, $R_E=1.2$ Kohms, $h_{FE}=50$ , given $V_{BE}=0.7$ volts, using find $V_{E}$ , $V_{CE}$ and $V_{C}$ . Draw the DC local line and locate the Oppoint
1c) P	Perform the following (show the steps) i) $(10.35)_8 = (?)_{10} = (?)_2 = (?)_{16}$
	With neat circuit diagram explain Base Bias method and derive the expression for operating point.
2Ь)	Design an inverting scaling Adder circuit using an op-amp to give the output voltage $V_0=-(6V_1+4V_2+15V_3)$ . Sketch the designed circuit.
2c)	Perform the following:(i)(13)10-(86)10 using 1's complement method
3	(1000111) <sub>2</sub> - (10011) <sub>2</sub> using 2 s complement memod
ja)	expression for voltage gain.
3Ъ	With neat circuit diagram explain Voltage Divider bias method using approximate analysis?
36	Perform the following i) $(16CF.2B)_{16}=(?)_8$ (ii) $(45)_{10}$ - $(8)_{10}$ using 2's complement method <b>OR</b> Determine the base value of x if $(211)_x=(152)_8$