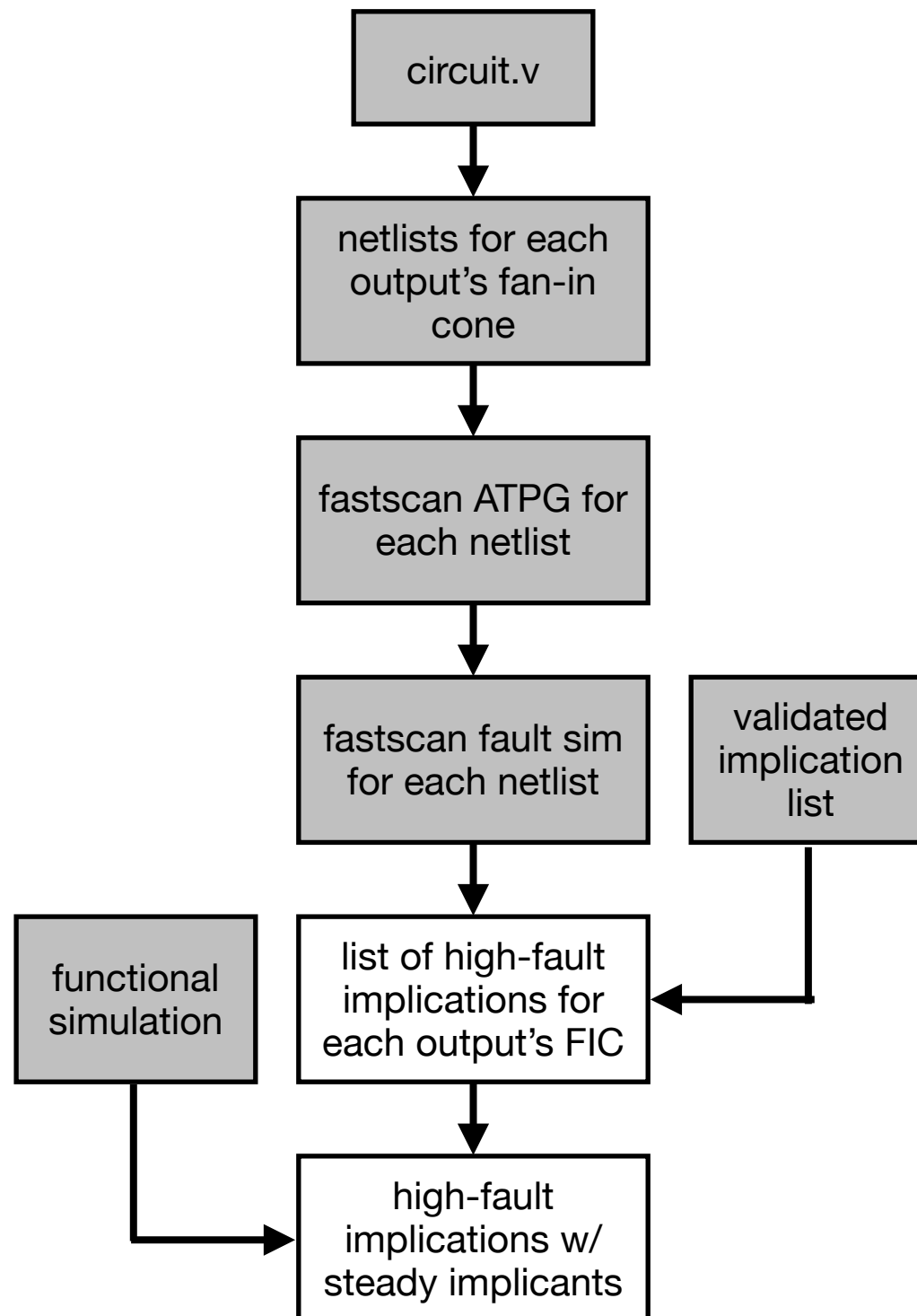
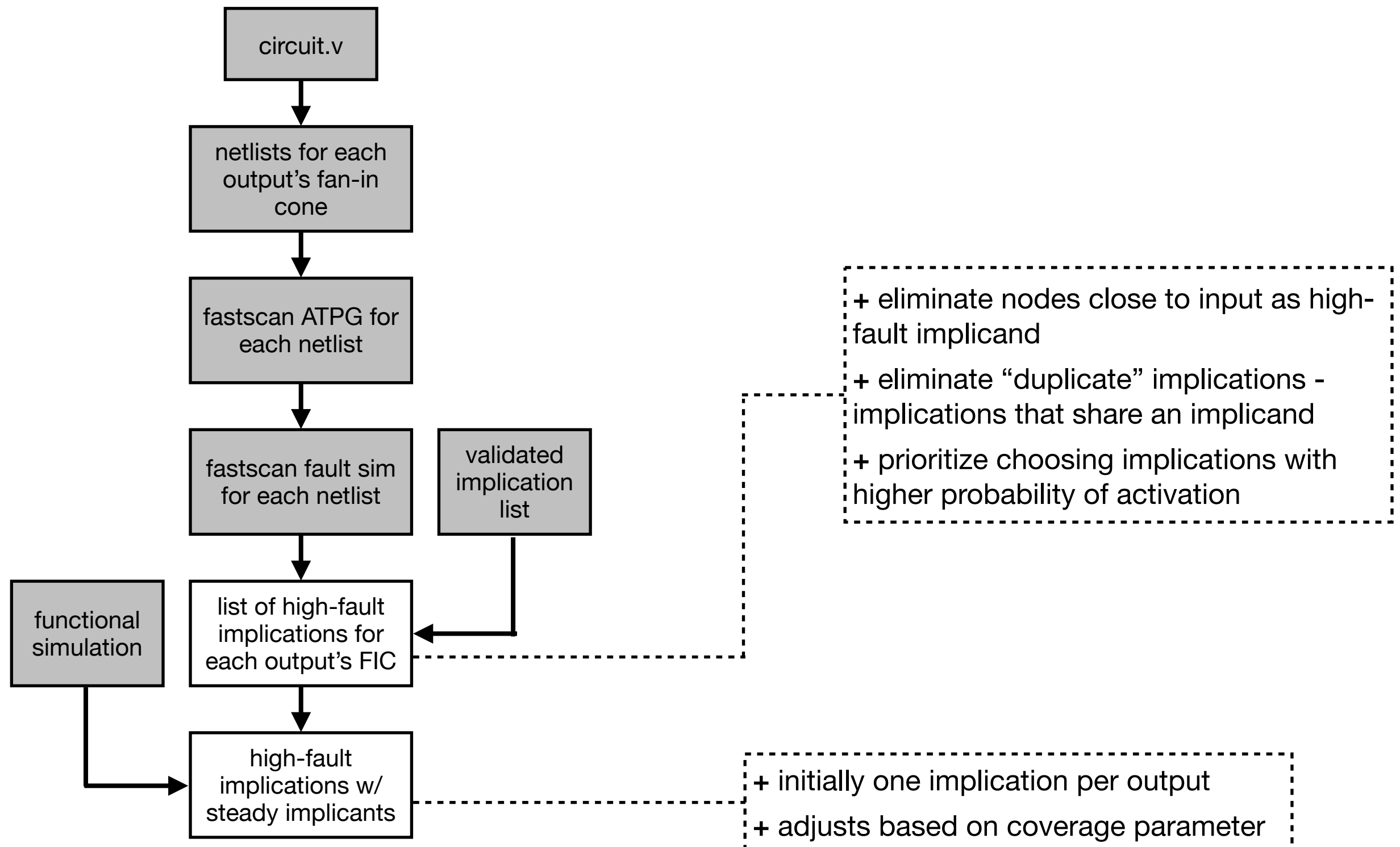


# Fault Simulation Workflow



# Fault Simulation Workflow



# RD53

## scenario 1 - fault reinforcement

**v5\_0:**

v4 : 0 :: w1088 : 0

**v5\_1:**

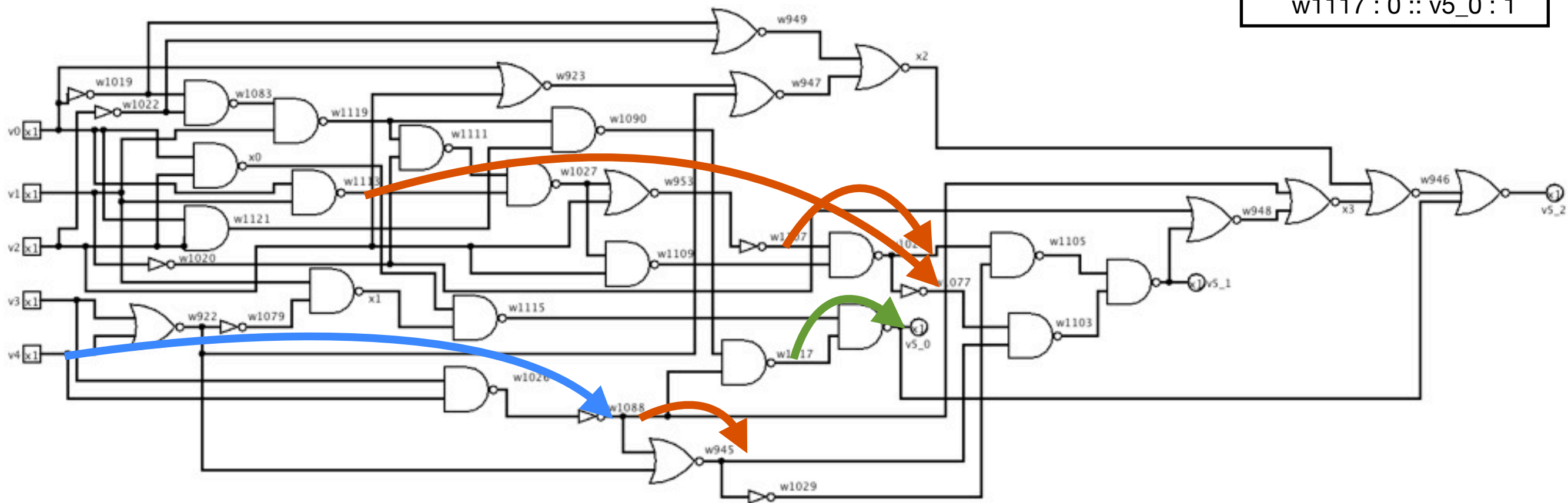
w1113 : 0 :: w1027 : 1

w1107 : 0 :: w1028 : 1

w1088 : 1 :: w945 : 0

**v5\_2:**

w1117 : 0 :: v5\_0 : 1



# RD53

## scenario 2 - fault reinforcement + additional coverage

**v5\_0:**

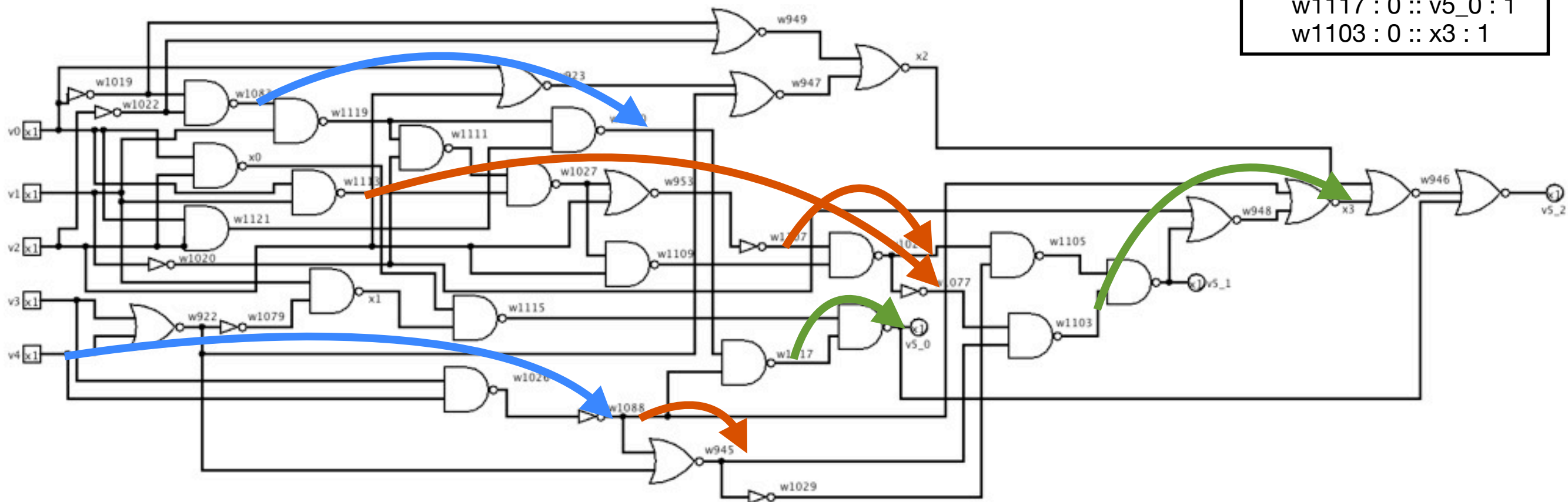
v4 : 0 :: w1088 : 0  
w1083 : 0 :: w1090 : 0

**v5\_1:**

w1113 : 0 :: w1027 : 1  
w1107 : 0 :: w1028 : 1  
w1088 : 1 :: w945 : 0

**v5\_2:**

w1117 : 0 :: v5\_0 : 1  
w1103 : 0 :: x3 : 1



# RD73

## scenario 1 - fault reinforcement

**v7\_0:**

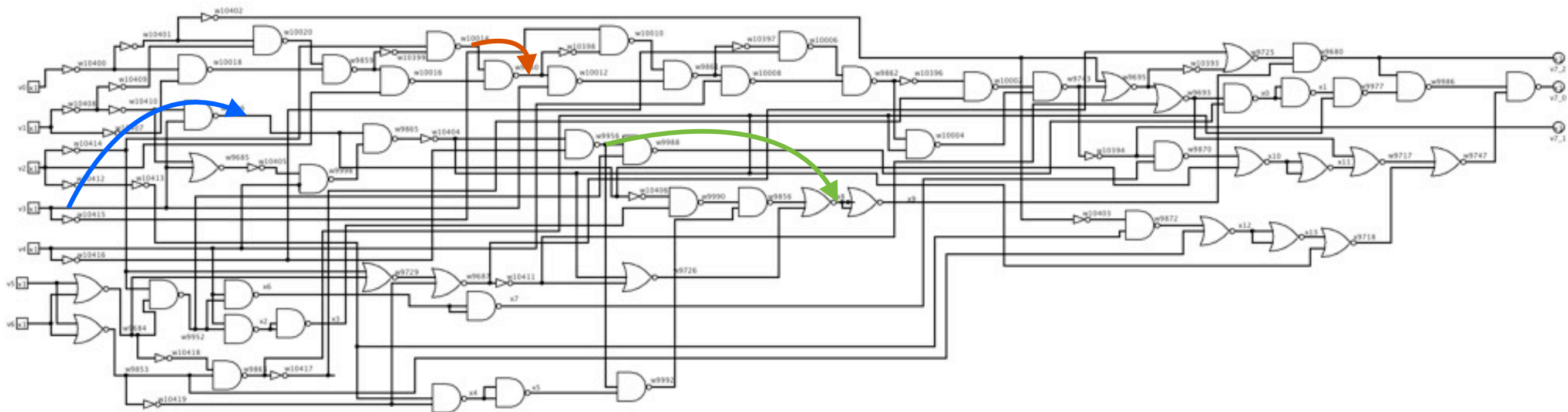
v3 : 0 :: w9846 : 1

**v7\_1:**

w10014 : 0 :: w9860 : 1

**v7\_2:**

w9956 : 0 :: x8 : 1



# RD73

## scenario 2 - fault reinforcement + additional coverage

**v7\_0:**

v5 : 0 :: w10419 : 0  
x8 : 0 :: w9680 : 0  
v3 : 0 :: w9846 : 1

**v7\_1:**

w10014 : 0 :: w9860 : 1  
w10012 : 0 :: w9861 : 1  
w9853 : 0 :: w9863 : 1

**v7\_2:**

w9956 : 0 :: x8 : 1

