ANDREW ROJAS

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Education

Johns Hopkins University | M.S.E. Computer Science — May 2021

- Completing 5-year combined BS/MSE program

Johns Hopkins University | Dual B.S. Computer Science & Math – 3.79/4.0 — May 2020

- Minor in Entrepreneurship & Management
- Clark Scholar chosen for academic achievement, leadership, and service
- ACM@JHU president from January 2019 to May 2020 (planned weekly events, hosted ICPC)
- Computer Science Department Service Award – For service to department and community

Experience

Lyft | High Value Drivers SWE Intern — June 2020 – July 2020

- Created Python-Flask microservice to manage driver rewards for over 1.4 million drivers
- Designed React front-end to be used internally to manage driver rewards service

NVIDIA | Enterprise Experience SWE Intern — May 2019 – August 2019

- Built Salesforce synchronization service to simplify DGX firmware upgrade experience
- Automated engineering workflow by deploying DGX system analysis tool in Python-flask

Johns Hopkins University | Course Assistant — January 2019 – Current

- Algorithms (2x), Operating Systems (1x)

PatientPop | EMR Integrations SWE Intern — May 2017 – May 2019

- Accelerated release testing by up to 70% by creating data seeding API in PHP-Laravel and OOP interface in Java-Selenium
- Added new synchronization job in PHP-Laravel to support new appointment type for 100s of existing and potential Sikka-integrated customers

Skills

- Programming Languages: C, C++, Python, PHP, Java, SQL, JavaScript
- Technologies: flask, NoSQL (DynamoDB), scikit-learn, NumPy, Docker, git, Unix/Linux, REST

Projects

- Semi-supervised Manifold Learning: Implemented machine learning model based on manifolds as part of data science project. Tested on MNIST dataset and Olivetti faces
- CHIP-8 Emulator: Writing 8-bit interpreter emulator in C using SDL library.
- XChange: Machine-learning app (Python, Flask, Vue.JS) to help restaurants optimize revenue and combat food waste. **3rd place JHU Business Plan Competition**
- SIMPLE Compiler & Interpreter: Implemented lexical, syntactic, and semantic analysis, interpreter, simple and optimized assembly code compilation, and stack-based procedures, targeting ARMv6