

```
8  entity EN_Adder is
9    generic (N: natural := 16);
10   port (
11     A, B : in std_logic_vector (N-1 downto 0);
12     S : out std_logic_vector (N-1 downto 0);
13     Cin : in std_logic;
14     Cout, Ovfl : out std_logic
15   );
16 end EN_Adder;
```