

```

1  -- Configuration declarations for TB_Adder.
2  -- Provide independent configs for:
3  --   - Functional verification per architecture (LACA, BKA, etc.)
4  --   - Timing verification using Quartus .vho (architecture 'structure') + SDF
5
6
7  library ieee;
8  use ieee.std_logic_1164.all;
9
10 -- functional testing
11 configuration CFG_FUNC_RCA of TB_Adder is
12     for behavior
13         for DUT : TestUnit use entity work.EN_Adder(RCA); end for;
14     end for;
15 end configuration;
16
17 configuration CFG_FUNC_CSA of TB_Adder is
18     for behavior
19         for DUT : TestUnit use entity work.EN_Adder(CSA); end for;
20     end for;
21 end configuration;
22
23 configuration CFG_FUNC_LACTA of TB_Adder is
24     for behavior
25         for DUT : TestUnit use entity work.EN_Adder(LACTA); end for;
26     end for;
27 end configuration;
28
29 configuration CFG_FUNC_BKA of TB_Adder is
30     for behavior
31         for DUT : TestUnit use entity work.EN_Adder(BKA); end for;
32     end for;
33 end configuration;
34
35 configuration CFG_FUNC_CBA of TB_Adder is
36     for behavior
37         for DUT : TestUnit use entity work.EN_Adder(CBA); end for;
38     end for;
39 end configuration;
40
41
42
43 -- Timing verification
44 -- Timing: bind to Quartus-generated .vho architecture, usually named 'structure'
45 -- Make sure EN_Adder.vho is compiled *before* EN_Adder.vhd in ModelSim.
46 configuration CFG_TIMING of TB_Adder is
47     for behavior
48         for DUT : TestUnit use entity work.EN_Adder(structure); end for;
49     end for;
50 end configuration;
51
52

```