Swami Vivekanand Subharti University

Enrollment Number

B-Tech

Computer Science Engineering

2022 YEAR 6 SEM EXAMINATION

Compiler Design

Subject Code - BCSE-601

[Time : 1 Hours]   **[Max. Marks : 9]**

Note/Instructions:

i) Attempt all questions.

ii) Marks are indicated against each section.

iii) Assume missing data if any.

Ques.1 Answer the following. (Any 1)   **(1*3=3)**

1. What is Syntax Directed Translation?

2. What is Loop Optimization Techniques?

Ques.2 Answer the following. (Any 1)   **(1*6=6)**

1. What do you mean by Error Handler and What are the types of Errors?

2. Converts Hardware Description Language into Logic Design.

3. What are the different phases of compiler?